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Decentralized Voltage-Sharing Control Strategy for Fully Modular Input-Series–Output-Series System With Improved Voltage Regulation

Wu Chen, *Member, IEEE*, and Guangjiang Wang

Abstract—Input-series–output-series (ISOS) system connection of dc–dc converters is suitable for high-input-voltage and high-output-voltage applications. Input voltage sharing and output voltage sharing of the constituent modules among the ISOS system must be ensured. This paper proposes a novel decentralized voltage-sharing control strategy, which has the advantages of full modularity and high reliability, and each module is controlled independently without any control communication with other modules. In order to ensure proper sharing of input and output voltages, the input voltage sensing signal is added to the reference voltage for each module, resulting in the positive output voltage gradient regulation characteristic of the ISOS system; moreover, a system output voltage shifting loop is introduced to improve the system voltage regulation. The proposed control strategy can effectively improve the modularity and reliability of the ISOS system. An ISOS system prototype consisting of three two-transistor forward converter modules is tested in the laboratory, and the experimental results verify the effectiveness of the control strategy.

Index Terms—Decentralized controller, input-series–output-series (ISOS), stability, voltage sharing, voltage shifting loop.

I. INTRODUCTION

IN high-voltage applications, the voltage stress of active switches in power converters is always a concern to power supply designers. Series connection of power switches is an advantageous solution; however, uneven voltage sharing among the series-connected devices, particularly during switching transient, cannot be avoided, and extra voltage balancing auxiliary circuits are required to overcome the problem [1]. The multilevel converter is a preferred choice to meet the high-voltage requirement with low-voltage low-power devices. In

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addition, series dc–dc power conversion system in which multiple standardized converter modules are connected in series at the input and/or output sides is also an alternative [2]–[5]. Due to the series connection of multiple modules, the ratings of the power devices in each single module are comparatively lower; moreover, reliability and maintainability of the power conversion system can be improved due to the redundancy provided by multiple modules.

For series-connection systems, input-series–output-parallel (ISOP) system, which consists of multiple modules connected in series at the input sides and in parallel at the output sides, is very well suited to high-input-voltage and large-output-current applications [6], whereas input-series–output-series (ISOS) system, consisting of multiple modules connected in series both at the input and output sides, is suitable for the applications where both input and output voltages are relatively high. For instance, the ISOS systems are expected to be employed in the power collection and transmission system to transmit the renewable energy to high-voltage dc grid [7].

It is critical to ensure equilibrium among the constituent modules for the proper operation of series-connection systems, i.e., input voltage sharing and output current sharing for the ISOP system and both input/output voltages sharing for the ISOS system. The equilibrium strategies for the ISOP system have been extensively studied [8]–[12], whereas only a few work have been done for the ISOS system. In [13], a three-loop control strategy composed of output voltage loop, inner current loop, and input voltage feedforward loop is proposed to achieve both input/output voltages sharing for the ISOS system. Although it is pointed out that the ISOS system with common duty ratio control scheme is unstable when there is even a very small mismatch in the turn ratios of the transformers of the constituent modules [8], for some special circuit topologies, for instance, an ISOS system consisting of two full-bridge converters with the common duty ratio control scheme is stable due to the duty ratio loss characteristics of the full-bridge converter [14]. Merwe and Mouton [15] also identified that a weak natural rebalancing ability exists in the ISOS system with interleaved switching for different cells, and voltage dependent losses, such as the switching loss, enhance the rebalancing ability; however, the voltage-sharing performance is considerably affected by the load current. Reference [16] proposed an interesting idea called duty cycle exchanging control strategy, and it showed satisfactory results; however, the control strategy is merely effective to two-module ISOS system and cannot be used to the ISOS system consisting of three or more modules.

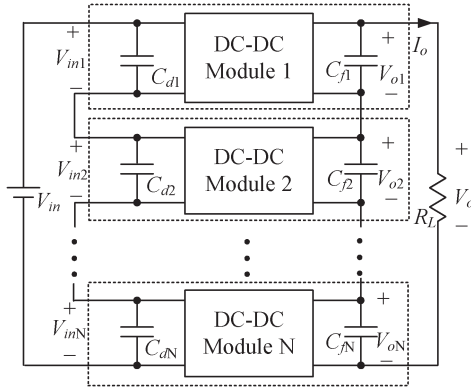


Fig. 1. Circuit diagram of an ISOS system.

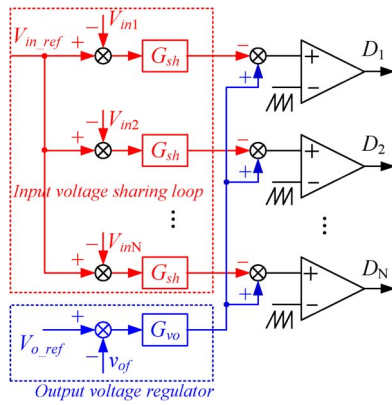


Fig. 2. Control strategy for the ISOS system in [4] and [13].

For the ISOS systems with the aforementioned control strategies, all the constituent modules share the central controllers [13], [14], [16] or connected in master–slave mode [15], and the ISOS systems cannot accommodate the failures of the central controllers or the master module, resulting in lower redundancy and reliability of the system. Moreover, modularity and expansibility, which are the main benefits of the multimodule system, are also limited due to the central controllers.

In this paper, a decentralized voltage-sharing control strategy is proposed, with which fully modular architecture can be realized for the ISOS system. With the proposed control strategy, all the constituent modules are designed to be identical with the same power stages and control stages; in addition, the central controller is eliminated, and there is no any control interconnection among the modules, leading to superior system reliability and modularity.

II. GENESIS OF THE DECENTRALIZED VOLTAGE-SHARING CONTROL STRATEGY

Fig. 1 illustrates the circuit diagram of an ISOS system consisting of N isolated dc–dc converter modules. For the ISOS system, a general control strategy was proposed to ensure both input/output voltages sharing in [4] and [13], as shown in Fig. 2. The control strategy consists of a system output voltage regulator (OVR) and N input voltage-sharing regulators (IVSRs). V_{o_ref} is the reference output voltage, v_{of} is the sampled system

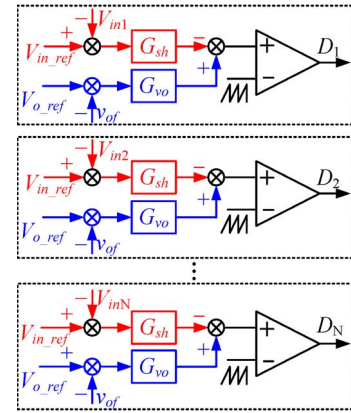


Fig. 3. OVR is dispatched to each module.

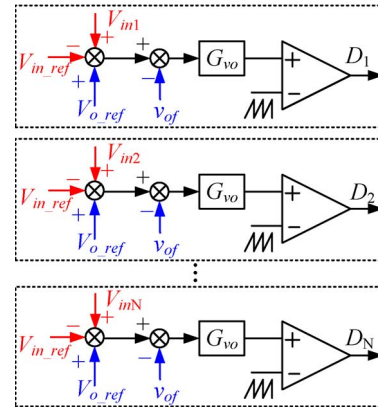


Fig. 4. Combination of the OVR and the IVSR.

output voltage, G_{vo} is the compensation gain of OVR, G_{sh} is the gain of IVSR, and V_{in_ref} is the reference voltage of each module input voltage, i.e.,

$$V_{in_ref} = \frac{V_{in1} + V_{in2} + \dots + V_{inN}}{N} = \frac{V_{in}}{N}. \quad (1)$$

It can be seen that the drive signal of each module is composed of two parts: the output of OVR, which is common for all modules, and the output of each IVSR. In order to realize decentralized control, the OVR needs to be dispatched to each module, as shown in Fig. 3. It has been proved that the input voltage-sharing loop and the system output voltage loop are decoupled, and the OVR and the IVSR can be designed independently [4]. We arbitrarily design the OVR and the IVSR to have the same gain, and the control strategy can be redrawn as Fig. 4. In Fig. 4, there are four control signals for each module controller, i.e., V_{o_ref} , V_{inj} ($j = 1, 2, \dots, N$), v_{of} , and V_{in_ref} . V_{o_ref} is a constant given value. V_{inj} ($j = 1, 2, \dots, N$) is the module's own input voltage. v_{of} and V_{in_ref} are two external input control signals. From the point of view of modularity, for the constituent module of the ISOS system, the less the external input control signals, the better the modularity and reliability. v_{of} is necessary to obtain the desired system output voltage. V_{in_ref} is variable when the system input voltage varies, and if we set V_{in_ref} as a constant value, such as V_{in_min}/N , then the external input control signal V_{in_ref} is eliminated, and for each module, there is only one external input control signal

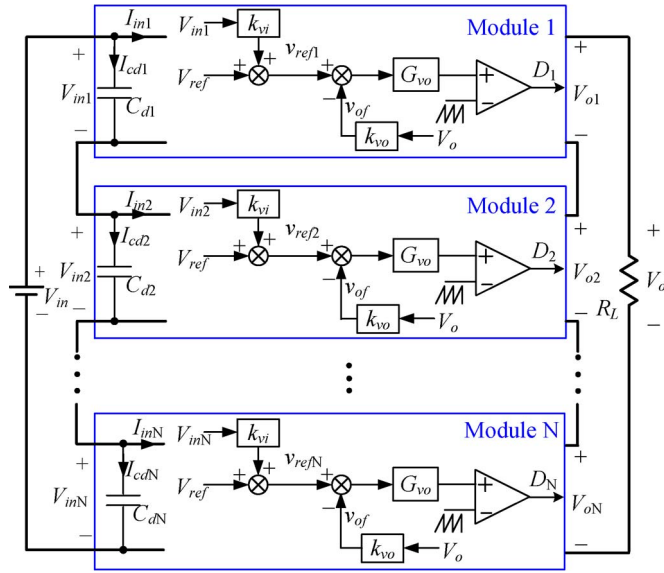


Fig. 5. Proposed decentralized voltage-sharing control strategy.

v_{of} . The input voltage of each module varies in a certain range from $V_{in\ min}/N$ to $V_{in\ max}/N$. When the module input voltage increases from $V_{in\ min}/N$, the error $V_{in\ j} - V_{in_ref}$ ($j = 1, 2, \dots, N$) slowly increases from zero because V_{in_ref} is set to be $V_{in\ min}/N$, and the system output voltage slowly increases, too. Of course, V_{in_ref} can be also set as a certain value between $V_{in\ min}/N$ to $V_{in\ max}/N$, which only affects the start position of the positive output voltage gradient regulation characteristics shown in Fig. 6. Then, the final configuration of the proposed control strategy is obtained, as illustrated in Fig. 5, where k_{vi} is the sensing factor of the module input voltage, k_{vo} is the sensing factor of V_{o_ref} and V_{in_ref} . The detailed control diagram block of the proposed control strategy is shown in Fig. 14, where V_{c1} represents the input reference V_{in_ref} (set as a constant value). It is equal to the sampled value of the module minimum input voltage. V_{c1} is subtracted from the sampled signal of the module input voltage, which is not illustrated in Fig. 5.

Compared with Figs. 2 and 3, as V_{in_ref} is set as a constant value, the external input control signal V_{in_ref} can be directly designed in the control stage, and there is no need to sample the module input voltage through voltage transducers in Fig. 5. In other words, N control signal lines, which are easily disturbed by high-voltage and high-current parts, are removed from the ISOS system. From the point of view of modularity, the less the external input control signals and communication lines, the better the modularity and reliability. Therefore, the ISOS system with the proposed decentralized voltage-sharing control strategy shown in Fig. 5 has superior modularity and reliability compared with the existing control strategies.

III. OPERATION PRINCIPLE OF THE PROPOSED CONTROL STRATEGY

Fig. 5 shows the proposed decentralized voltage-sharing control strategy for the ISOS system. As shown, all the modules are designed to be the same, the controllers are distributed into each

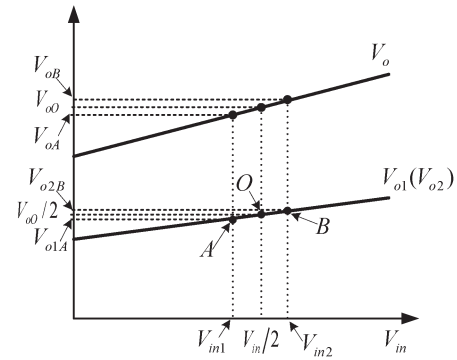


Fig. 6. Positive output voltage gradient regulation characteristics.

module, and there is no central controller. For each module, only the module input voltage and the system output voltage are sampled, and there is no other control signal communication among the modules. There is an output voltage loop in each single module, and the sampled signal of the module input voltage is added to the reference V_{ref} , which is the difference from the conventional output voltage loop with constant reference and is the key part of the decentralized control strategy contributing to the voltage sharing. It can be seen that, for each module, the real reference $v_{ref\ j}$ ($j = 1, 2, \dots, N$) increases with the increase in module input voltage, leading to the increase in the system output voltage; hence, the ISOS system with the proposed control strategy has positive output voltage gradient regulation characteristics, as shown in Fig. 6.

To simplify the analysis, a two-module ISOS system is taken as an example to explain the operation principle of the proposed control strategy. It is assumed that the two modules have the same output voltage regulation characteristics, as shown in Fig. 6. At steady state, the two modules share the input/output voltage, and both work at point O, i.e., $V_{in1} = V_{in2} = V_{in}/2$, and $V_{o1} = V_{o2} = V_{oO}/2$. Assuming that a perturbation occurs on the input divided capacitor voltages, e.g., V_{in1} decreases and V_{in2} increases, i.e., $V_{in1} < V_{in}/2 < V_{in2}$, whereas the input voltage V_{in} is unchanged. From Fig. 5, it can be seen that, although system output voltage V_o is taken as the feedback signal for each module controller, only the module's own output voltages V_{oj} ($j = 1, 2, \dots, N$) can be directly controlled by their corresponding controllers, and V_o is indirectly regulated by summing all the module output voltages. According to the positive output voltage gradient regulation characteristics, for the controller of module 1, the module output voltage should be V_{o1A} , and the system output voltage should be V_{oA} , whereas for the controller of module 2, the module output voltage should be V_{o2B} , and the system output voltage should be V_{oB} . In practice, the real system output voltage is V_{oO} , and we can obtain $V_{oA} < V_{oO} < V_{oB}$. Therefore, for the controller of module 1, the system output voltage is considered to be higher than its corresponding reference voltage, and the duty cycle will be regulated to reduce the module output voltage V_{o1} , and the output power and the input power of module 1 are also reduced. Hence, the module input current I_{in1} will decrease, and I_{cd1} will increase; then, the input voltage of module 1 increases, and the working point moves up from point A. In the meantime,

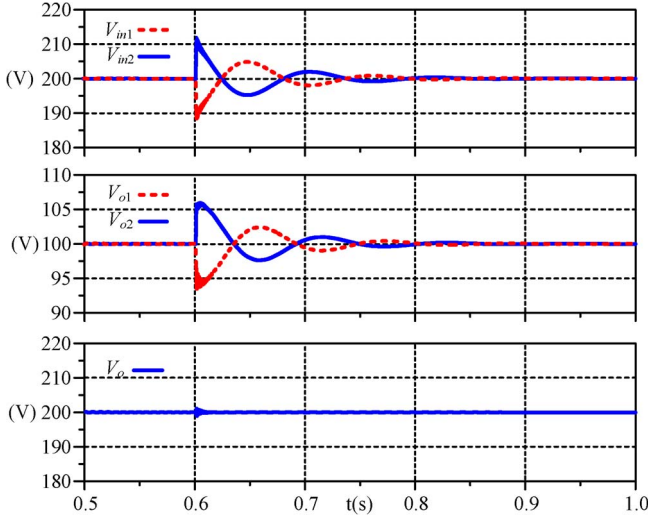


Fig. 7. Simulated individual input voltages, output voltages, and system output voltage corresponding to an input voltage perturbation.

for the controller of module 2, the system output voltage is considered to be lower than its corresponding reference voltage, and its duty cycle will be regulated to increase the module output voltage V_{o2} , and the output power and the input power of module 2 are also increased. Hence, I_{in2} will increase, and I_{cd2} will decrease; then, the input voltage of module 2 decreases, and the working point moves down from point B. Finally, the operating points of the two modules return to the steady-state point O.

The operation principle is simulated for an ISOS system consisting of two converters, and the results are shown in Fig. 7. At steady state, the module input and output voltages are 200 and 100 V, respectively. At 0.6 s, an input voltage perturbation occurs, V_{in1} decreases, and V_{in2} increases. It can be seen that V_{o1} is reduced to shrink the output power and the input power of module 1 to raise V_{in1} ; in the meantime, V_{o2} is increased to increase the output power and the input power of module 2 to decay V_{in2} , and finally, both input and output voltages converge to the steady point, and the system output voltage is almost undisturbed during the input voltage perturbation.

With the analysis and simulation, it can be seen that the constituent modules can evenly share the system input voltage with the proposed control strategy, and the output voltage sharing among the modules is automatically achieved. If the characteristics of the constituent modules are identical, the output voltage sharing is perfect.

In the foregoing analysis, the two modules evenly share the system input voltage with the assumption that the two modules have the same output voltage regulation characteristics as shown in Fig. 6; however, in practice, it is difficult to make the modules have exactly the same characteristics. The effect of the control parameters on the input voltage sharing accuracy will be analyzed with a two-module ISOS system. Assuming that the two modules have different voltage references, according to Fig. 5, at steady state, one can obtain

$$V_{ref1} + k_{vi}V_{in1} = k_{vo}V_o \quad (2)$$

$$V_{ref2} + k_{vi}V_{in2} = k_{vo}V_o. \quad (3)$$

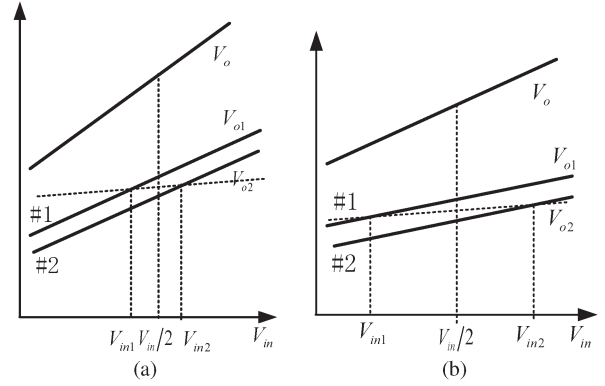


Fig. 8. Effect of k_{vi} on the input voltage-sharing accuracy and the system output voltage regulation. (a) Large k_{vi} . (b) Small k_{vi} .

Using (2) and (3), we have

$$V_{ref1} - V_{ref2} = k_{vi}(V_{in2} - V_{in1}) \quad (4)$$

$$V_o = \frac{V_{ref1} + V_{ref2} + k_{vi}V_{in}}{2k_{vo}} \quad (5)$$

$$\begin{aligned} \frac{\Delta V_o}{\Delta V_{in}} &= \frac{V_{oa} - V_{ob}}{V_{ina} - V_{inb}} \\ &= \frac{\frac{V_{ref1} + V_{ref2} + k_{vi}V_{ina}}{2k_{vo}} - \frac{V_{ref1} + V_{ref2} + k_{vi}V_{inb}}{2k_{vo}}}{V_{ina} - V_{inb}} \\ &= \frac{k_{vi}}{2k_{vo}}. \end{aligned} \quad (6)$$

From (4) and (6), it can be seen that, for a certain voltage reference deviation, the larger the k_{vi} , the better the input voltage-sharing accuracy; however, a larger k_{vi} leads to the deterioration of the system output voltage regulation performance. On the contrary, the smaller the k_{vi} , the worse the input voltage-sharing accuracy, but the system output voltage regulation performance is improved, as shown in Fig. 8. Moreover, the smaller the voltage reference mismatching, the better the input voltage-sharing accuracy. Therefore, a tradeoff must be made between the input voltage sharing and the output voltage regulation performance when designing the control circuit.

The proposed voltage-sharing control strategy is very similar to the droop method for the parallel operation of the dc-dc converters or dc-ac inverters to realize load current sharing [17]–[20]. In the droop method, the information of the module load current is introduced to each module's own controller to realize load current sharing. Correspondingly, in the proposed voltage-sharing control strategy, the information of the module input voltage is introduced to each module's own controller to realize input voltage sharing. The common advantage of the two control methods is that the current/voltage-sharing bus is eliminated, leading to superior system reliability and modularity. While the common shortage of the two control methods is the poor output voltage regulation performance, specifically, the droop method has the negative output voltage gradient regulation with the load current, whereas the proposed control strategy has the positive one. In order to improve the voltage

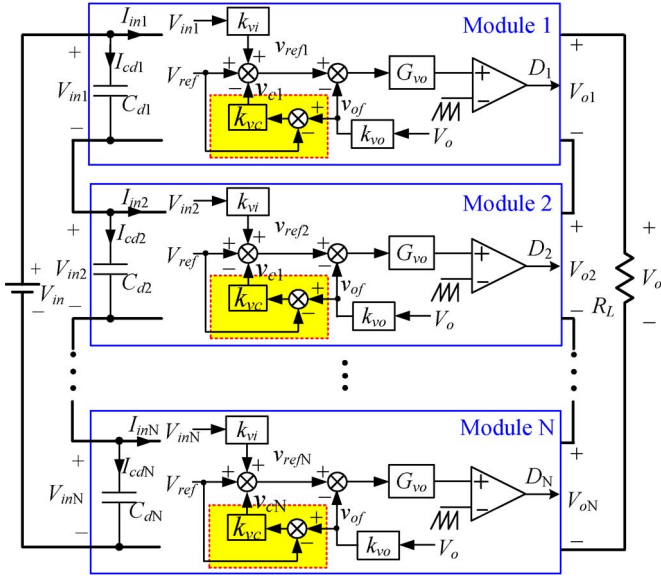


Fig. 9. Proposed decentralized voltage-sharing control strategy with improved output voltage regulation.

regulation of the droop method, a voltage shifting approach is proposed in [20]; although the voltage regulation performance is improved, an additional communication channel or current sharing bus is introduced to transmit the average current signal to each module, which cancels out the benefits of the droop method to some extent because the most important feature of the droop method is no control communication among the constituent modules.

In order to improve the voltage regulation of the ISOS system with respect to the input voltage variation, a novel voltage shifting approach is proposed in this paper without additional communication channel or voltage-sharing bus, as shown in the dashed lines in Fig. 9. In each module, the error signal between v_{of} and V_{ref} is amplified by k_{vc} times, and the output is subtracted from V_{ref} . When v_{of} is higher than V_{ref} , the output signal of the output voltage shifting loop v_{cj} ($j = 1, 2, \dots, N$) is positive, and this signal is subtracted from V_{ref} , meaning that the real reference voltage of each module v_{refj} ($j = 1, 2, \dots, N$) is decreased and the system output voltage will be decreased, correspondingly. v_{cj} ($j = 1, 2, \dots, N$) is identical due to that all the modules have the same V_{ref} , v_{of} , and k_{vc} , which means that same value is subtracted from V_{ref} for each module; hence, the introduction of the output voltage shifting loop has no influence on the operation principle of the proposed control strategy aforementioned.

According to Fig. 5, at steady state, we have $v_{of} = v_{refj}$ ($j = 1, 2, \dots, N$), i.e.,

$$k_{vo} V_o = V_{ref} + k_{vi} V_{inj}, \quad j = 1, 2, \dots, N \quad (7)$$

$$V_{inj} = \frac{V_{in}}{N}, \quad j = 1, 2, \dots, N. \quad (8)$$

Combining (7) and (8), yields

$$V_o = \frac{1}{k_{vo}} \left(V_{ref} + k_{vi} \frac{V_{in}}{N} \right). \quad (9)$$

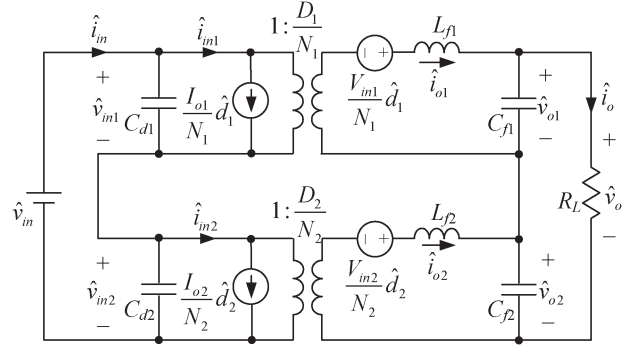


Fig. 10. Small-signal model of a two-module ISOS system.

TABLE I
SYSTEM PARAMETERS

Item	Symbol	Value
Filter inductance	L_f	200 μ H
Filter capacitance	C_f	2000 μ F
Input divided capacitance	C_{d1}, C_{d2}	470 μ F
Output voltage	V_o	100V
Turns ratio	N	5/6
Gain of the ramp	F_m	1/2.5
Load resistance	R_L	20 Ω
Input voltage sensing factor	K_{vi}	3/88
Duty cycle	D	0.4
Input voltage	V_{in}	200V

Hence, the gradient gain of the ISOS system without the output voltage shifting loop is

$$g_{s1} = \frac{k_{vi}}{N k_{vo}}. \quad (10)$$

According to Fig. 9, at steady state, we have

$$k_{vo} V_o = V_{ref} + k_{vi} V_{inj} - k_{vc} (k_{vo} V_o - V_{ref}), \quad j = 1, 2, \dots, N \quad (11)$$

where k_{vc} is the gain of the output voltage shifting loop. Combining (8) and (11) yields

$$V_o = \frac{1}{k_{vo} + k_{vc} k_{vo}} \left(V_{ref} + k_{vi} \frac{V_{in}}{N} + k_{vc} V_{ref} \right). \quad (12)$$

Hence, the gradient gain of the ISOS system with the output voltage shifting loop is

$$g_{s2} = \frac{k_{vi}}{N k_{vo} (1 + k_{vc})}. \quad (13)$$

According to (10) and (13), it can be seen that the gradient gain of the ISOS system can be significantly decreased with the output voltage shifting loop, leading to improved output voltage regulation performance. Moreover, with the output voltage shifting loop, a larger k_{vi} can be employed to ensure good input voltage-sharing accuracy. The concept of the proposed voltage shifting loop can be also applied to the droop method to improve the output voltage regulation without any additional communication channel or current sharing bus.

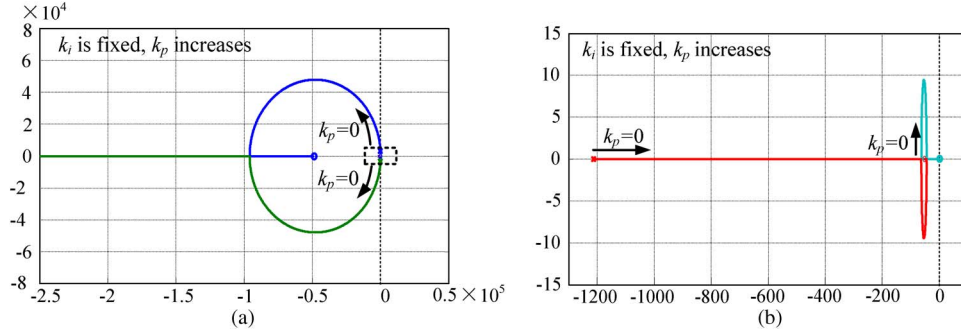


Fig. 11. (a) Root locus when $k_i = 1000$. (b) Expanded waveforms of the dashed box.

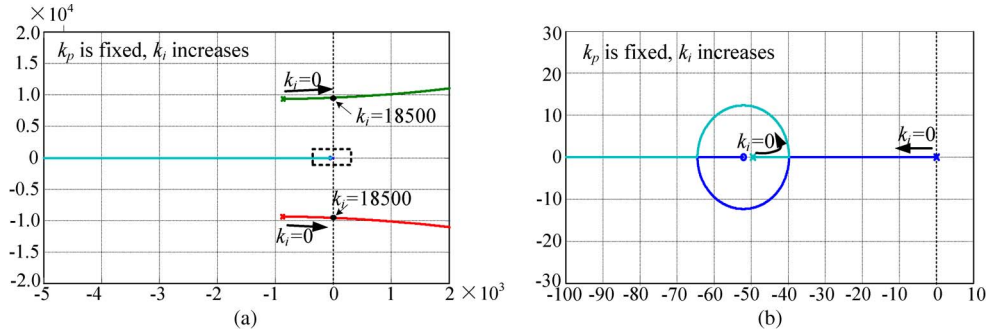


Fig. 12. (a) Root locus when $k_p = 10$. (b) Expanded waveforms of the dashed box.

IV. STABILITY ANALYSIS OF THE PROPOSED CONTROL STRATEGY

Here, the ISOS system stability with the proposed control strategy is studied. For simplicity, an ISOS system consisting of two forward converter modules is studied. The small-signal circuit model is shown in Fig. 10 [8], where \hat{d}_1 and \hat{d}_2 are the perturbations of the duty cycles; \hat{v}_{in1} and \hat{v}_{in2} are the perturbations of the individual input voltages; \hat{i}_{o1} and \hat{i}_{o2} are the perturbations of the individual output currents; N_1 and N_2 are the turns ratios of the transformers; and D_1 , D_2 , I_{o1} , and I_{o2} are the steady-state duty cycles and output currents, respectively.

For simplicity of analysis, the two converter modules are assumed to have the same turn ratio and the same output filter, i.e., $N_1 = N_2 = N$, $L_{f1} = L_{f2} = L_f$, and $C_{f1} = C_{f2} = C_f$. At steady state, $D_1 = D_2 = D$, we have

$$I_{o1} = I_{o2} = \frac{V_o}{R_L} \quad (14)$$

$$V_{in1} = V_{in2} = \frac{V_{in}}{2}. \quad (15)$$

According to Fig. 10, we obtain

$$\hat{i}_{o1} = \frac{N}{D} \left(\hat{i}_{in1} - \frac{V_o}{NR_L} \hat{d}_1 \right) \quad (16)$$

$$\hat{i}_{o2} = \frac{N}{D} \left(\hat{i}_{in2} - \frac{V_o}{NR_L} \hat{d}_2 \right) \quad (17)$$

$$\frac{D}{N} \hat{v}_{in1} + \frac{V_{in}}{2N} \hat{d}_1 = sL_f \hat{i}_{o1} + \hat{v}_{o1} \quad (18)$$

$$\frac{D}{N} \hat{v}_{in2} + \frac{V_{in}}{2N} \hat{d}_2 = sL_f \hat{i}_{o2} + \hat{v}_{o2} \quad (19)$$

$$\hat{v}_{o1} = \frac{1}{sC_f} \left(\hat{i}_{o1} - \frac{\hat{v}_o}{R_L} \right) \quad (20)$$

$$\hat{v}_{o2} = \frac{1}{sC_f} \left(\hat{i}_{o2} - \frac{\hat{v}_o}{R_L} \right). \quad (21)$$

According to Fig. 9, the perturbations of individual duty cycles can be expressed as follows:

$$\hat{d}_1 = G_{vo} F_m [k_{vi} \hat{v}_{in1} - k_{vo} (1 + k_{vc}) \hat{v}_o] \quad (22)$$

$$\hat{d}_2 = G_{vo} F_m [k_{vi} \hat{v}_{in2} - k_{vo} (1 + k_{vc}) \hat{v}_o] \quad (23)$$

where F_m is the gain of the ramp.

For the analysis of the input voltage sharing under condition of small-signal perturbation in total input voltage, the perturbation in the output voltage \hat{v}_o can be neglected, assuming a fast output voltage controller and a large gain of the output voltage shifting loop [8], i.e.,

$$\hat{v}_o = 0. \quad (24)$$

Combining (16), (18), (20), (22), and (24) yields

$$\begin{aligned} Z_{eq1} &= \frac{\hat{v}_{in1}}{\hat{i}_{in1}} \\ &= \frac{s^2 L_f C_f + 1}{G_{vo} F_m k_{vi} \frac{V_o}{NR_L} (s^2 L_f C_f + 1) + s C_f \frac{D}{N} \left(\frac{D}{N} + \frac{V_{in}}{2N} G_{vo} F_m k_{vi} \right)}. \end{aligned} \quad (25)$$

Similarly, combining (17), (19), (21), (23), and (24) yields (26), shown at the bottom of the next page.

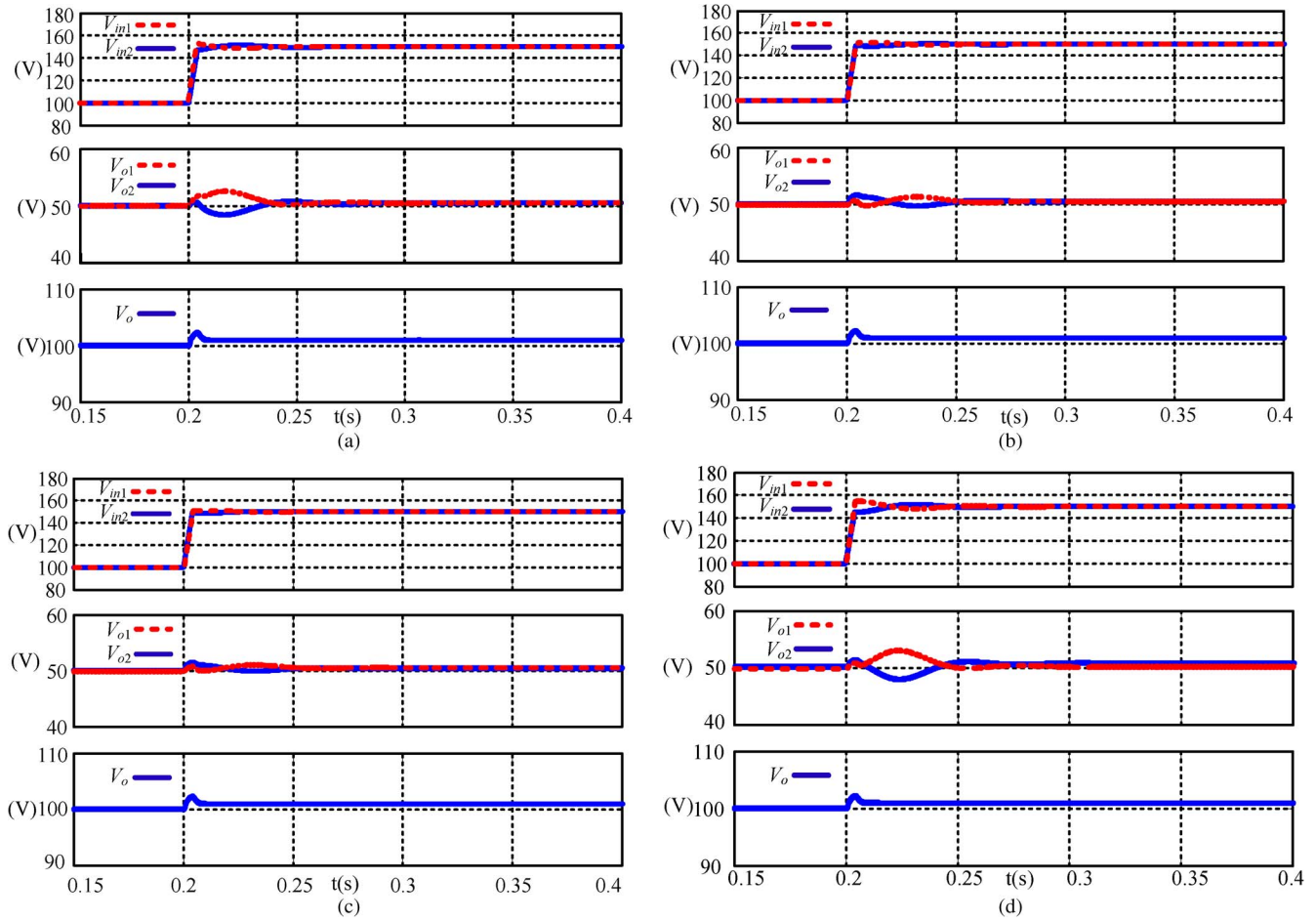


Fig. 13. Simulated individual input voltages, output voltages, and system output voltage corresponding to a stepped input voltage change in the case of component mismatch of modules 1 and 2. (a) Case of mismatch only in input divided capacitance ($C_{d1} = 400 \mu\text{F}$, $C_{d2} = 470 \mu\text{F}$). (b) Case of mismatch only in turns ratio ($N_1 = 5/6.5$, $N_2 = 5/6$). (c) Case of mismatch only in filter inductance ($L_{f1} = 170 \mu\text{H}$, $L_{f2} = 200 \mu\text{H}$). (d) Case of mismatch in input divided capacitance, turns ratio, and filter inductance ($C_{d1} = 400 \mu\text{F}$, $C_{d2} = 470 \mu\text{F}$; $N_1 = 5/6.5$, $N_2 = 5/6$; $L_{f1} = 170 \mu\text{H}$, $L_{f2} = 200 \mu\text{H}$).

Hence, the input voltage difference between the two modules where to the total input voltage can be expressed as

$$F(s) = \frac{\Delta \hat{v}_{in12}}{\hat{v}_{in}} = \frac{\hat{v}_{in1} - \hat{v}_{in2}}{\hat{v}_{in}} = \frac{sZ_{eq}(C_{d2} - C_{d1})}{2 + sZ_{eq}(C_{d2} + C_{d1})}. \quad (27)$$

For the output voltage loop, a classical proportional–integral (PI)-type regulator is used, and its transfer function G_{vo} can be expressed as

$$G_{vo} = k_p + \frac{k_i}{s}. \quad (28)$$

Substituting (25) and (28) into (27), the characteristic polynomial of $F(s)$ can be expressed as

$$D(s) = a_4 s^4 + a_3 s^3 + a_2 s^2 + a_1 s^1 + a_0 \quad (29)$$

$$\begin{cases} a_4 = L_f C_f (C_{d1} + C_{d2}) \\ a_3 = \frac{2V_o}{NR_L} L_f C_f F_m k_{vi} k_p \\ a_2 = \frac{2V_o}{NR_L} L_f C_f F_m k_{vi} k_i + 2C_f \frac{D^2}{N^2} \\ \quad + \frac{D}{N^2} C_f V_{in} F_m k_{vi} k_p + C_{d1} + C_{d2} \\ a_1 = \frac{D}{N^2} C_f V_{in} F_m k_{vi} k_i + \frac{2V_o}{NR_L} F_m k_{vi} k_p \\ a_0 = \frac{2V_o}{NR_L} F_m k_{vi} k_i. \end{cases} \quad (30)$$

As shown in (30), the polynomial has all positive coefficients; however, for the system to be stable, the following has to be satisfied:

$$b_1 = \frac{a_2 a_3 - a_1 a_4}{a_3} > 0 \quad (31a)$$

$$b_2 = \frac{(a_2 a_3 - a_1 a_4) a_1 - a_3^2 a_0}{a_2 a_3 - a_1 a_4} > 0. \quad (31b)$$

$$Z_{eq2} = \frac{\hat{v}_{in2}}{\hat{i}_{in2}} = \frac{s^2 L_f C_f + 1}{G_{vo} F_m k_{vi} \frac{V_o}{NR_L} (s^2 L_f C_f + 1) + s C_f \frac{D}{N} \left(\frac{D}{N} + \frac{V_{in}}{2N} G_{vo} F_m k_{vi} \right)} = Z_{eq1} = Z_{eq} \quad (26)$$

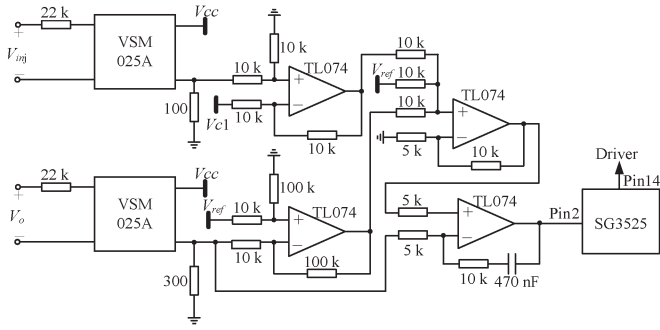


Fig. 14. Electrical diagram of the control circuit for the proposed control strategy.

With proper selection of different k_p and k_i , (31) can be satisfied, and the system will be stable. Selection guidance is based on root locus analysis, and the system parameters are listed in Table I.

The closed-loop root locus for different k_p is shown in Fig. 11. The value of k_i is fixed to be 1000, whereas the value of k_p increases from zero to test the system stability. The dashed box shown in Fig. 11(a) is expanded in Fig. 11(b). It can be seen that the system is always stable for different k_p .

The closed-loop root locus for different k_i is shown in Fig. 12. The value of k_p is fixed to be 10, whereas the value of k_i increases from zero to test the system stability. The dashed box shown in Fig. 12(a) is expanded in Fig. 12(b). As shown in Fig. 12, the system goes from stable to unstable when the value of k_i reaches 18 500 since the roots go from the left-half plane to the right-half plane. Hence, it is concluded that the ISOS system with the proposed decentralized voltage-sharing control strategy is stable if the PI parameters of the voltage compensator are properly selected.

In order to verify the system stability of the proposed method even in the case of large variations in component values, an ISOS system consisting of two forward converters is built for simulation. The basic system parameters are listed in Table I. Four cases of component mismatch are simulated, i.e., mismatch in input divided capacitance, mismatch in turns ratio of the power transformer, mismatch in output filter inductance, and mismatch in all of the above three items, respectively [21]. The simulation waveforms of individual input voltages, output voltages, and system output voltage corresponding to a stepped input voltage change from 200 to 300 V in the case of components mismatch are shown in Fig. 13. It can be seen that the large variations in component values have no influence on the system stability and the input voltage sharing.

V. EXPERIMENTAL RESULTS

In order to verify the feasibility of the proposed control strategy, an ISOS system consisting of three two-transistor forward converter modules has been built in the laboratory, and the specifications are listed as follows:

- system input voltage V_{in} : 300–450 V;
- system output voltage V_o : 150 V;
- maximum output current I_o : 5 A.

For each module, the input voltage is 100–150 V, the output voltage is 50 V, and the maximum output power is 250 W.

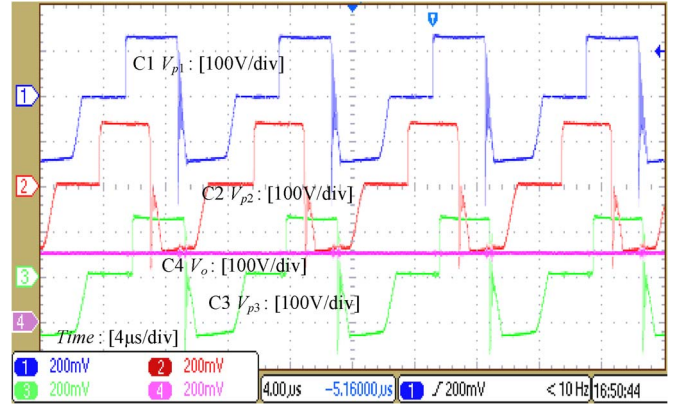


Fig. 15. Voltages across the primary windings of individual transformers under steady state.

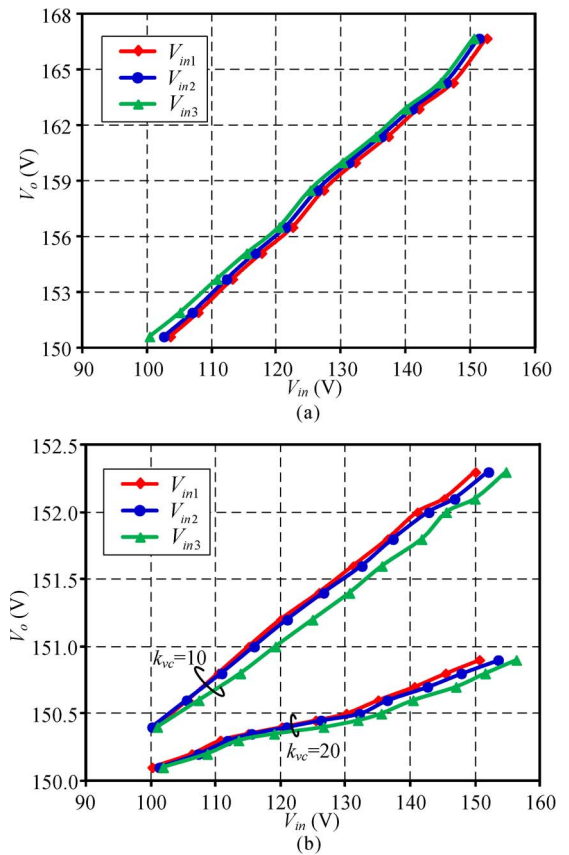


Fig. 16. Measured output voltage regulation curves of three modules. (a) Without voltage shifting loop. (b) With voltage shifting loop.

The detailed control diagram block of the proposed control strategy is shown in Fig. 14. VSM025A is a voltage transducer based on the measuring principle of the Hall effect with a galvanic isolation between the module input voltage and the system output voltage. V_{cc} is the power supply of the control circuit. In Fig. 14, V_{c1} is subtracted from the sampled signal of the module input voltage, which is not illustrated in Figs. 5 and 9; V_{c1} is equal to the sampled value of the module minimum input voltage. Therefore, the real value added to V_{ref} is zero under minimum input voltage.

It should be noted that V_{c1} can be also set as the sampled value of a certain constant value between the module minimum

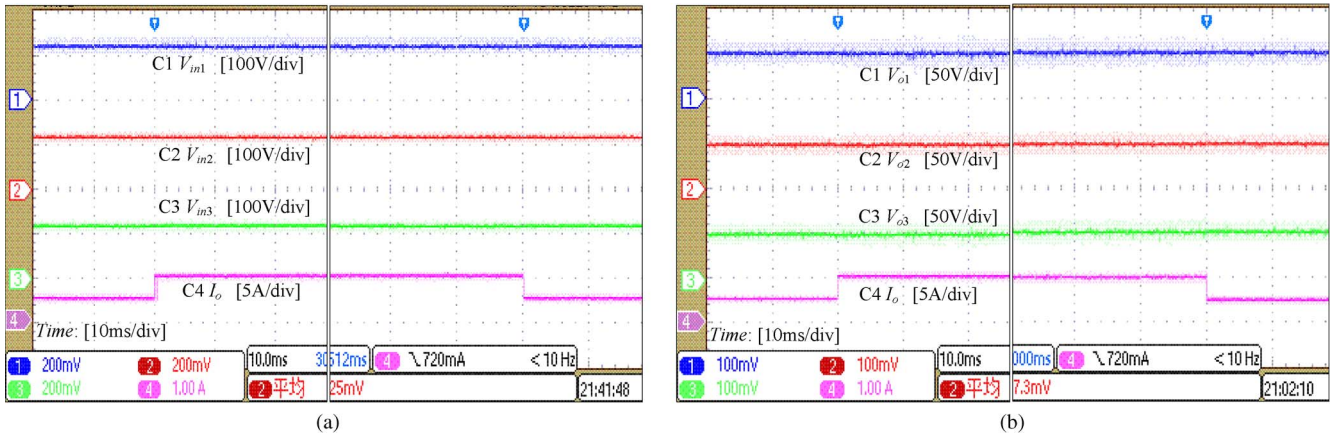


Fig. 17. Experimental waveforms in case of stepped load. (a) Individual input voltages and output current. (b) Individual output voltages and output current.

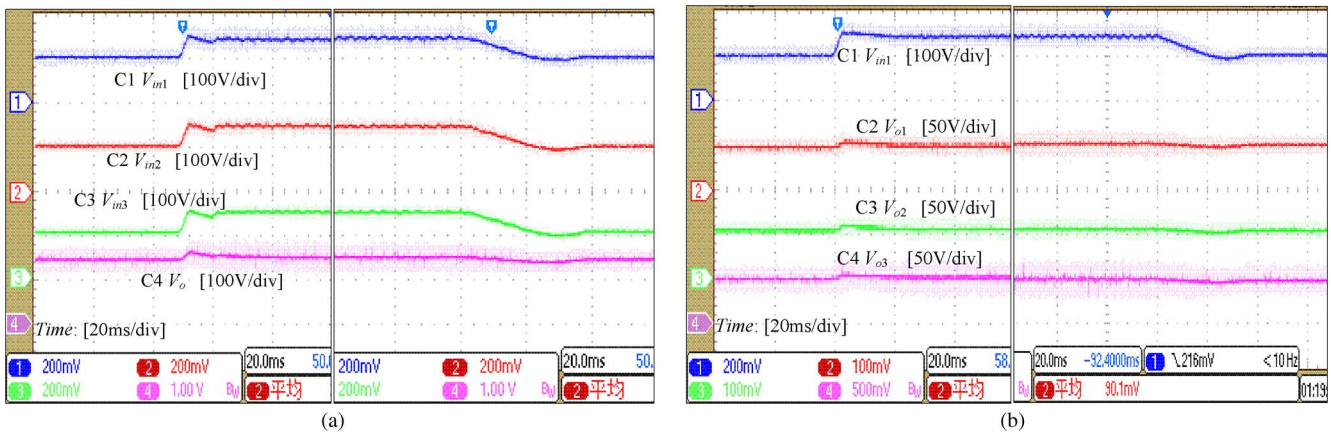


Fig. 18. Experimental waveforms in case of stepped input voltage. (a) Individual input voltages and system output voltage. (b) Individual output voltages and input voltage of module 1.

input voltage and maximum input voltage. For example, V_{c1} can be also equal to the sampled value of $V_{in\text{nom}}/N$, where $V_{in\text{nom}}$ is the center of the input voltage range.

Fig. 15 shows the voltages across the primary winds of individual transformers under normal input voltage (400 V) and full load. It can be seen that all the voltages have almost the same positive amplitudes, which means that the module input voltages are equal.

Fig. 16 shows the measured curves of the system output voltage regulation when the ISOS system input voltage varies from 300 to 450 V. Fig. 16(a) shows the measured curves without the voltage shifting loop (refer to Fig. 5); it can be seen that the increase in the system output voltage (ΔV_o) is nearly 17 V in the whole system input voltage range and that the input voltages of the three modules are well shared. Fig. 16(b) shows the measured curves with the voltage shifting loop (refer to Fig. 9) when $k_{vc} = 10$ and 20. As shown, ΔV_o is significantly reduced with the introduction of the voltage shifting loop, and $\Delta V_o = 0.8$ V when $k_{vc} = 20$, which means that the system output voltage regulation is 0.53% with respect to input voltage variation (300–450 V). Due to small mismatches in the controller parameters among the three prototypes, such as reference voltages, voltage sensors, and operational amplifiers, the measured maximum input voltage difference among the three modules is 5 Vdc, which is acceptable.

Figs. 17 and 18 show the experimental waveforms of the ISOS system corresponding to stepped load and stepped input voltage, respectively. Fig. 17 shows the input voltages and the output voltages of the three modules corresponding to a load stepping between half-load (2.5 A) and full-load (5 A) when system input voltage is 400 V. Fig. 18 shows the input voltages and the output voltages of the three modules and the system's output voltage corresponding to an input voltage stepping between 300 and 450 V under full-load condition. It can be seen that input voltages are well shared both at steady state and during transient, and sharing of the output voltage is automatically achieved.

In order to verify the redundancy of the ISOS system with the proposed decentralized control strategy, a fault is imitated in the ISOS system prototype by shorting the input capacitor of module 1 with a switch in series with a 0.5- Ω resistor limiting the discharging current of the capacitor, and a diode is antiparalleled with its filter capacitor to freewheel the load current. Fig. 19 shows the experimental waveforms in case of module 1 is isolated (closing the short switch) and inserted (opening the short switch) under system input voltage $V_{in} = 330$ V and full-load condition, respectively. After opening the short switch, the input capacitor of module 1 is charged by the system input current, and the module input/output voltage increases; in the meantime, the input/output voltages of

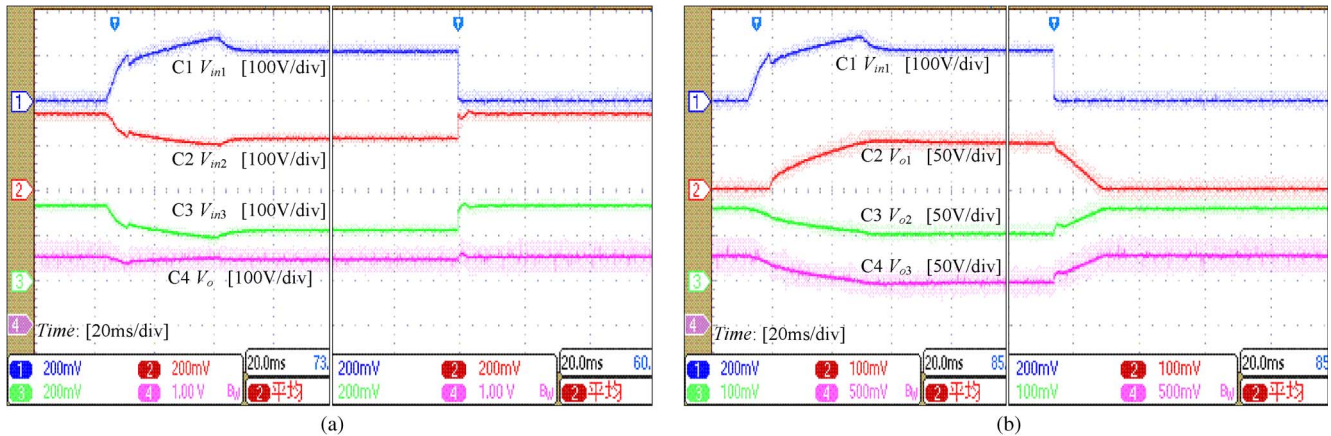


Fig. 19. Experimental waveforms in case of module 1 is inserted and isolated, respectively. (a) Individual input voltages and system output voltage. (b) Individual output voltages and input voltage of module 1.

modules 2 and 3 decrease. After a certain amount of regulation time, the three modules share the system input voltage, and the system output voltage is recovered to be stable. When the short switch is closed, the system input voltage is still evenly shared by the remaining two modules. The output voltage of module 1, i.e., V_{o1} , slowly decreases due to the energy stored in its filter capacitor, and in the meantime, the output voltages of modules 2 and 3, i.e., V_{o2} and V_{o3} , increase correspondingly. In addition, it should be noted that the corner present in the Fig. 19 is actually caused by the integrator windup. It can be seen that, without any control interconnection among the modules, redundancy in combination with hot-swap capability of modules can be easily achieved.

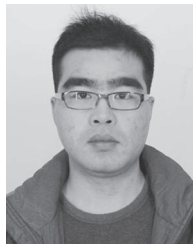
VI. CONCLUSION

This paper has proposed a decentralized voltage-sharing method for the ISOS connection of dc–dc converters to realize fully modular design. Its main feature is that it does not require any control communication link among the constituent modules. The proposed voltage sharing method uses the positive output voltage gradient regulation of the series modules to achieve an even distribution of the input/output voltages among them. Moreover, a voltage shifting loop is introduced to improve the system output voltage regulation. The decentralized voltage-sharing method has the following advantages: 1) all the modules have the same power stages and control stages, which facilitates the system design; 2) control circuits are distributed into each module, and there is no central controller; and 3) the system modularity, reliability, and maintainability are improved. Experimental results of a three-module ISOS system verify the effectiveness of the proposed control strategy.

REFERENCES

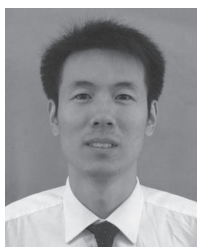
- [1] I. Baraia, J. Barrera, G. Abad, J. Segade, and U. Iraola, "An experimentally verified active gate control method for the series connection of IGBT/diodes," *IEEE Trans. Power Electron.*, vol. 27, no. 2, pp. 1025–1038, Feb. 2012.
- [2] S. Inoue and H. Akagi, "A bidirectional isolated dc–dc converter as a core circuit of the next-generation medium-voltage power conversion system," *IEEE Trans. Power Electron.*, vol. 22, no. 2, pp. 535–542, Mar. 2007.
- [3] B. R. Lin and C. Chao, "Soft-switching converter with two series half-bridge legs to reduce voltage stress of active switches," *IEEE Trans. Ind. Electron.*, vol. 60, no. 6, pp. 2214–2224, Jun. 2013.
- [4] W. Chen, X. Ruan, H. Yan, and C. Tse, "DC/DC conversion systems consisting of multiple converter modules: Stability, control and experimental verifications," *IEEE Trans. Power Electron.*, vol. 24, no. 6, pp. 1463–1474, Jun. 2009.
- [5] R.-L. Lin, W.-S. Liu, J.-F. Chen, M.-H. Chen, and C.-H. Liu, "Positive feedforward control for multimodule output-series power-conversion systems with individual nonideal sources," *IEEE Trans. Ind. Electron.*, vol. 60, no. 4, pp. 1323–1340, Apr. 2013.
- [6] Y. Hayashi, "Approach for highly efficient and ultra compact converters in next generation 380 V dc distribution system," in *Proc. IEEE ECCE*, 2012, pp. 3803–3810.
- [7] C. Zhan, A. Bullock, C. Smith, and A. Crane, "Power collection and transmission systems," EP Patent 2 341 594 A1, Jul. 6, 2011.
- [8] R. Giri, V. Choudhary, R. Ayyanar, and N. Mohan, "Common-duty-ratio control of input-series connected modular dc–dc converters with active input voltage and load-current sharing," *IEEE Trans. Ind. Appl.*, vol. 42, no. 4, pp. 1101–1111, Jul./Aug. 2006.
- [9] P. J. Grbović, "Master/slave control of input-series-and output-parallel-connected converters: Concept for low-cost high-voltage auxiliary power supplies," *IEEE Trans. Power Electron.*, vol. 24, no. 2, pp. 316–328, Feb. 2009.
- [10] X. Ruan, "Control strategy for input-series–output-parallel converters," *IEEE Trans. Ind. Electron.*, vol. 56, no. 4, pp. 1174–1185, Apr. 2009.
- [11] D. Sha, Z. Guo, and X. Liao, "Cross-feedback output-current-sharing control for input-series–output-parallel modular dc–dc converters," *IEEE Trans. Power Electron.*, vol. 25, no. 11, pp. 2762–2771, Nov. 2010.
- [12] D. Sha, K. Deng, Z. Guo, and X. Liao, "Control strategy for input-series–output-parallel high-frequency ac link inverters," *IEEE Trans. Ind. Electron.*, vol. 59, no. 11, pp. 4101–4111, Nov. 2012.
- [13] R. Giri, R. Ayyanar, and E. Ledezma, "Input-series and output-series connected modular dc–dc converters with active input voltage and output voltage sharing," in *Proc. IEEE APEC Expo.*, 2004, pp. 1751–1754.
- [14] Q. Lu, Z. Yang, S. Lin, S. Wang, and C. Wang, "Research on voltage sharing for input-series–output-series phase-shift full-bridge converters with common-duty-ratio," in *Proc. IEEE IECON*, 2011, pp. 1548–1553.
- [15] J. Merwe and H. Mouton, "An investigation of the natural balancing mechanisms of modular input-series–output-series dc–dc converters," in *Proc. IEEE ECCE*, 2010, pp. 817–822.
- [16] D. Sha, K. Deng, and X. Liao, "Duty cycle exchanging control for input-series–output-series connected two PS-FB dc–dc converters," *IEEE Trans. Power Electron.*, vol. 27, no. 3, pp. 1490–1501, Mar. 2012.
- [17] J. W. Kim, H. S. Choi, and B. H. Cho, "A novel droop method for converter parallel operation," *IEEE Trans. Power Electron.*, vol. 17, no. 1, pp. 25–32, Jan. 2002.
- [18] Q.-C. Zhong, "Robust droop controller for accurate proportional load sharing among inverters operated in parallel," *IEEE Trans. Ind. Electron.*, vol. 60, no. 4, pp. 1281–1290, Apr. 2013.

- [19] X. Lu, K. Sun, J. M. Guerrero, J. C. Vasquez, and L. Huang, "State-of-charge balance using adaptive droop control for distributed energy storage systems in dc microgrid applications," *IEEE Trans. Ind. Electron.*, vol. 61, no. 6, pp. 2804–2815, Jun. 2014.
- [20] S. Anand, B. G. Fernandes, and J. M. Guerrero, "Distributed control to ensure proportional load sharing and improve voltage regulation in low-voltage dc microgrids," *IEEE Trans. Power Electron.*, vol. 28, no. 4, pp. 1900–1912, Apr. 2013.
- [21] J. W. Kimball, J. T. Mossoba, and P. T. Krein, "A stabilizing, high-performance controller for input series-output parallel converters," *IEEE Trans. Power Electron.*, vol. 23, no. 3, pp. 1416–1427, May 2008.



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