Comparing Ladder Logic Diagrams and Petri Nets for Sequence Controller Design Through a Discrete Manufacturing System

Kurapati Venkatesh, Student Member, IEEE, MengChu Zhou, Senior Member, IEEE, and Reggie J. Caudill

Abstract—Design methods for sequence controllers play a very important role in advancing industrial automation. The increasing complexity and varying needs of modern discrete manufacturing systems have challenged the traditional design methods such as the use of ladder logic diagrams (LLD's) for programmable logic controllers. The methodologies based on research results in computer science have recently received growing attention by academic researchers and industrial engineers in order to design flexible, reusable, and maintainable control software. Particularly, Petri nets (PN's) are emerging as a very important tool to provide an integrated solution for modeling, analysis, simulation, and control of industrial automated systems. Petri nets (PN's) are emerging as a very important tool to provide an integrated solution for modeling, analysis, simulation, and control of industrial automated systems. The methodologies based on research results in computer science have recently received growing attention by academic researchers and industrial engineers in order to design flexible, reusable, and maintainable control software. Particularly, Petri nets (PN's) are emerging as a very important role in advancing industrial automation.

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TABLE I
VARIOUS METHODS OF PETRI NET BASED SEQUENCE CONTROL

<table>
<thead>
<tr>
<th>Implementation scheme of Petri net controller</th>
<th>Hardware used</th>
<th>Reference</th>
</tr>
</thead>
<tbody>
<tr>
<td>Petri net (High-level language descriptions)</td>
<td>Compiler</td>
<td>Control sequence tables and decision programs</td>
</tr>
<tr>
<td></td>
<td>Interpreter</td>
<td>Control tables</td>
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<tr>
<td>Petri net (Execution algorithm)</td>
<td>control</td>
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<tr>
<td>Petri net</td>
<td>Software</td>
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<tr>
<td>Petri net (Simulation)</td>
<td>Petri net simulator</td>
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<td>Software system based on ICL Plus</td>
<td>control</td>
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</tbody>
</table>

1. To identify the criteria to compare LLD's and PN's for design of sequence control,
2. To introduce Real-Time PN's (RTPN's) which closely resemble ordinary PN's as an integrated tool to develop discrete event controllers, and
3. To compare LLD's and RTPN's in designing sequence controllers that respond to specification changes.

II. CONTROL LOGIC DESIGN BY LLD'S AND PN'S AND THEIR COMPARISON CRITERIA

The application of LLD's for sequence control is widely known because they are used by several industries [9], [12]. An excellent tutorial on PN's and their applications is given in [11] and their applications in manufacturing automation are reported in [6], [13], [20]. In order to use PN's for real-time sequence control, timing and input/output sensory information has to be integrated into them which will be discussed next. The logic and other basic building blocks used in sequence control are modeled by PN's and LLD's as shown in Table II. In the table, the first four rows show the basic PN elements to model conditions, status, activity, information and material flow, and resources. Note that LLD's do not have the corresponding explicit representations. Logical AND and logical OR can be easily modeled by both PN and LLD with similar complexity. Other important concepts, e.g., concurrency, time delay, and synchronization are also illustrated in Table II.

The systematic methods to formulate PN models can be seen in [19], [20] and the methods of developing LLD's can be seen in [9], [12]. Two of the important factors for comparison of PN and LLD for discrete event control are identified as design complexity and response time as described below.

A. Design Complexity

Design complexity is defined as the complexity associated in designing the control logic for a given specification. Since it is influenced by many factors, e.g., the experience of designers, size of control program, and number of dynamic steps necessary for coding or changing the control program, it is very hard to quantify formally. However, it can be characterized by two factors, namely graphical complexity and adaptability for change in specification.

Graphical Complexity: It is mainly determined by the number of nodes and links for a given graphical control logic design. Graphical complexity influences the understandability of control logic by people who do not have knowledge of either PN's or LLD's. Hence, it is an important factor in designing the logic at the initial stages and subsequently debugging the errors during its implementation. The graphical complexity in terms of the net size is a major issue in manufacturing systems [18] and it was reported that the simpler the graphical representation of control logic, the easier to track the controller [1]. Graphical complexity may also influence response time as described later. For example, in the case of LLD's, the response time depends on the size of the LLD. Hence, a short LLD results in a fast controller [12].

Adaptability for Change in Specifications: This factor is gaining much importance in the context of agile manufacturing in which control sequences need to be changed often to meet the dynamically changing requirements of the market. The control software should be easily adaptable to changes in specifications in order to improve the software productivity and thus keep minimal development time. One of two
designs is said to be more adaptable if it needs fewer changes compared to another in order to fulfill a specification change.

B. Response Time

Response time is termed as scan time in LLD literature and execution time in PN. Its importance to control real-time systems is clear since it decides how fast the control system responds to an event in the system/process under control.

The important factor that influences graphical complexity and adaptability is the physical appearance (size) of the model, whereas the response time is influenced by not only the physical appearance but also the method of implementation. Methods of implementation constitute the software and hardware used to control the system using either PN’s or LLD’s. Graphical complexity and adaptability cannot be quantified, whereas response time can be measured accurately, given a logic design and implementation. However, since there are several ways to implement PN’s as shown in Table I and LLD’s [12] both in terms of hardware and software, it is very difficult for a fair comparison of LLD’s and PN’s solely on the response time criterion. The reasons discussed above will motivate one to find common measures that give an idea about the graphical complexity, adaptability, and response time. One of these measures is the number of nodes and links used in a control logic model. For PN’s nodes are places and transitions and links are arcs; whereas in LLD’s, nodes are normally opened/closed switches, timers, counters, relays, and push buttons, and links are connections. If more nodes and links are used in a design, it is graphically more complex and thus may need more response time. In a similar manner, a control logic is more adaptable if it needs fewer changes in the number of nodes and links compared to another logic to meet a change in specification. Hence, this study uses the number of nodes and links in LLD and PN as a measure to compare their design complexity and response time. For the sake of convenience nodes and links are called as basic elements. Future work is needed to address other common measures.

III. REAL-TIME PETRI NETS

PN’s have been augmented and implemented in a variety of ways to achieve real-time control as shown in Table I. Based on the research in PN control literature, this paper proposes a class of PN’s called Real-Time PN’s (RTPN’s) for sequence controller design. Also, it demonstrates a simple and straight-forward procedure to implement them [16]. Even though RTPN’s and earlier classes of PN’s for control share similar principles, some of the differences between them are listed below:

1. Earlier studies use a variety of places to model timers and counters [11]. This might make the model difficult to understand. In RTPN’s neither new places nor transitions are introduced for modeling them. Timers are modeled by assigning attributes to transitions and counters by places with initial markings and weights on certain arcs. In [14] new sets of places are introduced for modeling I/O signals which increases the number of places in the PN model. In RTPN’s, I/O signals are modeled as attributes for places and transitions respectively. Hence, due to the use of attributes, RTPN’s have less nodes and links compared to PN’s in [11], [14], thereby reducing the graphical complexity.

2. In earlier works (e.g., [1], [3], [14]) the resetting of timers and counters and an emergency stop are not explicitly modeled. Furthermore, often they use additional functions to model and implement timers and counters [14]. Using RTPN’s all these can be clearly modeled. The automatic resetting of timers and counters is also embedded in the execution of RTPN’s.

3. RTPN’s model the system more realistically by natural mapping of the limit switches, start, and stop buttons as places and can be easily extended to model breakdown handling procedures by using the concepts of Augmented Timed Petri nets [17].

4. RTPN’s can be implemented by a simple implementation scheme. For example, RTPN’s eliminate the usage of high level net description languages used in [3], [15], [18] since the RTPN model can be directly used for control with the help of a token player. By adopting this implementation scheme, the need to translate the PN model to higher level net description language is avoided. The actual implementation of the token player in RTPN’s is transparent to the users and hence their only task to control a system is simplified to model the control logic.

RTPN’s can be obtained by associating timing, I/O sensory information to the untimed PN’s and defined as follows: An RTPN is an eight tuple and defined as: \( RTPN = (P, T, I, O, m, D, X, Y) \) where:

1. \( P \) is a finite set of places;
2. \( T \) is a finite set of transitions with \( P \cup T \neq \emptyset \) and \( P \cap T = \emptyset \);
3. \( I: P \times T \rightarrow N \), is an input function that defines the set of directed arcs from \( P \) to \( T \) where \( N = \{0, 1, 2, \ldots\} \);
4. \( O: P \times T \rightarrow N \), is an output function that defines the set of directed arcs from \( T \) to \( P \);
5. \( m: P \rightarrow N \), is a marking whose \( i^{th} \) component represents the number of tokens in the \( i^{th} \) place. An initial marking is denoted by \( m_0 \);
6. \( D: T \rightarrow R^+ \), is a firing time function where \( R^+ \) is the set of nonnegative real numbers;
7. \( X: P \rightarrow \{-0.1, 2, \ldots, K\} \) and \( X(p_i) \neq X(p_j), i \neq j \), is an input signal function, where \( K \) is the maximum number of input signal channels, and "-" is the dummy attribute indicating no assigned channel to the place;
8. \( Y: T \rightarrow L \), is an output signal function, where \( L \) is a set of integers.

In an RTPN, the first five tuples represent the untimed PN and the last three tuples are extensions added to it and explained below:

1. Timing vector \( (D) \) is intended to associate time delays to transitions modeling the activities in the system;
2. Input signal vector \( (X) \) reads the state of the input signals from digital input interface. \( X \) associates at-
ttributes to every place. \( X_i = X(p_i) \) and is an attribute associated with place \( p_i \) and represents the input channel number associated with \( p_i \). For example, if \( p_i \) models a limit switch, the RTPN reads the status of that switch from the digital input interface through the channel number represented by \( X_i \). The initial marking, \( m_i(p_i) \) is considered as the first attribute of \( p_i \) and \( X_i \) is the second one. The contents of any input channel \( X_i \) are either 0 or 1; and

3. Output signal vector \((Y)\) is intended to send output signals through digital output interface. \( Y \) associates attributes to every transition. \( Y_i = Y(t_i) \) and is the attribute associated to transition \( t_i \) which represents the number that is to be sent to the digital output interface. For example, \( t_i \) may model the activity "send signal to actuate solenoid \( A_i \)" or "execute a procedure to control a robot." Each solenoid is activated by writing a specific number on to the digital output interface. During execution of the program, when a transition fires, RTPN writes the decimal number corresponding to the output channel to digital output interface. The contents of any output channel are either 0 or 1. The usage of this vector is later detailed in the example system.

There are two events for a transition firing, \textit{start firing} and \textit{end firing}. Between these the firing is in progress. The removal of tokens from a transition's input place(s) occurs at \textit{start firing}. The deposition of tokens to a transition's output place(s) occurs at \textit{end firing}. While transition firing is in progress, the time to end firing, called the \textit{remaining firing time}, decreases from firing duration to zero at which its firing is completed. The execution rules of a RTPN include enabling and firing rules:

1. A transition \( t \in T \) is enabled if \( \forall p \in P \) and \( I(p,t) \neq 0, m(p) \geq I(p,t) \) and \( X(p) \) has content 1.

2. Enabled in a marking \( m \), \( t \) fires and results in a new marking \( m' \) following the rule:

\[
m'(p) = m(p) + O(p,t) - I(p,t), \forall p \in P.
\]

The design procedure for formulating a RTPN based controller is shown in Fig. 1 and is briefed in the following five steps:

1. Model the control sequence using PN's to obtain the PN model of the sequence controller.
2. Assign input channels to inputs of the system such as limit switches, sensors, etc. to formulate an input mapping table.
3. Assign output channels to outputs of the system such as solenoids, switches, etc. Also, identify timing information for activities to obtain an output mapping table.
4. Using the input mapping table, assign an input channel number to each place in the PN based controller. The initial state of the system decides the initial marking of RTPN. In the PN model some places do not represent the inputs of the system as they represent the intermediate states of system or logical places to model counters in the sequence. Hence, no channel has to be assigned to these places. Represented by "-".
5. Using the output mapping table and the action(s) that are modeled by a transition, assign a number to each transition in a PN based controller. The operations and the time delays given in the sequence to be controlled decides firing time function of RTPN. In the PN model some transitions do represent concurrent actions. Hence, care should be taken to assign the numbers for such transitions.

By following the above procedure, an RTPN based controller can be formulated for a given sequence. There are several ways to use PN's to perform the sequence control. One is based on "token game" [16], [18] and the other converts the net into either Programmable Logic Controllers [15] or control code directly [20]. The first scheme is used in this work as illustrated in Fig. 2 and explained here. An RTPN based controller can be embedded in and executed by a computer. As the execution of RTPN starts and continues, the system being controlled also starts and continues to perform operations corresponding to the sequence modeled by RTPN. There are digital I/O interfaces that act as a bridge between the RTPN and the system being controlled. For more details of the software implementation of RTPN based controllers, refer to [16].
IV. COMPARISON OF LLD'S AND RTPN'S THROUGH A CASE STUDY

A. System Description

It is found that one effective way to perform the comparison between LLD's and RTPN's is through an actual industrial automated system. The system considered in this paper is shown in Fig. 3. It consists of four pneumatic cylinders (A, B, C, and D) which are operated by spring-loaded five ports and two-way solenoid valves. Each piston has two normally open limit switches. For example, when the end of piston A contacts limit switch a0(a1), a0(a1) is closed, it indicates that the piston A is at the end of its return stroke (forward stroke). The time that a piston takes for completion of either a forward or backward stroke is 1 s. In manufacturing, typical functions of these pistons can be to load/unload the part from the machine table, to extend/retract a cutting tool spindle, etc. Three push buttons are provided to start the system (switch SW1), to stop the system normally (switch SW2) and to stop the system immediately in emergency (switch SW3, ES).

Hence, the system has 11 inputs corresponding to 8 limit switches (two for each piston) and 3 push buttons. The system has 6 outputs corresponding to 4 solenoid valves and two lights that indicate the status of the system. “Interlock logic” also referred to as “double command” is an error normally encountered in designing LLD’s due to which both sides of the solenoid are activated simultaneously. Unfortunately, the system under study does not exhibit such problems because no double activated solenoids are used. The comparison between LLD and RTPN for complex interlock logic is important and should be addressed in the future. Breakdowns or fault sensors are not considered either, although this work can be extended for them using some concepts in the future. Breakdowns or fault sensors are not considered either, although this work can be extended for them using some concepts in [17]. In this study, LLD’s are implemented in a Modicon PLC and RTPN’s are implemented through IBM PC and digital I/O interface as shown in Fig. 3. The procedures given in Fig. 1 and 2 are used to design the control logic by RTPN’s. Tables III and IV show the I/O mappings of the system to PN respectively. For more details on this system and its implementation, refer to [16]. The following focuses on the design complexity when the control specification varies.

B. Sequence Controller Design

Sequence 1: \(A^+, B^+, \{C^+, A^-\}, \{B^-, C^-\}\): Consider that the system has to be controlled to execute the above sequence where \(A^+\) represents that the piston has to do forward stroke and \(A^-\) return one. \(\{C^+, A^-\}\) represents two concurrent actions taking place simultaneously: Piston C to do a forward stroke and Piston A to do a return one. Fig. 4(a) shows the LLD and Fig. 4(b) shows the RTPN corresponding to this sequence. Note that in the RTPN, a place has attributes \([n_1, n_2]\) where \(n_1\) is the first attribute representing an \(n\) number of tokens and \(n_2\) is the second one mapping an input channel number. Similarly, a transition has attributes \([n'_1, n'_2]\) where \(n'_1\) is the firing duration and, \(n'_2\) is to be written on the digital output interface. When concurrent actions such as \(\{C^+, A^-\}\) are to be modeled, care should be taken to associate the second attribute to transitions as shown in Table V.

As discussed earlier, basic elements in an LLD or RTPN are nodes and links. In an LLD, nodes are push buttons, normally opened switches, normally closed switches, output relay coils, timers, and counters and links are connections which connect these nodes. In an RTPN, nodes are places and transitions and links are arcs connecting them. The LLD shown in Fig. 4(a) has 56 basic elements (23 nodes and 33 links), whereas the RTPN shown in Fig. 4(b) has 46 basic elements (19 nodes and 27 links). At this stage, RTPN looks more complex due to the fact that all loops have to be closed to represent repetitive processes. This complicates the graphical appearance of PN compared to LLD.

C. Control for Other Sequences

In order to compare the LLD’s and RTPN’s, various sequences with increasing complexity are considered. These sequences will involve emergency stop, counters for counting the number of repetitive operations, and timers for providing delays between certain operations.

Sequence 2: START, 5 \([A^+, B^+, \{C^+, A^-\}, \{B^+, C^-\}]\) (With Emergency Stop and Counter): Now, consider that the specification is changed such that the new control sequence is...
as indicated above. In this sequence, there is a need to provide emergency stop and a counter. In this system, both the LLD and RTPN are implemented such that when the emergency stop switch, ES, is pressed, the whole system, including the active elements, are immediately stopped. In other words, when switch ES is pressed all the solenoids are immediately deactivated. Fig. 5(a) shows the LLD and Fig. 5(b) shows the RTPN corresponding to this sequence. In order to incorporate the emergency stop, the RTPN uses a place with an inhibitory arc as an input place for $t_2 - t_8$. The LLD shown in Fig. 5(a) has 86 basic elements (36 nodes and 50 links), whereas the RTPN shown in Fig. 5(b) has 59 basic elements (22 nodes and 37 links). Notice that there is no significant change in the physical appearance of LLD or RTPN compared to sequence 1. Observe that the LLD needs more additional basic elements compared to the RTPN. This is because the RTPN needs only one place $p_{13}$ with an arc as an input to $t_2$ to implement the counter. The counter resetting is modeled by $t_9$. In contrast to this, the LLD needs more normally opened, normally closed switches, a counter, and 15 more links to implement this sequence.

Sequence 3: START, 5 $[A+, B+, \{C+, A\-\}, B-, C-]$. (With Emergency Stop, Counter and Timer): The sequence is changed such that there is a need to incorporate a
timer in the control logic to provide 6 s delay in between \({C^+, A^-}\) and \({B^+, C^-}\). Fig. 6(a) shows the LLD and Fig. 6(b) the RTPN accordingly. The LLD shown in Fig. 6(a) has 95 basic elements (39 nodes and 56 links), whereas the RTPN shown in Fig. 6(b) is the same as the one shown in Fig. 5(b) with 59 basic elements. It is observed that the LLD needs more additional basic elements compared to the RTPN. This is because the same RTPN used in the earlier sequence is used without changing the physical appearance. In the RTPN shown in Fig. 5(b), only the first attribute of \(t_{8}\) is changed to obtain the RTPN shown in Fig. 6(b) to incorporate time delay of 6 s in the sequence. On the other hand, the LLD needs more normally opened, normally closed switches, a timer, and many links to implement this sequence.

Sequence 4: 3 \( [\text{START}, [A+, B+, [C^+, A^-], 6\ s, \{B^+, C^-\}], 10\ s, 2 [\text{START}, [A+, B+, [C^+, A^-], 6\ s, \{B^+, C^-\}]] \) (With Emergency Stop, Counters and Timers): This new sequence represents a complex one in which Sequence 3 is divided into two segments (one with three cycles and another with two cycles) with 10 s time delay between them. The LLD shown in Fig. 7(a) has 128 basic elements (53 nodes and 75 links), whereas the RTPN in Fig. 7(b) has 64 basic elements (24 nodes and 40 links). In this case also note that the LLD needs more additional basic elements compared to the RTPN. This is because the RTPN needs only one additional transition \(t_9\) with an input arc from \(p_{11}\) (to model the first three cycles in the sequence) and an output arc to \(p_{13}\) (to model the last two cycles).

V. DISCUSSION

As mentioned in Section 2, the number of basic elements is a common measure that gives an idea about graphical complexity, adaptability and response time. It is observed that as the specification changes, the RTPN requires fewer changes compared to the LLD. Table VI summarizes how the
It can be inferred that the RTPN shown in Fig. 4(b) used to execute the first sequence is slightly modified to get the RTPN shown in Fig. 7(b) corresponding to the last sequence. However, the LLD shown in Fig. 4(a) is significantly modified to get the LLD shown in Fig. 7(a). The modifications in terms of basic elements can be quantified using Table VI. Also, observe that the physical appearance of RTPN is preserved (with slight modifications) starting from the first sequence to the last sequence. This is not true in the case of the LLD as indicated in Fig. 4(a)-7(a). Furthermore, this case study reveals that RTPN's and LLD's do not differ much when the control sequence is relatively simple, as seen in the first sequence. In fact, the RTPN model may appear more complex than the LLD at first sight, as shown for the first sequence. However, when this sequence is modified to result in a complex one, RTPN's are more easily modifiable and hence maintainable than LLD's. Ease in modifiability and maintainability yields several advantages such as improvement in readability, understandability, and reliability as concluded in [11]. In LLD's, nodes appear multiple times which may lead to difficulty in understanding the logic and cause errors in developing the logic. LLD's need more basic elements to model timers and counters compared to RTPN's. In addition to these findings, the following points are experienced during the design and implementation of sequence controllers using LLD's and RTPN's:

1. Using RTPN's, the control logic can be qualitatively analyzed to check properties such as absence of deadlocks and presence of re-initializability in the system. Using LLD's qualitative analysis is not possible until it is simulated or implemented.
2. During implementation of control sequences 3 and 4 it is found that debugging of the control logic with LLD's is difficult compared to RTPN. This is because RTPN's help to dynamically track the system with the help of the states of places and transitions [16].
3. Using RTPN's, the initial state of the system can be directly represented by its initial marking.

VI. CONCLUSION AND FUTURE RESEARCH

Development of flexible, reusable, and maintainable control software is important to implement advanced industrial automated systems. Traditional methods of using ladder logic diagrams (LLD's) to design sequence controllers are being challenged by the needs in flexible and agile manufacturing systems. On the other hand, Petri nets (PN's) are an emerging tool that needs to be established for the control of discrete manufacturing systems. This paper identified design complexity and response time as the criteria to compare LLD's and PN's. Design complexity is defined and characterized by two factors, namely, graphical complexity and adaptability to meet changes in control specifications. A class of PN's called real-time PN's that resemble ordinary PN's are introduced to design sequence controllers. By designing and implementing the control of an industrial automated system for changing control requirements, LLD's and RTPN's are compared in terms of a common measure, namely, the number of basic elements that signifies both design complexity and response time. The significance of this present work is to help industry to recognize the prominence of RTPN's as an emerging technology and to encourage more applications. The procedure for controlling a system using RTPN's is straightforward, simple, and can be applied to control any discrete event system that has digital input/output interfaces and a computer.

RTPN's can be extended by adding more attributes to places and transitions in order to control complex hierarchical manufacturing systems that use advanced communication protocols and several computers for control. The present study can be extended by designing a discrete event controller in which there exist choices to perform a control task. Another research issue is to perform the comparison between LLD's and RTPN's to model breakdown handling, error recovery, and
interlock situations in manufacturing systems. A bench mark study comparing PN's, LLD's, and sequential flow charts is another interesting work to be performed.

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VENKATESH et al.: COMPARING LADDER LOGIC DIAGRAMS AND PETRI NETS FOR SEQUENCE CONTROLLER DESIGN

MengChu Zhou (S'88-M'90-SM'93), for a photograph and biography please see page 566 of this issue.

VENKATESH (S'92) received the B.S. degree in mechanical engineering from S.V. University, India, in 1988, the M.S. degree in mechanical engineering from the Indian Institute of Technology, Madras, India in 1990, and the M.S. degree in manufacturing systems engineering from Florida Atlantic University, Boca Raton, FL, in 1992.

At the Indian Institute of Technology, he worked as a research associate in a project by the Indian Department of Science and Technology to explore the applications of Petri nets in flexible automation. During 1991-1992 at Florida Atlantic University, he worked as a research assistant in the Robotics Center. For the past four years, he has been applying the concepts of Petri nets to investigate a variety of problems in flexible automation. Currently, he is a doctoral candidate in the Department of Mechanical Engineering at New Jersey Institute of Technology, Newark, NJ. His research interests include Petri nets, flexible automation, design of control software using object-oriented techniques, intelligent machining, and neural networks. He has published about 25 papers in several journals and conferences including Journal of Manufacturing Systems, Computers and Industrial Engineering, International Journal of Operations and Production Management, and Journal of Material Processing and Technology. He is a referee for Computers and Industrial Engineering.

Mr. Venkatesh is a student member of Phi Kappa Phi, SME, and IIE.

Reggie J. Caudill received the undergraduate and master’s degrees from the University of Alabama, and the Ph.D. in mechanical engineering from the University of Minnesota in 1976.

He was Co-Director of the Industrial Automation and Robotics Program at Princeton University. He was the Director of Robotics and Computer Graphic Laboratory and Associate Professor in Mechanical Engineering at the University of Alabama. He also served as the Technical Coordinator responsible for establishing the computer and CNC manufacturing laboratory at The Bevill Center, an advanced manufacturing center in Alabama. He is currently the Executive Director of the Center for Manufacturing Engineering Systems and Professor of Mechanical Engineering at the New Jersey Institute of Technology, Newark, NJ. The Center is an Advanced Technology Center of the New Jersey Commission on Science and Technology. In addition to research and technology assistance programs, the Center has overall responsibility for coordination and direction of NJIT’s manufacturing initiatives—facilities, collaborative partnerships, and academic programs. His primary research interests are in the integration of advanced machine and computer technologies with applications in CAD/CAM and robotics. His early pioneering research in automated vehicle systems helped to create the base for today’s advances in intelligent vehicle highway systems. His current research involves the development of technologies and systems leading to a fully contactless, ultra-precision manufacturing environment for hard metals and other hard-to-machine materials. He is the author of over 50 technical publications and has received over $3 million in research funding from various governmental agencies and private industry.