# An Incremental Tensor-Train Decomposition for Cyber-Physical-Social Big Data 

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#### Abstract

Cyber-physical-social big data generated from ubiquitous devices and diverse spaces generally are multi-source, heterogeneous, and deeply intertwined. To efficiently analyze and handle the ubiquitous cyber-physical-social big data, tensor is considered as an effective tool, but the curse of dimensionality is still the main bottleneck of tensor-based big data analysis. Tensor networks can considerably alleviate or overcome it through the tensor approximate theory. Therefore, this paper focuses on developing an efficient big data processing framework based on tensor networks and providing an incremental tensor train decomposition approach for the streaming big data. Concretely, this paper first presents a hierarchical cyber-physical-social big data processing framework composed of three planes, namely, data representation and decomposition, data storage and processing, and data analysis and service. Then, tensor train (TT) and quantized TT decompositions are particularly introduced to remarkably overcome the curse of dimensionality. Furthermore, to efficiently handle the continuous streaming big data and avoid the repeated decomposition for the history data, an incremental tensor train decomposition (ITTD) approach is proposed and the complexities are further analyzed in detail. Experimental results demonstrate that ITTD demonstrably outperforms the nonincremental TT decomposition in execution time on the precise of guaranteeing the nearly equal approximation error.


Index Terms-Cyber-physical-social systems, big data, ubiquitous computing, tensor decomposition, tensor network, incremental tensor train decomposition, quantized tensor train.

## 1 Introduction

WITH the continuous expansion of ubiquitous sensors, devices, and social media, all sorts of data generated from diverse spaces (e.g., cyber space, physical space, social space) become widely available and intertwined. Cyber-Physical-Social Systems (CPSS), an emerging paradigm, has received considerable attention in the academia and research community [1]. Differing from Cyber-Physical Systems (CPS) which merely focuses on connecting the physical world objects, CPSS integrates the information generated from not only cyber space and physical space, but also social space and further connects them into a large-scale, heterogeneous, federated, and complex system [2]. CPSS focuses on integrating and coordinating all data collected from these spaces to extract useful information and provide

[^0]intelligent services for our lives. CPSS has spurred intensive popularity and been applied to multiple realms, such as context-aware vehicular cyberphysical systems [3], NextMe prediction systems [4], personalized travel sequence recommendation systems [5], to name a few.

With the pervasiveness of data acquisition systems and the booming development of social networks and communication networks, the data acquired from CPSS become ubiquitous and very huge in amount. Cyber-physical-social big data are gradually formed and exhibit some specific characteristics, such as multi-source, heterogeneous, realtime streaming, etc. [6]. Due to the rapid increase in data scale, traditional ubiquitous computing methods show their limitations, and the data analysis methods in CPSS involving data representation, storage, and processing require new approaches. Meanwhile, the cyber-physical-social big data are generated in a streaming way, it is necessary to implement an incremental big data processing solution to satisfy the real-time requirements in real-world. Therefore, how to efficiently represent, analyze, and handle the streaming cyber-physical-social big data faces numerous challenges.

Tensor, a big data analysis tool, has been adopted in diverse branches because of its prominent advantages in representing and handling the complex and high-order data, such as economy, education, agriculture, transport, and military [7]. Some tensor-based data analysis methods, such as tensor decompositions, tensor networks, high-dimensional clustering, and multi-factor prediction, have also been utilized to deal with some practical problems. Especially for some large scale scenarios appeared in stochastic partial differential equations [8], brain data analysis [9], large-scale network anomography [10], etc., where the tensor's order
may reach up to 10,100 , even 1000 . However, curse of dimensionality is still the main bottleneck during the tensorbased big data analysis. The curse of dimensionality refers to the fact that the entry number of a tensor grows exponentially with its order [11]. Given a tensor with the order $N=50$ and the dimensionality $I=2$, then the number of entries is $I^{N}=2^{50}$. Suppose that each entry occupies 8 bytes in memory, then the volume of the tensor will reach up to $8 P B$. Accordingly, the additional overhead including the amount of operations, computations, and running memory grow exponentially with the tensor's order.

In order to tackle these problems arising from such huge volume, the theories of tensor approximation and multilinear algebra play important roles in numerical analysis and practical applications. Tensor approximation provide us an approach and opportunity to resolve these problems via a small number of parameters. Therefore, some typical tensor decompositions (TDs), such as, CANDECOMP-PARAFAC (CP) decomposition, Tucker decomposition, etc., are presented successively and widely used to analyze tensor data [12]. However, they appear discommodious because of their corresponding limitations when dealing with high-order tensors. For CP decomposition, the decomposition algorithms generally are not stable for high-order tensors and the computation of the optimal rank is considered as an NPhard problem [13]. For Tucker decomposition, although the decomposition algorithms are stable, it is also not suitable for high-order tensors in that the number of parameters of their core tensor is exponential with the tensor's order which remains suffered by the curse of dimensionality [14].

Recently, some tensor networks (TNs) are proposed to overcome these limitations when dealing with high-order tensors, such as Hierarchical Tucker (HT), Tensor Train (TT), Quantized Tensor Train (QTT) decompositions, etc. HT decomposition is considered as a multilevel variant of Tucker decomposition, it is also an alternative way to efficiently reduce the complexity of the Tucker decomposition [15], [16]. TT decomposition decomposes a high-order tensor into a series of low-order core tensors (typically 2nd-order or 3rdorder) which are interconnected by tensor contractions and connected to a linear train [11]. QTT decomposition decomposes a tensor with very high dimensionality as a series of sparsely interconnected low-order and low-dimensionality core tensors via synthetically exploiting quantization (or tensorization) and suitable TT decomposition [17].

TT, a special case of HT, is viewed as the simplest tensor network format and has some advantages. Firstly, TT decomposition is based on the low-rank approximation of auxiliary unfolding matrix, the decomposition algorithm is non-recursive and stable when facing high-order tensors. Secondly, the number of parameters of TT decomposition is asymptotically equal to that of CP decomposition [18], thus it does not suffer from the morass of curse of dimensionality. Thirdly, it is also feasible to directly implement some algebra operations in TT format, e.g., addition, dot product, norm, and matrix-by-vector, etc. [11], which are conducive to considerably improve the computation efficiency. Therefore, TT decomposition has already been successfully used to solve intractable high-dimensional problems in scientific computing, such as high-dimensional elliptic equations [19], extreme eigenvalues in higher dimensions [20], high-
dimensional operator equations and eigenvalue [21], etc.
Nevertheless, facing the large-scale and heterogeneous cyber-physical-social big data, there remains many challenges during the tensor-based big data analysis process in practical scenarios. Firstly, the constructed tensor for representing the cyber-physical-social big data is hardly stored and handled explicitly because of the curse of dimensionality. Hence, it is required to select a suitable tensor decomposition approach to tackle the problem. Secondly, after the cyber-physical-social tensor is decomposed, how to execute some tensor operations based on the decomposition results is also a challenge. These tensor operations are hardly performed based on the original tensor in that they can not be reconstructed because of the curse of dimensionality. Therefore, it is very meaningful to promote a novel processing framework to efficiently represent, store, analyze, and handle the cyber-physical-social big data.

Besides, the cyber-physical-social big data are dynamic and generally generated in a streaming way. After the new streaming data are appended to the original tensor, how to compute the TT format of the updated tensor faces challenges. Firstly, it is impossible to explicitly reconstruct the decomposed tensor and further achieve the updated tensor because of the curse of dimensionality. Secondly, it is necessary to reduce the total decomposition time to satisfy the time constraint in practical ubiquitous scenarios. In the past research, some incremental analysis methods are successively presented, e.g., SVD-based incremental approaches [22], [23] and incremental tensor-based approaches [24], [25]. However, there are little relevant methods for the incremental tensor train decomposition. Hence, it is significant that how to reuse the original TT decomposition result of the history tensor and compute the new TT format of the updated tensor.

Therefore, this paper focuses on developing an efficient big data processing framework based on tensor networks and providing an incremental tensor train decomposition approach for streaming big data. This paper first develops a novel processing framework for cyber-physical-social big data by exploiting the advantages of tensor networks to realize efficient storage and analysis. The proposed threelayer processing framework supports data representation and decomposition, data storage and processing, and data analysis and service. Afterwards, to avoid the repeated computation for the history data and compute the new TT format of the updated tensor, this paper provides an incremental TT decomposition (ITTD) approach. Finally, a series of experiments are conducted to validate the proposed framework and ITTD approach.

To summarize, the major contributions of this paper are listed as follows.

- Presents a cyber-physical-social big data processing framework based on tensor networks to considerably alleviate or overcome the curse of dimensionality by exploiting their characteristics and advantages in decomposition format and algebraic operation.
- Proposes an ITTD approach to avoid the repeated computation for the history data and reduce the total execution time of decomposition when facing the continuous streaming big data.
- Experimental results demonstrate that the proposed ITTD approach demonstrably outperforms the nonincremental TT decomposition in reducing the total execution time on the precise of guaranteeing the nearly equal approximation error and storage space.

The rest of this paper is organized as follows. Section 2 briefly recalls the related preliminaries. Section 3 presents a novel processing framework based on tensor networks for cyber-physical-social big data. In section 4, an incremental tensor train decomposition approach is proposed in detail. Section 5 compares the experimental results and section 6 concludes this paper.

## 2 Preliminaries and Tensor Train DecomPOSITION

Tensor, as the generalization of matrix in high-dimensional space, has been considered as an effective data representation and analysis tool for big data and has gained increasing attention by many researchers. For the concrete description about tensor operations, tensor decompositions, and tensor applications, please refer to [12], [18], [25], [26]. This section briefly reviews the presentation method based on tensor network diagram and tensor train decomposition.

### 2.1 Tensor Network Diagram

To visualize the corresponding operations and complex interactions between different tensors, tensor network diagram (TND) is graphically utilized [18]. Fig. 1 depicts the graphical representation of basic symbols and tensor operations through TND. In TND, there are generally two types of symbols, one is a series of nodes or shapers (e.g., squares, circles, spheres, ellipses, etc.) to graphically represent tensors, and the other is outgoing edges (or lines, branches, leads) emerging from one node to represent an order (or way, mode) of the tensor. The edges of TND are divided into two categories: connected edges which connect two nodes and represent a contraction of two respective tensors along the corresponding pair of orders, and free edges which connect to only one node and stand for a physical order of the tensor. Consequently, the order number of a tensor is decided by the number of free edges in TND. TND not only graphically provides illustrative large distributed networks but also performs complex tensor operations in an intuitive way and without using explicitly mathematical expressions.


Fig. 1. Graphical representation of basic symbols and operations via TND.

### 2.2 Tensor Train Decomposition

Tensor train decomposition (TTD) is firstly proposed by Oseledets in the numerical analysis community [11]. In practice, it is similar with the concept of "matrix product states" [27], [28]. Formally, an $N$ th-order tensor $\underline{X} \in \mathbb{R}^{I_{1} \times I_{2} \times \cdots \times I_{N}}$ is defined to be the TT-format if it satisfies the following format:

$$
\begin{equation*}
\underline{X}=\underline{X}^{(1)} \bullet \underline{X}^{(2)} \bullet \cdots \bullet \underline{X}^{(n)} \bullet \cdots \bullet \underline{X}^{(N)} \tag{1}
\end{equation*}
$$

where $\bullet$ denotes the operation of contracted product, $\underline{X}^{(n)} \in$ $\mathbb{R}^{r_{n-1} \times I_{n} \times r_{n}}\left(n=1, \cdots, N ; r_{0}=r_{N}=1\right)$ is referred to as core tensors (or cores), and $\left\{r_{0}, r_{1}, \cdots, r_{N}\right\}$ are viewed as TT-ranks of tensor. Fig. 2 illustrates the graphical representation of an $N$ th-order tensor in TT format. Alternatively, if the tensor is assigned to a specified index $\left(i_{1}, i_{2}, \cdots, i_{N}\right)$, then the entry-wise TT format can be represented as follows:

$$
\begin{equation*}
\underline{X}\left(i_{1}, i_{2}, \cdots, i_{N}\right)=\underline{X}^{(1)}\left(i_{1}\right) \underline{X}^{(2)}\left(i_{2}\right) \cdots \underline{X}^{(N)}\left(i_{N}\right) \tag{2}
\end{equation*}
$$

where $\underline{X}^{(n)}\left(i_{n}\right) \in \mathbb{R}^{r_{n-1} \times r_{n}}\left(n=1, \cdots, N ; r_{0}=r_{N}=1\right)$.


Fig. 2. Graphical representation of an $N$ th-order tensor in TT format.
According to [11], the implementation of TT decomposition is entirely based on low-rank decomposition through a series of SVDs of auxiliary unfolding matrices. The TT decomposition of an $N$ th-order tensor needs $N-1$ sequential SVDs. For each SVD on the auxiliary mode-( $1, \cdots, n$ ) unfolding matrix, the left singular matrix is transformed as the current core tensor, the remainder will be continuously executed by SVD in the same manner, until the last core tensor is yielded. Because the low-rank tensor rarely appears in practical computation, we should compress the original data tensor by exploiting the low-rank approximation theory. At this point the exact low-rank decomposition is modified to the approximate case. While executing the SVD for each given unfolding matrix $M_{k}$, if the singular values are truncated at $\delta$, i.e., $\left\|M_{k}-\widehat{M}_{k}\right\|_{F} \leq \delta\left\|M_{k}\right\|_{F}\left(\widehat{M}_{k}\right.$ is the reconstructed approximate matrix according to the truncated value), then the approximation error between the original $N$ th-order tensor $\underline{X}$ and the approximate tensor $\underline{\widehat{X}}$ will be $\varepsilon=\sqrt{N-1} \delta$, i.e., $\|\underline{X}-\underline{\widehat{X}}\|_{F} \leq \varepsilon\|\underline{X}\|_{F}$. For the proof, please refer to [11]. In other words, given any prescribed accuracy $\varepsilon$ for the TT decomposition, the threshold of truncation for each unfolding matrix should be set to $\delta=\frac{\varepsilon}{\sqrt{N-1}}$. In this case, the TT-ranks are actually $\delta$-ranks of the unfolding matrices.

Consequently, according to the above decomposition process and error estimation, the tensor train decomposition algorithm can be provided for any $N$ th-order tensor with any prescribed accuracy $\varepsilon$, whose pseudocode is described as Algorithm 1 [11].

## 3 Cyber-physical-social Big Data Processing Framework Based on Tensor Networks

In this section, a cyber-physical-social big data processing framework based on tensor networks is illustrated, the

```
Algorithm 1: Tensor Train Decomposition Algorithm
    Input: An \(N\) th-order tensor \(\underline{X} \in \mathbb{R}^{I_{1} \times I_{2} \times \cdots \times I_{N}}\) and a
    prescribed accuracy \(\varepsilon\).
    Output: TT cores \(\underline{X}^{(1)}, \underline{X}^{(2)}, \cdots, \underline{X}^{(N)}\) of
    approximation tensor \(\underline{\widehat{X}}\) satisfying
    \(\|\underline{X}-\underline{\hat{X}}\|_{F} \leq \varepsilon\|\underline{X}\|_{F}\).
    begin
        /* Initialize variables */
        \(\delta \leftarrow \frac{\varepsilon}{\sqrt{N-1}} ; T \leftarrow \underline{X} ; r_{0} \leftarrow 1 ;\)
        for \(n \leftarrow 1\) to \(N-1\) do
            /* Mode-[ \(n\) ] unfolding */
            \(M \leftarrow \operatorname{Unfolding-n(T,[r_{n-1}I_{n},\frac {\operatorname {numel}(M)}{r_{n-1}I_{n}}]);~}\)
            /* Compute \(\delta\)-truncated SVD */
            \([U, S, V] \leftarrow\) SVD \((M, \delta)\);
            i.e., \(M=U S V^{T}+E,\|E\|_{F} \leq \delta\|M\|_{F}\);
            /* Achieve new TT-rank and core */
            \(r_{n} \leftarrow \operatorname{rank}_{\delta}(M)\);
            \(\underline{X}^{(n)} \leftarrow \operatorname{Tensorization}\left(U,\left[r_{n-1}, I_{n}, r_{n}\right]\right)\);
            /* New temporary tensor */
            \(T \leftarrow S V^{T} ;\)
        /* Achieve last core and TT-rank */
        \(\underline{X}^{(N)} \leftarrow\) Tensorization \(\left(T,\left[r_{N-1}, I_{N}, r_{N}\right]\right)\);
        \(r_{N} \leftarrow 1\);
        return cores \(\underline{X}^{(1)}, \underline{X}^{(2)}, \cdots, \underline{X}^{(N)}\).
```

overview is depicted in Fig. 3. The hierarchical processing framework consists of three planes, namely, data representation and decomposition plane, data storage and processing plane, and data analysis and service plane. We elaborate the respective functions and responsibilities of each plane from a bottom-up view.


Fig. 3. Cyber-physical-social big data processing framework based on tensor networks.

### 3.1 Data Representation and Decomposition

Data representation and decomposition plane aims to represent the sensed and collected data from CPSS in a compact and efficient way. Ubiquitous cyber-physical-social big data derived from diverse spaces generally are multi-source and heterogeneous, hence, tensor is applied to this framework by virtue of its excellent characteristics in low-rank approximation and high-dimensional correlative analysis. Building on the previous work of our team in [25], the structured, semi-structured, and unstructured data with different types (such as text, image, audio, video, etc.) can be represented to low-order tensors. In general, the original constructed tensor not only takes up tremendous storage space, but also contains a great deal of noisy and redundant data. To reduce the storage space and extract the significant implicit features from the original tensor, tensor networks (TNs) can be applied under different practical situations, such as TT, QTT, HT, etc. The achieved data after implementing TNs generally become higher in quality and smaller in volume, which are conductive to the compact storage and accurate calculation, as well as the effective communication.

Besides, some data tensors derived from CPSS are characterised by very large dimensionality in some actual situations, such as very large-scale vectors or matrices. On that occasion, quantized tensor train (QTT) decomposition can be exploited. The implementation of QTT generally can be achieved from the following two steps. Firstly, we reformat the original tensor with high dimensionality to a higherorder tensor with low dimensionality via tensorization or quantization. Secondly, we apply a suitable TN (e.g., TT) to decompose the tensorized tensor. Fig. 4 provides two examples of QTT decomposition. Such a quantized tensor can achieve a good compression performance if it admits low-rank approximation [29]. Moreover, to deal with the streaming data and meet the requirements of response time in CPSS, some incremental updating methods should be exploited to avoid the repeated computation for the history data and reduce the computation time.


Fig. 4. Examples of quantized tensor-train decomposition.

### 3.2 Data Storage and Processing

Data storage and processing plane is responsible to realize the distributed data storage and parallel tensor computation based on the decomposed result through ubiquitous computing. Differing from the standard TDs (e.g., CP or HOSVD) which decompose a tensor to only one core tensor, TNs aim to decompose a high-order tensor into a sequence of low-order core tensors (typically 2nd-order or 3rd-order). These cores become relatively smaller in size and are interconnected by some specific contracted orders, such as the TT decomposition of an $N$ th-order tensor in Fig. 2.

TNs have different decomposed formats, but they can be reciprocally converted from one to the other [30]. This kind of decomposition approaches can considerably alleviate or overcome the adverse effects caused by the curse of dimensionality in computation and storage (please see the analysis in section 4.5). Therefore, these cores can be stored in data centers or clouds or ubiquitous computing devices in a distributed manner only if we grasp the relationship among cores according to their specific rules.

In addition, some TN formats have some special properties and support some algebraic and tensor-based operations, such as addition, minus, matrix-by-vector, and norm in TT format [11]. These operations can be directly computed in the same TN format without reconstructing these cores to the original tensors, and the result will also maintain the same TN format. Therefore, these original algebraic and other tensor-based operations can be converted to the operations among these distributed low-order cores, including typical tensor addition, Hadamard product, inner product, multilinear product, Einstein product, etc. They can be feasibly executed in parallel under ubiquitous computing environment. It is beneficial to further improves the efficiency of tensor networks based big data analysis.

### 3.3 Data Analysis and Service

Data analysis and service plane focuses on providing effective and proactive services for people based on the analysis results according to the practical ubiquitous intelligence scenarios. The core tensors after executing TNs generally contain enormous latent feature information. Based on these cores, several big data analysis methods, such as high-dimensional alternative clustering algorithms, multidimensional correlative analysis algorithms, personalized recommendation and accurate prediction algorithms, can be performed to mine valuable knowledge hidden in cyber-physical-social big data. Owing to the integration of heterogeneous data derived from multiple spaces and the multidimensional correlative analysis, their analysis results commonly are more targeted and accurate. These results are beneficial for providing excellent services for people according to ubiquitous intelligence scenarios and concrete application requirements, such as proactive healthcare services in smart monitor systems, accurate recommendations for learners in smart educational systems, and accurate prediction of traffic jams in smart transport systems, etc.

## 4 Incremental Tensor-Train DecomposiTION

Generally, the data in CPSS are generated in a streaming manner, to avoid the repeated computation for the history data, this section detailedly illustrates an incremental TT decomposition (ITTD) approach. The proposed incremental approach is performed by virtue of the special mathematical properties of TT format rather than the incremental SVD.

### 4.1 Main Solution

Suppose that the original tensor is $\underline{X} \in$ $\mathbb{R}^{I_{1} \times I_{2} \times \cdots \times I_{k} \times \cdots \times I_{N}}$, and the incremental tensor along the $k$ th order is $\underline{Y} \in \mathbb{R}^{I_{1} \times I_{2} \times \cdots \times I_{k}^{\prime} \times \cdots \times I_{N}}$, hence, the
updated tensor is $\underline{Z} \in \mathbb{R}^{I_{1} \times I_{2} \times \cdots \times\left(I_{k}+I_{k}^{\prime}\right) \times \cdots \times I_{N}}$. The main process of ITTD is described as Fig. 5, which is comprised of four steps. (1) Execute the TT decomposition of the incremental tensor $\underline{Y}$ and yield its TT format $\underline{Y}_{t t}$. It is noted that the TT format $\underline{X}_{t t}$ of original tensor $\underline{X}$ is known and has been stored in advance. (2) Compute their TT cores of the original and incremental zero-padding tensors on the basis of the decomposed TT cores $\underline{X}_{t t}$ and $\underline{Y}_{t t}$, represented as $\underline{X}_{t t}^{\prime}$ and $\underline{Y}_{t t}^{\prime}$, respectively. Thereinto, appending zeros to the original and incremental tensors is to maintain the consistency of their dimensionality. (3) Compute the TT format $\underline{Z}_{t t}^{\prime}$ of the updated tensor $\underline{Z}$ directly by exploiting the addition of their TT cores of the original and incremental zero-padding tensors. (4) Orthogonalize and compress the TT cores of $\underline{Z}_{t t}^{\prime}$, which can be periodically implemented in actual situations. That is because the new TT cores after executing the addition do not necessarily maintain the orthogonality. Hereto, the ultimate TT format $\underline{Z}_{t t}$ of updated tensor $\underline{Z}$ is yielded.


Fig. 5. Main process of the incremental tensor train decomposition.
For a more detailed explanation, Fig. 6 illustrates a specific example of the incremental tensor train decomposition. Given a 3rd-order tensor $\underline{X} \in \mathbb{R}^{2 \times 3 \times 3}$, suppose that the incremental tensor is $Y \in \overline{\mathbb{R}^{1} \times 3 \times 3}$, which will be appended to $\underline{X}$ along the first order. To compute the TT format of the updated tensor $X+Y$, we first compute the TT format of the incremental tensor $\underline{Y}$ and represent it as $\underline{Y}_{t t}$, and the TT format of the original tensor $\underline{X}$ can be directly achieved from the historical results and depicted as $\underline{X}_{t t}$. Secondly, we append zeros to the original and incremental tensors to maintain the consistency of their dimensionality, and compute the TT format ( $\underline{X}_{t t}^{\prime}$ and $\underline{Y}_{t t}^{\prime}$ ) of the zero-padding tensors on the basis of the existing TT cores $\left(\underline{X}_{t t}\right.$ and $\left.\underline{Y}_{t t}\right)$. Thirdly, we directly compute the sum of two zero-padding tensors in TT format. Finally, we orthogonalize the TT cores of the updated tensor to maintain their orthogonality and compress them according to the prescribed accuracy.

### 4.2 TT Format of Zero-padding Tensor

Suppose that the TT format of a given tensor is known, if we append enough zeros to the given tensor along a specified order, how can we compute the new TT format of the zero-padding tensor based on the known TT cores of the given tensor? This section is to provide an effective solution to compute the TT format for the zero-padding tensor.


Fig. 6. Example of the incremental tensor train decomposition for a 3rdorder tensor along the first order.

Before providing the computation approach, two theorems are illustrated and proved in detail, they are extended and improved on the basis of the conclusion in [22].

Theorem 1. Given a matrix $M_{1} \in \mathbb{R}^{m \times n}$, and a matrix $M_{2} \in \mathbb{R}^{(m+\Delta m) \times n}$ which is constructed by appending zeros on the bottom rows of $M_{1}$, i.e., $M_{2}=\left[\begin{array}{c}M_{1} \\ 0\end{array}\right]$. Supposed that the SVDs of $M_{1}$ and $M_{2}$ are represented as $M_{1}=U_{1} S_{1} V_{1}^{T}$, $M_{2}=U_{2} S_{2} V_{2}^{T}$. If the same $\delta$-truncations are applied in the SVDs of $M_{1}$ and $M_{2}$, and their $\delta$-ranks are $r_{1}$ and $r_{2}$, then $r_{2}=r_{1}, U_{2}^{r_{2}}=\left[\begin{array}{c}U_{1}^{r_{1}} \\ 0\end{array}\right]$, and $V_{2}^{r_{2}}=V_{1}^{r_{1}}$.
Proof. According to the properties of SVD, we can know that $U_{1}$, $V_{1}, U_{2}$, and $V_{2}$ are orthogonal matrices, $S_{1}$ and $S_{2}$ are diagonal matrices. Therefore,

$$
\begin{align*}
& M_{1}^{T} M_{1}=\left(V_{1} S_{1}^{T} U_{1}^{T}\right)\left(U_{1} S_{1} V_{1}^{T}\right)=V_{1} S_{1}^{2} V_{1}^{T}  \tag{3}\\
& M_{2}^{T} M_{2}=\left(V_{2} S_{2}^{T} U_{2}^{T}\right)\left(U_{2} S_{2} V_{2}^{T}\right)=V_{2} S_{2}^{2} V_{2}^{T} \tag{4}
\end{align*}
$$

Consider $M_{2}^{T} M_{2}=\left[\begin{array}{ll}M_{1}^{T} & 0\end{array}\right]\left[\begin{array}{c}M_{1} \\ 0\end{array}\right]=M_{1}^{T} M_{1}$, by integrating Eq. 3 and Eq. 4, we can obtain $V_{1} S_{1}^{2} V_{1}^{T}=V_{2} S_{2}^{2} V_{2}^{T}$. It is noted that $S_{1}^{2}$ and $S_{2}^{2}$ are the eigenvalue matrices of the two equal symmetric matrices $M_{1}^{T} M_{1}$ and $M_{2}^{T} M_{2}$, respectively. According to the uniqueness of eigenvalues, it can be inferred that $S_{1}^{2}$ and $S_{2}^{2}$ are equal, thus their corresponding singular values of $M_{1}$ and $M_{2}$ are also equal. Accordingly, it can be easily concluded that $V_{1}$ and $V_{2}$ are equivalent, i.e., $V_{2}=V_{1}$. If the same $\delta$ truncation are applied in the SVDs of $M_{1}$ and $M_{2}$, then their $\delta$-ranks shall be equal, i.e., $r_{2}=r_{1}$. Furthermore, we can infer that $V_{2}^{r_{2}}=V_{1}^{r_{1}}$.

Besides, suppose that $U_{2}=\left[\begin{array}{c}U_{1} \\ 0\end{array}\right]$, according to the above analysis, we can know that $S_{1}$ and $S_{2}$ are equal, $V_{1}$ and $V_{2}$ are equivalent. Therefore, $M_{2}=U_{2} S_{2} V_{2}^{T}=\left[\begin{array}{c}U_{1} \\ 0\end{array}\right] S_{2} V_{2}^{T}=$ $\left[\begin{array}{c}U_{1} S_{2} V_{2}^{T} \\ 0\end{array}\right]=\left[\begin{array}{c}U_{1} S_{1} V_{1}^{T} \\ 0\end{array}\right]=\left[\begin{array}{c}M_{1} \\ 0\end{array}\right]$. It shows that the assumption is correct. Therefore, we can further obtain that $U_{2}^{r_{2}}=\left[\begin{array}{c}U_{1}^{r_{1}} \\ 0\end{array}\right]$.

Theorem 2. Given a matrix $N_{1}=\left[v_{1}, v_{2}, \cdots, v_{n}\right] \in \mathbb{R}^{m \times n}$, and let $N_{2}=\left[v_{1}, 0, v_{2}, \cdots, 0, \cdots, 0, \cdots, v_{n}\right] \in \mathbb{R}^{m \times(n+\Delta n)}$
in which $\Delta n$ zero column vectors are inserted to $N_{1}$. Suppose that the SVDs of $N_{1}$ and $N_{2}$ are expressed as $N_{1}=P_{1} S_{1} Q_{1}^{T}, N_{2}=$ $P_{2} S_{2} Q_{2}^{T}$. If the same $\delta$-truncations are applied in the SVDs of $N_{1}$ and $N_{2}$, and their $\delta$-ranks are $r_{1}$ and $r_{2}$, then $r_{2}=r_{1}$, $P_{2}^{r_{2}}=P_{1}^{r_{1}}$, and $Q_{2}^{r_{2}}=\left[\begin{array}{c}Q_{1}^{r_{1}} \\ 0\end{array}\right]$.
Proof. Clearly, by employing some elementary matrix transformations, $N_{2}=\left[v_{1}, 0, v_{2}, \cdots, 0, \cdots, 0, \cdots, v_{n}\right]$ can be transformed to $N_{2}=\left[v_{1}, v_{2}, \cdots, v_{n}, 0, \cdots, 0\right]=\left[N_{1}, 0\right]$. If we transpose the matrix $M_{2}$ in Theorem 1, then $M_{2}^{T}=\left[M_{1}^{T}, 0\right]$. We can see that their forms of the two equations are identical. Therefore, according to the conclusion in Theorem 1, it can be easily infer that $r_{2}=r_{1}, P_{2}^{r_{2}}=P_{1}^{r_{1}}, Q_{2}^{r_{2}}=\left[\begin{array}{c}Q_{1}^{r_{1}} \\ 0\end{array}\right]$.

Suppose that there is a given $N$ th-order tensor $\underline{X} \in \mathbb{R}^{I_{1} \times I_{2} \times \cdots \times I_{k} \times \cdots \times I_{N}}$. If we append zeros to $\underline{X}$ along the $k$ th order, the zero-padding tensor can be generated and represented as $\underline{X}^{\prime} \in \mathbb{R}^{I_{1} \times I_{2} \times \cdots \times\left(I_{k}+I_{k}^{\prime}\right) \times \cdots \times I_{N}}$. $\underline{X}^{\prime}$ contains the given tensor (i.e., subtensor $\left.\underline{X}^{\prime}\left(:,:, \cdots,\left(1: I_{k}\right), \cdots,:\right)\right)$ and the appended zero tensor (i.e., subtensor $\left.\underline{X}^{\prime}\left(:,:, \cdots,\left(I_{k}+1: I_{k}+I_{k}^{\prime}\right), \cdots,:\right)\right)$, which is exhibited in Fig. 6. Suppose that the given tensor has already been decomposed to TT format and stored in advance, which is described as follows:

$$
\begin{align*}
& \underline{X}\left(i_{1}, i_{2}, \cdots, i_{k}, \cdots, i_{N}\right) \\
& =\underline{X}^{(1)}\left(i_{1}\right) \underline{X}^{(2)}\left(i_{2}\right) \cdots \underline{X}^{(k)}\left(i_{k}\right) \cdots \underline{X}^{(N)}\left(i_{N}\right) \tag{5}
\end{align*}
$$

where $\underline{X}^{(n)}\left(i_{n}\right) \in \mathbb{R}^{r_{n-1} \times r_{n}}\left(n=1,2, \cdots, N ; r_{0}=r_{N}=1\right)$.
To achieve the TT format of zero-padding tensor from the existing TT format of the given tensor, we illustrate the TT decomposition process via tensor network diagram. Fig. 7 graphically demonstrates the TT decomposition process of an $N$ th-order tensor, where $N-1$ SVDs are executed successively. For illustrating the decomposition process in more depth, the implementation details of the second step (i.e., step (2) in Fig. 7) are interpreted as follows. Firstly, we unfold the tensor derived from the previous step (i.e., box (T1) in Fig. 7) to a matrix (i.e., box (M) in Fig. 7) according to mode- $(1, \cdots, n)$ unfolding operation. Then SVD ( $M=$ $U S V^{T}$ ) is executed on the unfolding matrix, the left singular matrix (i.e., box $(\mathrm{U})$ in Fig. 7) is reshaped to the current core tensor $\underline{X}^{(2)}$, and the remaining section (i.e., box $\left(\mathrm{SV}^{T}\right)$ in Fig. 7) will be viewed as the input of next step (i.e., box (T2) in Fig. 7) and decomposed continuously.

Therefore, while computing the first $k-1$ TT cores of the zero-padding tensor $\underline{X}^{\prime}$, the $k$ th order is always located in the column of each unfolding matrix. After unfolding tensors $\underline{X}$ and $\underline{X}^{\prime}$ along the first order, we can find that the unfolding matrix $M_{1}^{\prime}$ generated from tensor $\underline{X}^{\prime}$ can be yielded by inserting some zero column vectors in the unfolding matrix $M_{1}$ generated from tensor $\underline{X}$, namely, $M_{1}^{\prime}=\left[\begin{array}{ll}M_{1} & 0\end{array}\right]$, which is illustrated in Fig. 8(a). If we decompose the two matrices through $\delta$-SVD and express them as $M_{1}^{\prime} \approx U_{1}^{r_{1}^{\prime}} S_{1}^{r_{1}^{\prime}} V_{1}^{r_{1}^{\prime} T}$ and $M_{1} \approx U_{1}^{r_{1}} S_{1}^{r_{1}} V_{1}^{r_{1} T}$. According to Theorem 2, it can be inferred that their left singular matrices are equivalent, i.e., $U_{1}^{r_{1}{ }^{\prime}}=U_{1}^{r_{1}}$, and their right singular matrices shall satisfy: $V_{1}^{r_{1}{ }^{\prime}}=\left[\begin{array}{c}V_{1}^{r_{1}} \\ 0\end{array}\right]$. In accordance with the foregoing analysis in Fig. 7, the left singular matrix


Fig. 7. Illustration of tensor train decomposition for an $N t h$-order tensor.
will be transformed as the first core tensor, it means that the first TT cores of tensors $\underline{X}$ and $\underline{X}^{\prime}$ are equal. And the remaining section $S_{1}^{r_{1}{ }^{\prime}} V_{1}^{r_{1}{ }^{\prime} T}=\left[\begin{array}{ll}S_{1}^{r_{1}} V_{1}^{r_{1} T} & 0\end{array}\right]$ will be transformed to the input matrix of the second step, and its form is same with the first unfolding matrix $M_{1}^{\prime}$. Similarly, the second input matrix will be decomposed in the same manner until the $(k-1)$ th core tensor is yielded. Therefore, we can conclude that the first $k-1$ TT cores of tensor $\underline{X}$ and $\underline{X}$ are identical.


Fig. 8. Illustration of tensor unfolding for $\underline{X}$ and $\underline{X}^{\prime}$.
While computing the $k$ th core of the zero-padding tensor $\underline{X}^{\prime}$, the $k$ th order is just located in the row of the unfolding matrix $M_{k}^{\prime}$, and the bottom rows of $M_{k}^{\prime}$ are all zeros, i.e., $M_{k}^{\prime}=\left[\begin{array}{c}M_{k} \\ 0\end{array}\right]$, which is illustrated in Fig. 8(b). Then we execute $\delta$-SVD for $M_{k}^{\prime}$ and $M_{k}$ and express them as $M_{k}^{\prime} \approx U_{k}^{r_{k}{ }^{\prime}} S_{k}^{r_{k}{ }^{\prime}} V_{k}^{r_{k}{ }^{\prime} T}$ and $M_{k} \approx U_{k}^{r_{k}} S_{k}^{r_{k}} V_{k}^{r_{k} T}$. According to Theorem 1, it can be seen that $U_{k}^{r_{k}{ }^{\prime}}=\left[\begin{array}{c}U_{k}^{r_{k}} \\ 0\end{array}\right]$ and will be transformed as the $k$ th TT core. Consequently, we can achieve the $k$ th TT core of the zero-padding tensor $\underline{X}^{\prime}$ by appending zeros to the existing $k$ th TT core of the given tensor $\underline{X}$ according to corresponding transformation rules. Besides, their right singular matrices $V_{k}^{\prime}$ and $V_{k}$ are equivalent in accordance with the conclusion in Theorem 1. It illustrates that the following unfolding matrices maintain
unchanged and the corresponding TT cores from $k+1$ to $N$ accordingly are the same.

Therefore, all TT cores of the zero-padding tensor $\underline{X}^{\prime}$ can be achieved from the TT cores of the given tensor $\underline{X}$ without repeatedly recomputing the TT decomposition. According to aforementioned analysis, we can find that all TT cores remain unchanged except for the $k$ th core. If the TT format of the zero-padding tensor $\underline{X}^{\prime}$ is represented as follows:

$$
\begin{align*}
& \underline{X}^{\prime}\left(i_{1}, i_{2}, \cdots, i_{n} \cdots, i_{N}\right) \\
& =\underline{X}^{\prime}(1)\left(i_{1}\right) \underline{X}^{\prime(2)}\left(i_{2}\right) \cdots \underline{X}^{\prime(n)}\left(i_{n}\right) \cdots \underline{X}^{\prime(N)}\left(i_{N}\right) \tag{6}
\end{align*}
$$

where $\underline{X}^{\prime}(n)\left(i_{n}\right) \in \mathbb{R}^{r_{n-1}^{\prime} \times r_{n}^{\prime}}(n=1,2, \cdots, N)$. Then the rules of yielding all new TT cores $\underline{X}^{\prime(n)}\left(i_{n}\right)(n=$ $1,2, \cdots, N)$ of the zero-padding tensor $\underline{X}^{\prime}$ are as follows:

$$
\underline{X}^{\prime}(n)\left(i_{n}\right)=\left\{\begin{array}{cc}
0, & n=k \text { and } i_{n}>I_{k}  \tag{7}\\
\underline{X}^{(n)}\left(i_{n}\right), & \text { otherwise } .
\end{array}\right.
$$

Meanwhile, it can be inferred that the TT ranks remain unchanged after appending zeros to the matrix according to Theorems 1 and 2.

$$
\begin{equation*}
r_{n}^{\prime}=r_{n}, n=1,2, \cdots, N \tag{8}
\end{equation*}
$$

### 4.3 Addition of Two Tensors in TT Format

According to the approach provided in section 4.2, we can compute the TT format of the original and incremental zero-padding tensors. Then our current objective is how to compute the TT format of the updated tensor based on the TT formats of the original and incremental zeropadding tensors. Therefore, our solution is to directly merge their corresponding TT cores by exploiting the addition characteristic of TT format.

According to Eq. 7, the TT format of the original zeropadding tensor and incremental zero-padding tensor can be yielded and represented as follows:

$$
\begin{equation*}
\underline{X}^{\prime}\left(i_{1}, i_{2}, \cdots, i_{N}\right)=\underline{X}^{\prime(1)}\left(i_{1}\right) \underline{X}^{\prime(2)}\left(i_{2}\right) \cdots \underline{X}^{\prime}(N)\left(i_{N}\right) \tag{9}
\end{equation*}
$$

where $X^{\prime}(n)\left(i_{n}\right) \in \mathbb{R}^{r_{n-1} \times r_{n}}\left(n=1, \cdots, N ; r_{0}=r_{N}=1\right)$.

$$
\begin{equation*}
\underline{Y}^{\prime}\left(i_{1}, i_{2}, \cdots, i_{N}\right)=\underline{Y}^{\prime(1)}\left(i_{1}\right) \underline{Y}^{\prime(2)}\left(i_{2}\right) \cdots \underline{Y}^{\prime(N)}\left(i_{N}\right) \tag{10}
\end{equation*}
$$

where $Y^{\prime}(n)\left(i_{n}\right) \in \mathbb{R}^{s_{n-1} \times s_{n}}\left(n=1, \cdots, N ; s_{0}=s_{N}=1\right)$.
Suppose that the TT format of the updated tensor is represented as:

$$
\begin{equation*}
\underline{Z}^{\prime}\left(i_{1}, i_{2}, \cdots, i_{N}\right)=\underline{Z}^{\prime}(1)\left(i_{1}\right) \underline{Z}^{\prime(2)}\left(i_{2}\right) \cdots \underline{Z}^{(N)}\left(i_{N}\right) \tag{11}
\end{equation*}
$$

where $Z^{\prime(n)}\left(i_{n}\right) \in \mathbb{R}^{t_{n-1} \times t_{n}}(n=1, \cdots, N)$. Then the computation of the TT format of the updated tensor can be converted to the merge of original TT cores and incremental TT cores. According to the addition operation provided in [11], the generation rule of updated TT cores is defined as:

$$
\underline{Z}^{\prime(n)}\left(i_{n}\right)=\left\{\begin{array}{l}
\left(\underline{X}^{\prime(1)}\left(i_{1}\right) \underline{Y}^{\prime}(1)\left(i_{1}\right)\right), n=1  \tag{12}\\
\left(\begin{array}{cc}
\underline{X}^{\prime(n)}\left(i_{n}\right) & 0 \\
0 & \underline{Y}^{\prime}(n) \\
\left(i_{n}\right)
\end{array}\right), n=2, \cdots, N-1 \\
\left(\begin{array}{l}
\underline{X}^{\prime}(N) \\
\underline{Y}^{\prime}\left(i_{N}\right) \\
\underline{S}^{(N)}\left(i_{N}\right)
\end{array}\right), n=N
\end{array}\right.
$$

Actually, by substituting Eq. 12 into Eq. 11 and exploiting continuous matrix multiplication, we can obtain:

$$
\begin{align*}
& \underline{Z}^{\prime}\left(i_{1}, i_{2}, \cdots, i_{N}\right)=\underline{Z}^{\prime(1)}\left(i_{1}\right) \underline{Z}^{\prime(2)}\left(i_{2}\right) \cdots \underline{Z}^{\prime(N)}\left(i_{N}\right) \\
& =\left(\underline{X}^{\prime(1)}\left(i_{1}\right) \quad \underline{Y}^{\prime(1)}\left(i_{1}\right)\right)\left(\begin{array}{cc}
\underline{X}^{\prime} \\
& (2) \\
0 & \left.\underline{Y}_{2}\right) \\
\underline{Y}^{\prime}(2) & \left(i_{2}\right)
\end{array}\right) \cdots \\
& \cdots\left(\begin{array}{cc}
\underline{X}^{\prime}{ }^{(N-1)}\left(i_{N-1}\right) & 0 \\
0 & \underline{Y}^{\prime(N-1)}\left(i_{N-1}\right)
\end{array}\right)\left(\begin{array}{l}
\underline{X}^{\prime(N)}\left(i_{N}\right) \\
\underline{Y}^{\prime}(N) \\
\left(i_{N}\right)
\end{array}\right) \\
& =\underline{X}^{\prime(1)}\left(i_{1}\right) \underline{X}^{\prime(2)}\left(i_{2}\right) \cdots \underline{X}^{\prime(N-1)}\left(i_{N-1}\right) \underline{X}^{\prime(N)}\left(i_{N}\right) \\
& +\underline{Y}^{\prime}{ }^{(1)}\left(i_{1}\right) \underline{Y}^{\prime(2)}\left(i_{2}\right) \cdots \underline{Y}^{\prime(N-1)}\left(i_{N-1}\right) \underline{Y}^{\prime(N)}\left(i_{N}\right) \\
& =\underline{X}^{\prime}\left(i_{1}, i_{2}, \cdots, i_{N}\right)+\underline{Y}^{\prime}\left(i_{1}, i_{2}, \cdots, i_{N}\right) \text {. } \tag{13}
\end{align*}
$$

From Eq. 13, it can be seen that any entry of tensor $\underline{Z}$ is the sum of the corresponding entries of tensor $\underline{X}^{\prime}$ and $\underline{Y}^{\prime}$. Besides, it can be inferred from Eq. 12 that the TT ranks of the updated tensor $\underline{Z}^{\prime}$ are the sum of the corresponding TT ranks of the original and incremental zero-padding tensors $\underline{X}^{\prime}$ and $\underline{Y}^{\prime}$, which is illustrated as follows:

$$
t_{n}=\left\{\begin{array}{l}
1, n=1 \text { or } N  \tag{14}\\
r_{n}+s_{n}, n=2,3, \cdots, N-1
\end{array}\right.
$$

### 4.4 Orthogonalization and Compression of Updated TT Cores

The TT format of the updated tensor can be efficiently computed through the aforementioned approach. Nonetheless, some new TT cores after executing the addition of two tensors in Eq. 12 do not necessarily satisfy the orthogonality. Meanwhile, their TT ranks may also be increased. Therefore, to maintain the orthogonalization of updated TT cores and avoid the growth of TT ranks while satisfying the prescribed accuracy, the orthogonalization and compression of updated TT cores can be selectively implemented. In practice, to save the execution time, it is not necessary to execute the orthogonalization and compression operation after every updating. Generally, the process should be executed in the following situations. (1) If the update times reach up to a setting threshold, it can be periodically executed offline. (2) If the storage space of cores reaches up to a warning value. (3) If the requirement of orthogonalization is required before some special operations.

Algorithm 2 gives the pseudocode of orthogonalizing and compressing the updated TT cores [11]. Lines $4-12$ are to orthogonalize TT cores whose size are $r_{n-1} \times I_{n} r_{n}$ through QR decomposition from right to left, where $Q$ is orthogonal and will be transformed to the new orthogonal TT core and $R$ is triangle matrix and will be passed to the left TT core. Lines $13-22$ are responsible for compressing TT cores whose size is $r_{n-1} I_{n} \times r_{n}$ through SVD operations from left to right while maintaining the prescribed accuracy.

Therefore, according to the solution in section 4.1, the ITTD algorithm can be realized according to the proposed approach, the pseudocode is illustrated in algorithm 3.

### 4.5 Complexity Analysis

### 4.5.1 Time Complexity Analysis

The execution time of the proposed ITTD approach consists of TT decomposition of incremental tensor, computation of new TT format for zero-padding tensors, addition of two

```
Algorithm 2: Algorithm for the Orthogonalization and
Compression of Updated TT Cores
    Input: TT cores \(\underline{Z}^{(1)}, \underline{Z}^{(2)}, \cdots, \underline{Z}^{(N)}\) of an \(N\) th-order
    updated tensor \(\underline{Z}\) and a prescribed accuracy \(\varepsilon\).
    Output: Orthogonal TT cores \(\underline{Z}^{\prime(1)}, \underline{Z}^{\prime(2)}, \cdots, \underline{Z}^{\prime(N)}\)
    of the approximate tensor \(\underline{Z}^{\prime}\). The computed
    approximation satisfies \(\left\|\underline{Z}-\underline{Z^{\prime}}\right\|_{F} \leq \varepsilon\|\underline{Z}\|_{F}\).
    begin
        /* Compute truncation parameters */
        \(\delta \leftarrow \frac{\varepsilon}{\sqrt{N-1}} ;\)
        /* Right-to-left orthogonalization */
        for \(n \leftarrow N\) to 2 do
            /* Reshape the current core */
            \(M_{n} \leftarrow \operatorname{Reshape}\left(\underline{Z}^{(n)},\left[r_{n-1}, I_{n} * r_{n}\right]\right)\);
            /* Execute QR operation */
            \([Q, R] \leftarrow \mathbf{Q R}\left(M_{n}\right)\);
                \(\underline{Z}^{(n)} \leftarrow \operatorname{Reshape}\left(Q,\left[r_{n-1}^{\prime}, I_{n}, r_{n}\right]\right)\);
                /* Change next core tensor */
                \(\underline{Z}^{(n-1)} \leftarrow \underline{Z}^{(n-1)} \times{ }_{3} R ;\)
        if \(\varepsilon \neq 0\) then
            /* Left-to-right compression */
            for \(n \leftarrow 1\) to \(N-1\) do
                /* Reshape the current core */
                \(M_{n} \leftarrow \operatorname{Reshape}\left(\underline{Z}^{(n)},\left[r_{n-1} * I_{n}, r_{n}\right]\right)\);
                    /* Execute SVD operation */
                    \([U, S, V] \leftarrow \operatorname{SVD}(M, \delta)\);
                \(\underline{Z}^{\prime}{ }^{(n)} \leftarrow \operatorname{Reshape}\left(U,\left[r_{n-1}, I_{n}, r_{n}^{\prime}\right]\right)\);
                /* Change next core tensor */
                \(\underline{Z}^{(n+1)} \leftarrow \underline{Z}^{(n+1)} \times{ }_{1} S V^{T}\);
            return cores \(\underline{Z}^{\prime(1)}, \underline{Z}^{\prime(2)}, \cdots, \underline{Z}^{\prime}(N)\).
```

tensors in TT format, and orthogonalization and compression of TT cores. Let $T_{i t t}, T_{\text {ttzero }}, T_{\text {ttadd, }}$, and $T_{\text {ttorth }}$ denote the execution time consumed by each section. Therefore, the total time consumption $T_{I T T D}$ can be expressed as:

$$
\begin{equation*}
T_{I T T D}=T_{i t t}+T_{t t z e r o}+T_{t t a d d}+T_{t t o r t h} \tag{15}
\end{equation*}
$$

Given an $N$ th-order tensor $\underline{X} \in \mathbb{R}^{I_{1} \times I_{2} \times \cdots \times I_{N}}$, for simplicity, we assume that $I=\max \left\{I_{n}\right\}(n=1, \cdots, N)$ and $r=\left\{r_{0}, r_{1}, \cdots, r_{N}\right\}$, where $r_{n}$ is the TT rank of the tensor. If the incremental dimensionality along the specific order is $I^{\prime}$ (in general, $I^{\prime}<I$ ), then the maximal dimensionality of the original, incremental, and updated tensors are $I, I$, and $I+I^{\prime}$, respectively.

To compute the TT decomposition of an incremental tensor, $N-1$ successive SVDs need to be executed and $\operatorname{cost} \mathcal{O}\left(I^{N+1}\right) \sim \mathcal{O}\left(I^{\frac{N}{2}+2} r_{N / 2-1}^{2}\right)$ operations. The analysis process can be illustrated as follows:

Consider the first $N / 2$ SVDs, we can calculate the consumed time according to the size of unfolding matrices.

$$
\begin{aligned}
& n=1, I * I^{N-1}, \mathcal{O}\left(I^{N-1} * I^{2}\right)=\mathcal{O}\left(I^{N+1}\right) \\
& n=2, I r_{1} * I^{N-2}, \mathcal{O}\left(I^{N-2} *\left(I r_{1}\right)^{2}\right)=\mathcal{O}\left(I^{N} r_{1}{ }^{2}\right) \\
& n=3, I r_{2} * I^{N-3}, \mathcal{O}\left(I^{N-3} *\left(I r_{2}\right)^{2}\right)=\mathcal{O}\left(I^{N-1} r_{2}{ }^{2}\right)
\end{aligned}
$$

```
Algorithm 3: The Process of ITTD Algorithm
    Input: The TT format \(\underline{X}_{t t}\) of the original \(N\) th-order
    tensor \(\underline{X} \in \mathbb{R}^{I_{1} \times I_{2} \times \cdots \times I_{k} \times \cdots \times I_{N}}\), an incremental
    tensor \(\underline{Y} \in \mathbb{R}^{I_{1} \times I_{2} \times \cdots \times I_{k}^{\prime} \times \cdots \times I_{N}}\) and a prescribed
    accuracy \(\varepsilon\). Output: TT cores \(\underline{Z}^{(1)}, \underline{Z}^{(2)}, \cdots, \underline{Z}^{(N)}\) of
    the updated tensor \(\underline{Z} \in \mathbb{R}^{I_{1} \times I_{2} \times \cdots \times\left(I_{k}+I_{k}^{\prime}\right) \times \cdots \times I_{N}}\) in
    TT format.
    begin
        /* Compute the TT format of tensor \(\underline{Y}\)
            according to algorithm \(1 \quad\) */
        \(\underline{Y}_{t t}\left(\underline{Y}^{(1)}, \underline{Y}^{(2)}, \cdots, \underline{Y}^{(N)}\right) \leftarrow \mathbf{T T}(\underline{Y}, \varepsilon) ;\)
        /* Compute TT format of zero-padding
            tensors according to Eq. 7 */
        \(\underline{X}_{t t}^{\prime}\left(\underline{X}^{\prime}(1), \underline{X}^{\prime}{ }^{(2)}, \cdots, \underline{X}^{\prime}{ }^{(N)}\right) \leftarrow\) TT_zeros \(^{\left(\underline{X}_{t t}, k\right) ; ~}\)
        \(\underline{Y}_{t t}^{\prime}\left(\underline{Y}^{\prime}(1), \underline{Y}^{\prime}(2), \cdots, \underline{Y}^{\prime}(N)\right) \leftarrow \mathbf{T T}_{-} \mathbf{z e r o s}\left(\underline{Y}_{t t}, k\right)\);
        /* Execute addition of two tensors in
            TT format according to Eq. 12 */
        \(\underline{Z}_{t t}^{\prime}\left(\underline{Z}^{\prime(1)}, \underline{Z}^{\prime(2)}, \cdots, \underline{Z}^{\prime(N)}\right) \leftarrow \mathbf{A d d} \_\mathbf{T T}\left(\underline{X}_{t t}^{\prime}, \underline{Y}_{t t}^{\prime}\right) ;\)
        if the requirement of orthogonalization and
        compression is satisfied then
            /* Orthogonalize and compress the
                TT cores of updated tensor
                acoording to algorithm 2 */
            \(\underline{Z}^{(1)}, \underline{Z}^{(2)}, \cdots, \underline{Z}^{(N)} \leftarrow \mathbf{O r t h} \_\mathbf{T T}\left(\underline{Z}_{t t}^{\prime}, \varepsilon\right) ;\)
        return TT cores \(\underline{Z}^{(1)}, \underline{Z}^{(2)}, \cdots, \underline{Z}^{(N)}\).
```

$$
\begin{aligned}
& n=\frac{N}{2}, \operatorname{Ir}_{N / 2-1} * I^{\frac{N}{2}}, \mathcal{O}\left(I^{\frac{N}{2}} *\left(\operatorname{Ir}_{N / 2-1}\right)^{2}\right) \\
& =\mathcal{O}\left(I^{\frac{N}{2}+2} r_{N / 2-1}{ }^{2}\right)
\end{aligned}
$$

It can be inferred that the total decomposition time is less than twice the sum of the time consumed by the first $N / 2$ SVDs. Therefore, $T_{i t t}=\mathcal{O}\left(2 *\left(I^{N+1}+I^{N} r_{1}{ }^{2}+\right.\right.$ $\left.I^{N-1} r_{2}{ }^{2}+\cdots+I^{\frac{N}{2}+2} r_{N / 2-1}^{2}\right)$ ). If $r_{1} \leq \sqrt{I}, r_{2} \leq \sqrt{I} r_{1}, r_{3} \leq$ $\sqrt{I} r_{4}, \cdots, r_{N / 2-1} \leq \sqrt{I} r_{N / 2-2}$, then $T_{i t t}=\mathcal{O}\left(I^{N+1}\right)$; and if $r_{1} \geq \sqrt{I}, r_{2} \geq \sqrt{I} r_{1}, r_{3} \geq \sqrt{I} r_{4}, \cdots, r_{N / 2-1} \geq$ $\sqrt{I} r_{N / 2-2}$, then $T_{i t t}=\mathcal{O}\left(I^{\frac{N}{2}+2} r_{N / 2-1}{ }^{2}\right)$.

If all unfolding matrices are full rank and no truncation is applied to all SVDs, then the worst time complexity of TT decomposition will be generated and can be represented as $T_{i t t}=\mathcal{O}\left(2 *\left(I^{N+1}+I^{N+2}+I^{N+3}+\cdots+I^{\frac{3 N}{2}}\right)\right)=\mathcal{O}\left(I^{\frac{3 N}{2}}\right)$. Suppose that $r=\max \left\{r_{n}\right\}(n=0,1, \cdots, N)$, if $r \geq I^{\frac{N-2}{4}}$, the time complexity is $T_{i t t}=\mathcal{O}\left(I^{\frac{N}{2}+2} r^{2}\right)$, and if $r$ increases to $I^{\frac{N}{2}-1}$, it will reach up to the worst time complexity $\mathcal{O}\left(I^{\frac{3 N}{2}}\right)$.

According to Eq. 7, the computation of the new TT format for the zero-padding tensor need only to change the $k$ th TT core, which just costs $T_{\text {ttzero }}=\mathcal{O}\left(I r^{2}\right)$ operations. According to Eq. 12, the addition of two tensors in TT format costs $T_{\text {ttadd }}=\mathcal{O}\left(N I r^{2}\right)$ operations. In the process of orthogonalization and compression of TT cores, the right-to-left orthogonalization requires successive $N-1 \mathrm{QRs}$ for matrices of size $2 r$-by- $2 r I$ and costs $\mathcal{O}\left(N * 2 r I *(2 r)^{3}\right)=\mathcal{O}\left(16 N I r^{4}\right)$ operations, the left-to-right compression requires $N-1$ SVDs for matrices of size $2 r I$-by- $2 r$ and costs $\mathcal{O}\left(16 N I r^{4}\right)$ operations. Hence, the total time of orthogonalization and
compression of TT cores is $T_{\text {ttorth }}=\mathcal{O}\left(32 N I r^{4}\right)$. Therefore, according to Eq. 15, the total time consumption is:

$$
T_{I T T D}=\left\{\begin{array}{l}
O\left(I^{N-1} I^{\prime 2}+N I r^{4}\right), r \leq I^{\frac{N-2}{4}}  \tag{16}\\
O\left(I^{\frac{N}{2}+2} r^{2}+N I r^{4}\right), r>I^{\frac{N-2}{4}}
\end{array}\right.
$$

Besides, the time complexity of nonincremental TT decomposition (NTTD) is:

$$
T_{N T T D}=\left\{\begin{array}{c}
O\left(\left(I+I^{\prime}\right)^{N+1}\right), r^{\prime} \leq\left(I+I^{\prime}\right)^{\frac{N-2}{4}}  \tag{17}\\
O\left(\left(I+I^{\prime}\right)^{\frac{N}{2}+2} r^{\prime 2}\right), r^{\prime}>\left(I+I^{\prime}\right)^{\frac{N-2}{4}}
\end{array}\right.
$$

To compare other incremental HOSVD (IHOSVD) in [25], its time complexity is illustrated as follows:

$$
\begin{equation*}
T_{I H O S V D}=O\left(N\left(I^{N-1} r^{2}+I^{N} I^{\prime}\right)\right) \tag{18}
\end{equation*}
$$

From the time complexity analysis in Eq. 16, we can find that $T_{I T T D}$ is mainly consumed by the TT decomposition of incremental tensor and the orthogonalization and compression of TT cores. If $N$ and $I$ are larger or $r$ is smaller, then the orthogonalization and compression procedure will account for less proportion of execution time. By comparing Eqs. 16 and 17, we can find that $r \leq r^{\prime}$. Therefore, after analyzing Eqs. 16, 17 and 18, it can be inferred that ITTD takes greater advantage in execution time compared with NTTD and IHOSVD when the order and dimensionality of tensor are larger and the compression rank is smaller, which is conducive to the cyber-physical-social big data.

### 4.5.2 Storage Cost Analysis

The storage cost refers as to the amount of storage space of the TT cores decomposed by TNs. Given an $N$ th-order tensor $\underline{X} \in \mathbb{R}^{I_{1} \times I_{2} \times \cdots \times I_{N}}$, suppose that $I=\max \left\{I_{n}\right\}(n=$ $1, \cdots, N)$, and $r=\max \left\{r_{n}\right\}(n=0,1, \cdots, N)$. According to the tensor network diagram in Fig. 2, it can be seen that the storage cost of a TT format is $\mathcal{O}\left(N I r^{2}\right)$. But the storage cost for the original tensor is $\mathcal{O}\left(I^{N}\right)$. It can be seen that the number of parameters of TT decomposition is asymptotically equal to that of CP decomposition [11]. Notably, if the tensor has low-rank structure or truncations are applied, the TT-ranks shall keep small, and the storage and computational complexities should be substantially reduced, which will play a significant role in storing and handling the cyber-physical-social big data.

Besides, QTT is adopted in some scenarios, especially for tensors with very large dimensionality, i.e., very large-scale vectors or matrices. Given an $N$ th-order tensor $\underline{X} \in \mathbb{R}^{I_{1} \times I_{2} \times \cdots \times I_{N}}$ with $I_{n}=q^{J_{n}}$, if all orders are simultaneously quantized, then a quantized tensor $\underline{Y} \in \mathbb{R}^{q \times q \times \cdots \times q}$ can be yielded, its order number is $J_{1}+J_{2}+\cdots+J_{N}$ and dimensionality is $q$. Suppose that the TT decomposition is executed on the quantized tensor $\underline{Y}$, then the storage cost of QTT format is $\mathcal{O}\left(N \log _{q}(I) q r^{2}\right)$, where $I=\max \left\{I_{n}\right\}(n=$ $1, \cdots, N)$, and $r=\max \left\{r_{n}\right\}\left(n=0,1, \cdots, J_{1}+J_{2}+\right.$ $\left.\cdots+J_{N}\right)$. In practical computation, $r$ is typically small, $q$ commonly is equal to 2,3 or 4 , thus the storage cost of QTT can be further reduced to a logarithmic (sub-linear) storage requirement $\mathcal{O}\left(\mathrm{Nlog}_{q}(I)\right)$ from the original exponential storage requirement $\mathcal{O}\left(I^{N}\right)$, which is referred as to so-called super-compression [17]. The super-compression is quiet important for reducing the storage and computation complexity for huge cyber-physical-social big data.

## 5 EXPERIMENTS

To verify the performance of the proposed ITTD approach, a series of experiments are conducted on cloud platform in accordance with various categories of datasets. Furthermore, we compare the ITTD with NTTD from multiple perspectives, including execution time, approximation error, and storage space.

### 5.1 Experimental Design

The experiments are implemented through Java Toolkit AKKA and Python Package Numpy. All experiments are executed on a cloud platform which configures an Intel's 16 -core Xeon E5-2630 processor with 2.4 GHZ and a 125 G memory. The experiments are conducted from two scenarios, one is implemented on the random data and the other is on the real-world CPSS data. The random data are generated according to different parameters, e.g., order, scale, and density. Therein, the scale refers to the tensor size, the density is the ratio of the number of nonzero elements to the number of total elements of the tensor, and the prescribed accuracy is the setting value during TT decomposition. The real-world CPSS data are derived from the public traffic system in Guangzhou city of China.

To compare the performance of ITTD, NTTD is selected as the baseline. In reality, the CPSS data decomposed by TT decomposition have been already stored in clouds or networks in TT format. Therefore, when new streaming data have generated, the NTTD should execute the following steps to obtain the TT format of the updated tensor. Firstly, the TT format of the original tensor should be reconstructed. Secondly, we should add the incremental tensor to the original tensor and generate the updated tensor. Finally, we decompose the updated tensor according to TT decomposition and obtain the ultimate TT format.

### 5.2 Simulations and Evaluations

To verify the performance of the proposed ITTD approach and more fully reflect the influence of various kinds of data, we conduct a series of experiments by generating some random data according to different scales, densities, and prescribed accuracies. In these experiments, the tensor orders are set to 7 th-order, 8 th-order, and 9 th-order. Their original tensor sizes are $10 * 10 * 10 * 10 * 10 * 10 * 2$ (denoted as $10^{6} * 2$ ), $10 * 10 * 10 * 10 * 10 * 10 * 10 * 2$ (denoted as $10^{7} * 2$ ), and $10 * 10 * 10 * 10 * 10 * 10 * 10 * 10 * 2$ (denoted as $10^{8} * 2$ ), respectively. And every incremental tensor sizes are $10^{6} * 1,10^{7} * 1$, and $10^{8} * 1$, which shall be appended to the original tensor along the last order, until the updated tensors increase to $10^{6} * 10,10^{7} * 10$, and $10^{8} * 10$ after 8 increments. Three densities are set to $1,1 e-4$, and $1 e-5$, and three prescribed accuracies are set to $0,1 e-2$, and $1 e-1$. Therefore, 9 random datasets are generated according to different orders (i.e., order $=7,8,9$ ) and densities (i.e., $d=1,1 e-4,1 e-5)$. Based on these datasets, we conduct 27 groups of experiments according to different prescribed accuracies (i.e., eps $=0,1 e-2,1 e-1$ ). Afterwards, the comparisons between ITTD and NTTD under diverse situations are illustrated from the execution time, approximation error, and storage space perspectives.

### 5.2.1 Comparison of Execution Time

The comparisons of execution time between ITTD and NTTD under different cases are depicted in Figs. 9~12, where the X -axis and Y -axis represent the tensor size and execution time, respectively. Fig. 9 illustrates the influence of different data scales to the execution time. It can be seen from Figs. 9(a)(b)(c) that ITTD is inferior to NTTD in execution time when the original tensor size is $10^{6} * 2$. However, when the original tensor size increases to $10^{7} * 2$ or $10^{8} * 2$, the performance of ITTD in execution time outperforms that of NTTD. And the noticeable trend is that the larger the scale is, the greater superiority the ITTD achieves. The experimental results are exactly consistent with the analysis in Eq. 16. Note that only partial experimental results are displayed in Fig. 9 because of the space limitations, and the remainder also follows the above conclusion.


Fig. 9. The comparisons of execution time between ITTD and NTTD under different scales.

Fig. 10 demonstrates the influence of different prescribed accuracies to the execution time of ITTD and NTTD. In the 18 groups of experimental results, the general trend is that the larger the prescribed accuracy is, the less execution time the ITTD and NTTD cost. That is because that more noisy information are removed when the prescribed accuracy is larger. It can be seen from Figs. 9 and 10 that the execution time consumed by NTTD continuously grows up at a rapid rate with the increase of tensor size for all cases. However, if the prescribed accuracy is zero (eps=0), i.e., there are no truncations in SVDs, the execution time spent by ITTD will also rise with the increase of tensor size, but the rise rate is much slower than NTTD. And if the prescribed accuracy is given (eps $=1 e-2$ or $1 e-1$ ), i.e., the truncations are applied in SVDs, then the execution time of ITTD will remain nearly stable with the increase of tensor size. That is because that only the incremental tensor is decomposed and the orthogonalization and compression of low-order updated TT cores are executed in ITTD, and these operations shall take the same amount of time when the incremental tensors are the same size. The improved performance in execution time is very conductive to handling the cyber-physical-social big data.

Fig. 11 demonstrates the influence of different densities to the execution time of ITTD and NTTD. It shows a faint trend that the smaller the density is, the less time the ITTD


Fig. 10. The comparisons of execution time between ITTD and NTTD under different prescribed accuracies.
and NTTD spend, but the influence of different densities is not distinct. Actually, we can infer from Eq. 16 that the execution time of ITTD is related to tensor order $N$, dimensionality $I$, and TT rank $r$. Therefore, if the tensor size is given, the execution time will be determined by the TT rank. In general, the smaller the tensor's density, the smaller the TT rank of random tensor if the prescribed accuracy is given. But note that the TT rank of a tensor is determined by the tensor's own property when there is no truncation in SVDs (i.e., eps $=0$ ), rather than the tensor's density.


Fig. 11. The comparisons of execution time between ITTD and NTTD under different densities.

Fig. 12 depicts the influence of different orthogonalization and compression intervals to the execution time of ITTD. According to the explanation in section 4.4, the orthogonalization and compression procedure can be selectively executed in practical applications. In these experiments, four types of intervals (i.e., $I N V L=1,2,4,8$ ) are set, which means that the orthogonalization and compression procedure will be executed after every one, two, four, and eight incremental updates, respectively. Figs. 11(a)(b) and Figs. 11(c)(d) illustrate the independent and accumulated execution time of ITTD after every incremental update. It is obvious that ITTD will cost less execution time when the orthogonalization and compression procedure need not be executed. However, if the interval becomes larger, the execution time consumed by the last orthogonalization and compression will be very large, which may lead to larger accumulated execution time. It can be seen from Figs. 11(c)(d) that ITTD under the condition of $I N V L=1$ costs the largest
accumulated execution time, followed by $I N V L=2,4,8$ (or $I N V L=2,8,4$ ). Therefore, we can select an appropriate interval to execute the orthogonalization and compression procedure according to different practical situations.


Fig. 12. The comparisons of execution time between ITTD and NTTD under different orthogonalization and compression intervals.

### 5.2.2 Comparison of Approximation Error

Fig. 13 illustrates the comparisons of the practical approximation error between ITTD and NTTD according to different prescribed accuracies. The approximation error is defined as $A E=\frac{\|\underline{X}-\underline{\widehat{x}}\|_{F}}{\|\underline{X}\|_{F}}$ [25], where $\underline{X}$ is the original tensor and $\underline{X}$ is the reconstructed tensor from the TT format. We select 12 groups of experiments based on two scales ( order $=8,9$ ), two prescribed accuracies $(e=1 e-2,1 e-1)$, and three densities $(d=1,1 e-4,1 e-5)$. It shows that the practical approximation errors of ITTD and NTTD are nearly equal and ITTD is slightly larger than NTTD. That is because ITTD executes two approximative operations in the TT decomposition for the incremental tensor and the compression for the ultimate TT cores, and NTTD executes the approximative operation only in the TT decomposition for the updated tensor. However, all approximation errors are less than the prescribed accuracies. When the prescribed accuracy is $1 e-2$, their practical approximation errors in Figs. 13(b)(c)(f) and Figs. 13(a)(d)(e) is $1 e-14$ and $1 e-3$. When the prescribed accuracy is 0.1 , their practical approximation errors are less than 0.1 and the average difference between ITTD and NTTD is only 0.005 .


Fig. 13. The comparisons of approximation error between ITTD and NTTD under different situations.

### 5.2.3 Comparison of Storage Space

The comparisons of storage space between ITTD and NTTD under different situations are depicted in Fig. 14. It illustrates that ITTD and NTTD can save large amount of storage space compared with the original tensor, except for the first incremental update. The phenomenon are consistent with the storage analysis in section 4.5.2, namely, the advantage of TT decomposition in storage will become more prominent with the increment of the tensor order $(N)$ and dimensionality $(I)$. And if the prescribed accuracy is specified, we can find that the smaller the density is, the larger storage space the ITTD and NTTD save. That is because that the TT ranks become smaller when the data are sparser if the prescribed accuracy is specified. Besides, Fig. 14 shows that the differences of storage space between ITTD and NTTD are very subtle and nearly equal. Note that ITTD and NTTD take up less storage space with the increase of tensor size. That is because that ITTD once again compresses the original tensor during the compression for the updated TT cores, NTTD also compresses the reconstructed tensor again during the TT decomposition for the updated tensor.


Fig. 14. The comparisons of storage space between ITTD and NTTD under different situations.

From the abundant experimental results, it is obvious that ITTD is significantly superior to NTTD in execution time on the premise of guaranteeing the nearly identical approximation error and storage space. The advantage is beneficial to improve the analysis efficiency for cyber-physical-social big data.

### 5.3 Case Study on CPSS Data

To further verify the performance of the proposed framework and ITTD approach on CPSS data, another group of experiments are conducted. The real-world CPSS data derive from the public traffic system in Guangzhou city of China. The dataset consists of 8 million bus card records generated by 2 million users and 4 bus lines from Aug. 1st, 2014 to Dec. 31st, 2014 ${ }^{1}$. Firstly, we removed some incomplete bus card records. Then we integrate the remaining records (including bus lines, card types, traffics, time, etc.) and other data (including weather, temperature, etc.) to the fusion data. Afterwards, the characteristics of these data is analyzed, such as the value range, mean, variance,

[^1]etc. Finally, the original CPSS tensor is constructed after preprocessing these CPSS data.

TABLE 1
The description of each order of bus traffic tensor

| Order | Dimensionality | Description |
| :--- | :---: | :--- |
| Line | 4 | bus No. 6,10,11,15 |
| Cardtype | 3 | student card, old card, <br> ordinary card |
| Traffic | 60 | traffic is in $0 \sim 1200$, <br> interval is 20 |
| Weather | 6 | sunny, cloudy, light rain, <br> moderate rain, heavy rain, snow |
| Temp | 10 | temperature is in $-10^{\circ} \mathrm{C} \sim$ <br> $40^{\circ} \mathrm{C}$, interval is $5^{\circ} \mathrm{C}$ |
| Hour | 24 | $1,2, \cdots, 24$ |
| Month | 12 | Jan., Feb., $\cdots$, Dec. |
| Day | 7 | Mon., Tues., $\cdots$, Sun. |

Therefore, we construct an 8th-order tensor $\underline{X} \in$ $\mathbb{R}^{4 \times 3 \times 60 \times 6 \times 10 \times 24 \times 12 \times 7}$ according to the fusion data. The corresponding information of each order of tensor $\underline{X}$ is explained in Table 1, and the entry of tensor $\underline{X}$ represents the swiped count. In the experiment, the streaming data tensor will be appended to the original tensor along the day order. To compare their performance of ITTD and NTTD, the data during the first week in August are selected as the test data. The initial tensor size is $4 \times 3 \times 60 \times 6 \times 10 \times 24 \times 12 \times 1$, and every incremental tensor size is $4 \times 3 \times 60 \times 6 \times 10 \times 24 \times 12 \times 1$, it will be gradually appended to the original tensor until the ultimate tensor size reaches $4 \times 3 \times 60 \times 6 \times 10 \times 24 \times 12 \times 7$ after 6 increments. Afterwards, we conduct a series of experiments according to four different prescribed accuracies (i.e., $e p s=0,1 e-5,1 e-2,1 e-1)$ and compare the execution time, approximation error, and storage space of ITTD and NTTD. The comparisons are illustrated in Fig. 15, where the X -axis represents the day order of tensor and Y -axis represents the execution time, approximation error, and storage space, respectively.

It can be seen from Figs. 15(a)~(d) that ITTD outperforms NTTD in the execution time and the superiority becomes more prominent with the increase of tensor size. If the truncation is applied (e.g., Figs. 15(b)~(d)), the execution time of ITTD maintains a linear growth and even remains nearly constant with the increase of tensor size, however, NTTD keeps an exponential growth. Figs. 15(e)~(h) show the comparisons of approximation errors between ITTD and NTTD, we can see that the average approximation errors are much less than the prescribed accuracies and their differences are nearly negligible. For example, their average difference of approximation errors is $1 e-14$ when the prescribed accuracy is $1 e-5$. Figs. 15(i) $\sim(1)$ demonstrate the comparisons of storage space between ITTD and NTTD. Note that the Y-axis represents the logarithm of the storage space. We can see that ITTD and NTTD maintain the same order of magnitude. As shown in Fig. 15(i), the storage space of ITTD and NTTD slightly exceed that of the original tensor when the prescribed accuracy is zero (i.e., eps $=0$ ), that is because no truncation is applied during SVDs. However, the storage space of ITTD and NTTD are far less than that of the original tensor when the truncation is applied in SVDs (i.e., $e p s=1 e-5,1 e-2,1 e-1$ in Figs. 15(j) $\sim(1))$. Experimental


Fig. 15. The comparisons between ITTD and NTTD on CPSS data from execution time, approximation error, and storage space perspectives.
results demonstrate that the proposed ITTD approach also demonstrably outperforms the NTTD in reducing the execution time on real-world data.

## 6 Conclusions

In this paper, we present an effective processing framework based on tensor networks to represent, store, analyze, and handle cyber-physical-social big data. Tensor networks have some outstanding advantages in big data analysis, such as powerful data compression, distributed data storage, ubiquitous tensor computation, etc. TT decomposition, as the simplest yet efficient format of tensor networks, is illustrated in detail and applied to handle cyber-physicalsocial big data. Besides, given that the cyber-physical-social big data are generally generated in a streaming way, this paper also proposes an incremental tensor train decomposition approach to avoid the repeated computation for the history data. A wealth of experimental results demonstrate that the proposed ITTD approach outperforms NTTD in execution time on the premise of guaranteeing the nearly equal approximation error and storage space. The proposed processing framework and incremental decomposition approach are conducive to improving the cyber-physical-social big data analysis efficiency under ubiquitous computing environment.

In the future, we shall extend ITTD to meet the multiorder incremental scenarios by improving the implementation approach for achieving the TT format of the zeropadding tensor in multi-order incremental cases. Besides, we shall study the parallel computation of tensor operations in TT format without transforming TT cores to the original tensor by exploiting the mathematical characteristics of TT
format and taking advantage of the distributed TT cores. Finally, we shall further study the TTcore-based data analysis methods and their practical applications.

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