HPC Selection of Models of DNA Substitution for Multicore Clusters

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Abstract

This paper presents the High Performance Computing (HPC) support of jModelTest2, the most popular bioinformatic tool for the statistical selection of models of DNA substitution. As this task can demand vast computational resources, especially in terms of processing power, jModelTest2 implements three parallel algorithms for model selection: (1) a multithreaded implementation for shared memory architectures, (2) a message-passing implementation for distributed memory architectures, such as clusters, and (3) a hybrid shared/distributed memory implementation for clusters of multicore nodes, combining the workload distribution across cluster nodes with a multithreaded model optimization within each node. The main limitation of the shared and distributed versions is the workload imbalance that generally appears when using more than 32 cores, a direct consequence of the heterogeneity in the computational cost of the evaluated models. The hybrid shared/distributed memory version overcomes this issue reducing the workload imbalance through a thread-based decomposition of the most costly model optimization tasks.

The performance evaluation of this HPC application on a 40-core shared memory system and on a 528-core cluster has shown high scalability, with speedups of the multithreaded version of up to 32, and up to 257 for the hybrid shared/distributed memory implementation. This can represent a reduction in the execution time of some analyses from four days down to barely 20 minutes.

The implementation of the three parallel execution strategies of jModelTest2 presented in this paper are available under a GPL license at http://code.google.com/jmodeltest2.

Keywords: High Performance Computing (HPC), Multicore Cluster, Message-Passing in Java (MPJ), Phylogeny, Nucleotide Substitution, Performance Evaluation
1 Introduction

In recent years, DNA sequence data has been accumulated in databases (e.g., GenBank) at an exponential rate. These DNA sequences can be used for example to study the history of the different species that inhabit our planet, for example estimating phylogenetic trees from multiple sequence alignments. All phylogenetic methods make assumptions, whether explicit or implicit, about the process of DNA substitution [9]. It is well known that the use of one or another probabilistic model of nucleotide substitution can change the outcome of the analysis [2][12][3], and model selection has become a routinary step for the estimation of molecular phylogenies.

The most popular bioinformatic tool to select appropriate models of DNA substitution for a given DNA sequence alignment is jModelTest [13]. This program calculates the likelihood score for each model and uses different model selection techniques to choose the “best” one according to the likelihood and number of parameters. The model selection strategies implemented in jModelTest are the Akaike Information Criterion (AIC) [1], Bayesian Information Criterion (BIC) [15] and dynamic Likelihood Ratio Tests (dLRTs) [14].

jModelTest supports 88 submodels of the general time-reversible model (Table 1). Table 1 shows the 88 candidate substitution models supported by jModelTest. In top of different substitution schemes and ACGT frequencies, each of these models can assume that some sites do not change between sequences (i.e., are invariant; “+I” parameter), or they do it at different rates (approximated with a discrete gamma distribution “+G”). The estimation of the α shape parameter of the gamma distribution can be complicated, and models that include this parameter (“+G” models) carry an extra computational burden.

We define the basic execution task as the optimization of a single model. jModelTest makes an extensive use of third party bioinformatics libraries and software, aggregating
Table 1: Substitution models available in jModelTest. Any of these can include a proportion of invariable sites (+I), rate variation among sites (+G), or both (+I + G).

<table>
<thead>
<tr>
<th>Model</th>
<th>Free Parameters</th>
<th>Base Frequencies</th>
<th>Substitution Rates</th>
<th>Substitution Code</th>
</tr>
</thead>
<tbody>
<tr>
<td>JC</td>
<td>k</td>
<td>Equal</td>
<td>AC = AG = AT = CG = CT = GT</td>
<td>0000000</td>
</tr>
<tr>
<td>F81</td>
<td>k + 3</td>
<td>Unequal</td>
<td>AC = AG = AT = CG = CT = GT</td>
<td>0000000</td>
</tr>
<tr>
<td>K80</td>
<td>k + 1</td>
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<td>AC = AT = CG = GT, AG = CT</td>
<td>010010</td>
</tr>
<tr>
<td>HKY</td>
<td>k + 4</td>
<td>Unequal</td>
<td>AC = AT = CG = GT, AG = CT</td>
<td>010010</td>
</tr>
<tr>
<td>TrNef</td>
<td>k + 2</td>
<td>Equal</td>
<td>AC = AT = CG = GT, AG, CT</td>
<td>010020</td>
</tr>
<tr>
<td>TrN</td>
<td>k + 5</td>
<td>Unequal</td>
<td>AC = AT = CG = GT, AG, CT</td>
<td>010020</td>
</tr>
<tr>
<td>TPM1</td>
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<td>Equal</td>
<td>AC = GT, AT = CG, AG = CT</td>
<td>012210</td>
</tr>
<tr>
<td>TPM1uf</td>
<td>k + 5</td>
<td>Unequal</td>
<td>AC = GT, AT = CG, AG = CT</td>
<td>012210</td>
</tr>
<tr>
<td>TPM2</td>
<td>k + 2</td>
<td>Equal</td>
<td>AC = AT, CG = GT, AG = CT</td>
<td>010212</td>
</tr>
<tr>
<td>TPM2uf</td>
<td>k + 5</td>
<td>Unequal</td>
<td>AC = AT, CG = GT, AG = CT</td>
<td>010212</td>
</tr>
<tr>
<td>TPM3</td>
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<td>Equal</td>
<td>AC = CG, AT = GT, AG = CT</td>
<td>012012</td>
</tr>
<tr>
<td>TPM3uf</td>
<td>k + 5</td>
<td>Unequal</td>
<td>AC = CG, AT = GT, AG = CT</td>
<td>012012</td>
</tr>
<tr>
<td>TIM1ef</td>
<td>k + 3</td>
<td>Equal</td>
<td>AC = GT, AT = CG, AG, CT</td>
<td>012230</td>
</tr>
<tr>
<td>TIM1</td>
<td>k + 6</td>
<td>Unequal</td>
<td>AC = GT, AT = CG, AG, CT</td>
<td>012230</td>
</tr>
<tr>
<td>TIM2ef</td>
<td>k + 3</td>
<td>Equal</td>
<td>AC = AT, CG = GT, AG, CT</td>
<td>010232</td>
</tr>
<tr>
<td>TIM2</td>
<td>k + 6</td>
<td>Unequal</td>
<td>AC = AT, CG = GT, AG, CT</td>
<td>010232</td>
</tr>
<tr>
<td>TIM3ef</td>
<td>k + 3</td>
<td>Equal</td>
<td>AC = CG, AT = GT, AG, CT</td>
<td>012032</td>
</tr>
<tr>
<td>TIM3</td>
<td>k + 6</td>
<td>Unequal</td>
<td>AC = CG, AT = GT, AG, CT</td>
<td>012032</td>
</tr>
<tr>
<td>TVMef</td>
<td>k + 4</td>
<td>Equal</td>
<td>AC, AT, CG, GT, AG = CT</td>
<td>012314</td>
</tr>
<tr>
<td>TVM</td>
<td>k + 7</td>
<td>Unequal</td>
<td>AC, AT, CG, GT, AG = CT</td>
<td>012314</td>
</tr>
<tr>
<td>SYM</td>
<td>k + 5</td>
<td>Equal</td>
<td>AC, AG, AT, CG, CT, GT</td>
<td>012345</td>
</tr>
<tr>
<td>GTR</td>
<td>k + 8</td>
<td>Unequal</td>
<td>AC, AG, AT, CG, CT, GT</td>
<td>012345</td>
</tr>
</tbody>
</table>
multiple tasks in a pipeline and providing a high-level view of the analysis. Figure 1 shows the workflow of jModelTest, where the most time-consuming part of the process is the calculation of the likelihood scores (carried out by the PhyML program [11]). Because this calculation represents more than 99% of the execution time in most cases, our parallel adaptation is focused in this part of the model selection process. The parallel strategies here exposed are implemented in a new version of jModelTest, available at http://code.google.com/jmodeltest2. A preliminary version of the parallelization of jModelTest, including only the shared and distributed memory versions, has been presented in [6]. This paper extends the previous work by implementing a hybrid shared/distributed memory version which overcomes the limitations of the previous work, namely the poor scalability and the workload imbalance, achieving 8 times higher performance (from speedups around 30 to speedups around 230).
2 Parallel Algorithm For Model Selection

Most of the execution time of the model selection analysis is spent optimizing each substitution model from the candidate model set, maximizing the likelihood function (the likelihood is the probability of the data, a multiple sequence alignment, given the model), which depends on the size and complexity of the data. For large-scale alignments this task cannot be completed in a reasonable time with just a single core. Thus, we implemented a High Performance Computing (HPC) version of jModelTest that supports its parallel execution on shared memory systems such as current multicore desktop processors and HPC clusters, distributing the workload among nodes and also taking advantage of multicore processors within nodes.

Maximum Likelihood model optimization was proved NP-complete [4]. Thus, it is really difficult to estimate the runtime of each single task. However, we can estimate the relative workload depending on the model parameters. Figure 2 shows the high variance between task runtimes regarding invariant sites (+I) and discrete rate categories (+G) parameters. This variance slightly depends on the input data characteristics (e.g., number of taxa, sequences length or divergence between sequences). A representative real dataset ($91 \times 33.148$). In order to homogeneously distribute the workload, it is better to run the most complex (i.e, +I+G and +G models) tasks at first (reverse complexity estimate). The lightest tasks would take up the remaining computational resources as long as the candidate models are optimized.

This paper presents three parallel algorithms for model selection using asynchronous communication and dynamic load-balancing: (1) a threaded shared-memory approach using Java built-in thread pool, (2) a distributed memory approach using a Message-Passing in Java (MPJ), and (3) a hybrid shared-distributed memory approach using message-passing for inter-node synchronization, a custom thread pool for intra-node synchronization, and OpenMP [5] for parallelizing the basic task. The first two ap-
Figure 2: Computational load and execution times of the 88 model optimizations. There are 22 models of each rate variation parameter. The pie graph represents the proportion of the execution times of the models including invariant sites (+I), rate variation among sites (+G) or both (+I+G).

2.1 Design Overview

The original implementation was partially redesigned to grant model extensibility, traceability and encapsulation, taking advantage of the code included in the ProtTest3 API [7], a similar program for protein sequences already adapted for HPC environments.

Figure 3 shows the high-level design of the HPC version of jModelTest. There is not coupling between the front-end and the back-end layers, delegating communications through a façade design pattern. Some features were organized into a class hierarchy, decoupling the related classes from the controller (i.e, ModelTestService), therefore making the model easier to extend through several interfaces:

1. The execution modes use the RunPhyml hierarchy. A common interface hides the
model optimization behavior, and internally is able to run several PhyML instances in a shared memory architecture using a thread pool (RunPhymlThreaded), synchronize several processes in a distributed memory architecture (RunPhymlMPJ) or synchronize multiple thread pools in different nodes (RunPhymlHybrid).

2. The model selection task can be performed applying different information criteria that in general terms behaves in the same way. For this reason a common specification (InformationCriterion) hides each single criterion. As before, this decouples these classes from the controller, and also brings extensibility to the architecture.

3. The view classes do not directly depend on the inner model. The use cases are implemented in the main application service. Using an observer design pattern the execution information is displayed in real time. This works this way not only for the Graphic User Interface (GUI), but also for the command console executions, both threaded and distributed. In the distributed approach, only the root model is in charge of the I/O operations, unless they mean read/write data from/into scratch.

Data: Execution parameters, input data, resource information
Result: Best-fit model
begin Application initialization // tasks initialization
    build(model set);
    Estimate workload per model;
    begin Initialize parallel environment
        Initialize thread pool;
        Synchronize thread pool;
    end
end
begin Tasks Computation // model optimization
    build(task queue);
    foreach model optimization task do
        Wait for an idle thread;
        Assign the next task to the thread;
        Optimize model;
    end
end

Algorithm 1: jModelTest algorithm for shared memory parallel model optimization.
2.2 Shared Memory Implementation

The shared memory implementation of jModelTest relies on a thread pool to handle the execution of tasks on shared memory architectures. This approach is fully portable as it relies on thread pools from the Java Concurrence API, present in Java 1.5 and higher. Figure 4 presents the shared memory parallel operation. The task queue contains the whole set of tasks which will be processed by the thread pool in a particular order (reverse complexity estimate) (Fig. 5.a).

This multithreaded shared memory approach is especially suitable for the parallel execution of jModelTest2 on multicore desktop computers, benefiting also from the availability of a GUI. However, it is limited by the number of available cores in the system, and especially from the memory consumption, directly proportional to the number of
Figure 4: Activity diagram for the shared memory parallel operation algorithm

2.3 Distributed Memory Implementation

In order to handle the computation of tasks on distributed memory architectures (e.g., clusters), this implementation manages processes, which rely on message-passing for communication, and uses a dedicated distributor thread to allocate the workload according to a dynamic distribution strategy (Fig. 5.b).
Figure 5: Parallel execution strategies in (a) shared memory, (b) distributed memory and (c) hybrid shared/distributed memory.

The process synchronization is explicitly achieved through message-passing blocking communication. The models are sequentially distributed and gathered among the processes through non-blocking communications. Only the root process is in charge of I/O, centralizing the displaying of runtime information and results. This central management has a negligible impact on the whole performance as long as there is no output during the model optimization task, which is by far the most time-consuming part of the model selection process. Every process works with its own copy of the input alignment from...
scratch, avoiding this way read or write conflicts. Figure 6 shows the operation in a distributed memory environment.

This approach saves the previous bottleneck with memory requirements. However,
\[ \text{Speedup}_{\text{max}} = \frac{\text{Runtime}}{\text{Runtime}_{\text{max}}} \]

Figure 7: Maximum reachable speedup using task level parallelization.

because each single task is executed sequentially, the workload imbalance in the model optimization tasks (see Figure 2) represents a new bottleneck. In fact, in most cases half of the candidate models requires more than 80% of the total execution time. The more computational resources are used, the more probably is that the total execution time depends on the optimization of a single model, the one which takes longer to optimize. This way, looking at Figure 2 we see the maximum runtime of a single model optimization was 248 minutes. The total runtime of the dataset was 135 hours. Thus, dividing the total runtime by the maximum single task runtime we can predict a performance limit below a speedup of 32.83, no matter how many processes are used. Although this is a particular example, empirical tests show that the workload imbalance usually leads to a maximum speedup below 40 for these task level parallel strategies.

2.4 Parallel Model Optimization Implementation

A detailed knowledge of the performance of the PhyML parallel implementation is key for an optimal assignment of resources (processor cores) for each model optimization task. As the +G and +I+G models used to take four times longer to optimize than the rest of the models, a proportion of 4-to-1 in relative speedups would balance the workload for each task. However, since the parallel efficiency of the PhyML parallel implementation is not optimal a tradeoff between the expected speedup and the available computational resources has to be considered.

PhyML uses the Maximum Likelihood algorithm [8] (ML) for finding the model parameters that maximize the likelihood function. The likelihood evaluation algorithm
is the most time consuming part of the ML process, because it is executed for each new model state proposal (i.e., after changing the parameters configuration, the tree topology or the branch lengths). This likelihood evaluation algorithm is highly parallelizable, since it is a site-independent operation performed all along the column patterns in the alignment. We used OpenMP directives for parallelizing this loop.

Figure 8 shows the performance of our parallel PhyML version, where the scalability is notably higher for models with rate variation among sites (+G). In these models, the likelihood evaluation for each site is repeated for each different rate category (typically 4 categories are used).

The parallel section here represents around 75% for single category models and 98% of the total execution time for models with rate variation among sites. Amdahl’s law states an expected speedup of 2.91 and 7.02 respectively using 8 threads, and 3.36 and 12.31 respectively using 16 threads. However, there is a high parallel overhead as long as the consumed execution time is caused by a large number of sequential repetitions of the call to this function and not so by the computational load of this loop in itself.

Looking at these results, it is important to select the best ratio of resources allocated to +G and +I+G models regarding non-gamma models. For example, a 4-to-1 ratio is expected to balance the workload of the tasks for 4 and 8-core nodes. Additionally, using a number of threads that is a divisor of the number of available cores per node will maximize the number of +G and +I+G models that can be optimized in parallel. For example, an efficient allocation rule for a cluster of 12-core nodes would be the use of 4 or 6 threads for each gamma model (+G and +I+G) and a single thread for the rest.
2.5 Hybrid Shared/Distributed Memory Implementation

The performance limitations of the previous implementations can be solved using the previous parallel implementation of the basic task for avoiding workload imbalance, and relying on a distributed memory approach to cope with memory limitations, thus implementing a three-level hybrid shared/distributed memory implementation (Fig. 5.c). Figure 9 shows its operation in a hybrid shared/distributed memory environment, such as a cluster of multicore nodes. A single jModelTest process is executed for each node, containing a custom thread pool implementation that manages the tasks that are executed within the node, and the number of cores allocated for each task (i.e., the number of OpenMP threads used for the model optimization task). This distribution, known as “thread scheduling”, allows for assigning different amounts of computational resources depending on the estimated workload of each task. Therefore the parallel efficiency is maximized in the simplest models by using a reduced number of cores (1 or 2), while
Figure 9: Activity diagram for the hybrid shared/distributed memory parallel operation algorithm
the heaviest tasks are split, thus balancing the global workload.

Parallel synchronization between nodes is performed using MPJ, as in the previous distributed memory version. Each MPJ process uses a custom implementation of the thread pool, where both tasks and cores per task are managed. Thus, the model optimization tasks can be heterogeneously distributed among the total number of cores within the node. The workload is decomposed relying on our custom parallel PhyML version implemented with OpenMP (sources of this parallel patch are available from the authors).

3 Performance Evaluation

The performance of the three parallel algorithms for model selection of jModelTest2 has been evaluated on two representative HPC systems, a 40-core shared memory system and a cluster of multicore nodes (44 nodes with 12 cores per node, hence 528 cores). The distributed memory and the hybrid shared/distributed memory algorithm have been evaluated in this latter system.

3.1 Data Set Configuration

The data sets used in the performance evaluation consist of four simulated multiple sequence alignments, covering a wide range of number of sequences, from 12 to 91, and a wide range of sites for each sequence, from 3,000 to 33,148 (see Table 2). The largest alignment, ALIGN1 (91 sequences of 33,148 sites), has the largest sequential runtime (5.65 days) whereas the sequential execution time of the smallest one, ALIGN4 (12 sequences of 5,831 sites), is around 5 hours.

The calculation of the model likelihood scores requires an initial phylogenetic tree (“base” tree), generated using likelihood estimation (ML) [8]. This algorithm provides much more accurate results in the model selection process in exchange of significantly
Table 2: Data sets used in the performance evaluation.

<table>
<thead>
<tr>
<th>Name</th>
<th># Sequences</th>
<th>Length</th>
<th>Base Tree</th>
<th>Sequential Runtime</th>
</tr>
</thead>
<tbody>
<tr>
<td>ALIGN1</td>
<td>91</td>
<td>33,148</td>
<td>ML</td>
<td>135:42:01</td>
</tr>
<tr>
<td>ALIGN2</td>
<td>40</td>
<td>4,203</td>
<td>&quot;</td>
<td>15:23:56</td>
</tr>
<tr>
<td>ALIGN3</td>
<td>40</td>
<td>3,200</td>
<td>&quot;</td>
<td>14:33:48</td>
</tr>
<tr>
<td>ALIGN4</td>
<td>12</td>
<td>5,831</td>
<td>&quot;</td>
<td>4:51:13</td>
</tr>
</tbody>
</table>

higher runtimes than other algorithms such as BIONJ [10]. ML estimation is NP-complete, while BIONJ has a computational complexity of $O(n^3)$, where $n$ is the number of sequences.

The distribution of the tasks is limited by the maximum number of substitution models to be computed, 88 in this case, so it is not possible to take advantage from the use of more than 88 processes. Moreover, the variability of the runtimes across models has a significant impact on performance as workload imbalance reduces the speedup and the parallel efficiency obtained. Thus, Figure 2 presents the overhead of the model likelihood calculation of ALIGN1 for each model type. In this case, when a small number of models per processor is distributed the differences in execution times can delay significantly the completion of the parallel execution.

Furthermore, even the execution times of the optimization of the models with the same parameters (e.g, “+I” and “+I+G” models) present significant variance. This characteristic, together with the fact that their execution time can not be estimated a priori, contribute to the presence of a performance bottleneck as the number of cores increases (i.e, the less models per processor, the less probability the work is balanced). In order to reduce this overhead a heuristic, which consists of starting the optimization with the most complex models, has been proposed. This approach has reported more balanced executions as the main source of imbalance, starting the computation of a
complex model at the end of the optimization process, is avoided.

However, the scalability using the shared and distributed memory implementations is limited by the replacement models with the largest execution time, which can account for more than 80% of the overall runtime. In these cases, the runtime is determined by the longest optimization, even if the execution is prioritized efficiently using the proposed heuristic for selecting the models to be optimized first.

In fact, the execution of the entire set of 88 candidate models is expected to achieve a maximum speedup of around 40, as the largest optimization is usually twice the average model time. Moreover, the higher the number of cores, the higher the workload imbalance due to runtime differences. In fact, JModelTest2 usually can take advantage of only up to 55 cores, approximately.

3.2 Testbed Configuration

The shared memory testbed is a system with 4 Westmere-EX (Westmere-based EXPandable/multiprocessor) Intel Xeon E7-4850@2.0GHz 10-core processors (hence 40 cores) and 512 GB memory. The OS is Linux Ubuntu 11.10 64 bits, the C compiler is gcc 4.6 and the JVM is OpenJDK 1.6.0_23 64-Bit Server VM.

The second testbed is a cluster of multicore nodes, used for the evaluation of the distributed and hybrid shared/distributed memory implementation. This cluster is also a Westmere-based system, Westmere-EP (Efficient Performance), which consists of 44 nodes, each of them with 2 Intel Xeon X5675@3.06GHz hexa-core processors (hence 12 cores per node, 528 cores in the cluster) and 24 GB of RAM (1104 GB of RAM in the cluster). The interconnection networks are InfiniBand (DDR 4X: 16 Gbps of maximum theoretical bandwidth), with OFED driver 1.5.3, and Gigabit Ethernet (1 Gbps). The OS is Linux CentOS 5.3, the C compiler is gcc 4.6, the JVM is Oracle 1.6.0_23, and the Java message-passing library is FastMPJ 1.0b.
The performance metrics considered in this performance evaluation are the execution time and its associated speedup, defined as \( Speedup(n) = \frac{T_{seq}}{T_n} \), where \( T_{seq} \) is the runtime of the sequential execution of jModelTest2, and \( T_n \) the time measured when using \( n \) cores. Another metric considered is the parallel efficiency, defined as \( Efficiency(n) = \frac{Speedup(n)}{n} \), which is 100\% in case of a linear speedup (speedup of \( n \) when using \( n \) cores) and is close to 0\% in case of highly inefficient parallel executions.

### 3.3 Evaluation of the Shared Memory Algorithm

Figure 10 and Table 3 present, respectively, the speedups and execution times obtained using the shared memory implementation in the 40-core Westmere-EX shared memory system. In this scenario the speedup is close to the ideal case (i.e., obtaining speedups around \( n \) with \( n \) cores), especially using up to 24 threads. The use of a higher number of threads (32 and 40) results in workload imbalance due to the reduced number of models optimized per thread, what makes more difficult to balance the workload even with this dynamic distribution (generally 3 or less models per thread).

Table 3: Execution times (HH:mm:ss) in a shared memory system (40-core Westmere-EX testbed)

<table>
<thead>
<tr>
<th>Threads</th>
<th>ALIGN1</th>
<th>ALIGN2</th>
<th>ALIGN3</th>
<th>ALIGN4</th>
</tr>
</thead>
<tbody>
<tr>
<td>2</td>
<td>66:11:01</td>
<td>07:39:52</td>
<td>07:45:53</td>
<td>02:37:45</td>
</tr>
<tr>
<td>4</td>
<td>34:14:08</td>
<td>03:56:18</td>
<td>03:43:59</td>
<td>01:17:28</td>
</tr>
<tr>
<td>8</td>
<td>17:39:16</td>
<td>02:16:03</td>
<td>01:52:30</td>
<td>00:38:01</td>
</tr>
<tr>
<td>12</td>
<td>12:10:04</td>
<td>01:20:03</td>
<td>01:17:56</td>
<td>00:27:40</td>
</tr>
<tr>
<td>16</td>
<td>09:31:29</td>
<td>01:02:39</td>
<td>01:02:13</td>
<td>00:24:02</td>
</tr>
<tr>
<td>24</td>
<td>06:19:31</td>
<td>00:42:45</td>
<td>00:47:34</td>
<td>00:11:57</td>
</tr>
<tr>
<td>32</td>
<td>05:36:53</td>
<td>00:38:21</td>
<td>00:41:11</td>
<td>00:10:33</td>
</tr>
<tr>
<td>40</td>
<td>04:49:08</td>
<td>00:37:08</td>
<td>00:38:27</td>
<td>00:09:02</td>
</tr>
</tbody>
</table>

The processors of this testbed have Simultaneous MultiThreading (SMT) or hyper-
threading, whose activation allows to run 80 threads simultaneously on 40 physical cores (two threads per physical core). However, the use of more than 40 threads in this evaluation has not reported any benefit as the workload imbalance limits the scalability. In fact, the speedups obtained from running 64 and 80 threads (not shown for clarity purposes) are slightly lower than running 40 threads due to the higher overhead of executing twice the number of model optimization tasks, which burdens memory access performance and OS thread scheduling. This result would seem questionable as Intel has reported that hyperthreading can provide up to 30% higher performance and indeed we reported such a performance benefit in our previous work on an 8-core system [6]. Nevertheless, there is no contradiction as hyperthreading increases jModelTest2 shared memory scalability when an increment in the number of threads improves the workload balance, such as running on an 8-core system, whereas it does not yield any benefit when
using 40 or more cores.

### 3.4 Evaluation of the Distributed Memory Algorithm

The distributed memory implementation of jModelTest2 has been evaluated on the Westmere-EP cluster, showing the measured execution times and the associated speedups in Figure 11 and Table 4, respectively. In this testbed the sequential execution time is around 15–25% faster than on the Westmere-EX system, an expected result according to their respective computational power, measured in terms of the SPEC CPU floating point CFP2006 benchmark (the optimization of models is intensive in floating point operations). Thus, a single core from the Westmere-EX system obtains a result of 49.6 in the CFP2006 whereas a single core from the Westmere-EP system achieves a result of 60.3, a 22% higher.

![Figure 11: Scalability of the distributed memory version (Westmere-EP testbed)](image.png)
Table 4: Execution times (HH:mm:ss) of the distributed memory implementation (Westmere-EP testbed)

<table>
<thead>
<tr>
<th>Threads</th>
<th>ALIGN1</th>
<th>ALIGN2</th>
<th>ALIGN3</th>
<th>ALIGN4</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>100:11:08</td>
<td>12:53:56</td>
<td>12:36:04</td>
<td>04:01:50</td>
</tr>
<tr>
<td>12</td>
<td>09:25:32</td>
<td>01:05:50</td>
<td>01:03:29</td>
<td>00:21:34</td>
</tr>
<tr>
<td>16</td>
<td>08:51:50</td>
<td>00:49:56</td>
<td>00:49:47</td>
<td>00:16:58</td>
</tr>
<tr>
<td>22</td>
<td>06:11:03</td>
<td>00:45:38</td>
<td>00:46:15</td>
<td>00:15:39</td>
</tr>
<tr>
<td>32</td>
<td>04:03:22</td>
<td>00:27:44</td>
<td>00:32:50</td>
<td>00:09:38</td>
</tr>
<tr>
<td>44</td>
<td>04:11:06</td>
<td>00:32:05</td>
<td>00:34:47</td>
<td>00:09:03</td>
</tr>
<tr>
<td>64</td>
<td>04:19:04</td>
<td>00:25:41</td>
<td>00:24:31</td>
<td>00:08:47</td>
</tr>
<tr>
<td>88</td>
<td>03:30:02</td>
<td>00:26:36</td>
<td>00:25:31</td>
<td>00:07:49</td>
</tr>
</tbody>
</table>

This implementation distributes the workload among the available message-passing processes, using up to 88 processes, the maximum number of models to be optimized and running each process in a single core. In this testbed the particular allocation of processes among the cluster nodes has a negligible impact on performance (< 0.1% runtime overhead) due to the computationally intensive nature of the application with respect to the communications required (ML optimization accounts for nearly all the execution time). In this performance evaluation the processes have been distributed among the cluster nodes using a fill-up allocation rule, minimizing the number of nodes by using the 12 cores available per node (e.g., the execution using 88 processes has distributed 12 processes per node across 7 nodes, and 4 processes in the eighth node).

The workload imbalance presented in the evaluated data sets (22 out of 88 tasks optimizing the most complex models accounts for approximately 50% of the total runtime and 44 out of 88 tasks require more than 80% of the total runtime, as shown in Figure 2) imposes an upper bound in the scalability, limiting the measured speedups to around 30, as for the shared memory implementation. Thus, distributing a small number of models per process severely limits the load balancing benefits, as it is not possible to take advantage of the spare computational power available once a process finishes its task.
processing and the task queue is empty. In fact, little performance benefits are obtained when using more than 32 processes, the speedups using 32 processes are around 25 (78% parallel efficiency), whereas the speedups using 88 processes are around 30 (34%), the use of 56 additional processes (a 175% resources increase, from 32 up to 88 cores) hardly improves speedups (20% higher, from 25 to 30). When using more than 32 cores most of the processes only compute a single model and finish working earlier than the longest running model, which is the one that determines the overall runtime (and hence the speedup) in these scenarios.

3.5 Evaluation of the Hybrid Shared/Distributed Algorithm

The limitations of the shared and distributed memory implementations of jModelTest2 have motivated the development of a more scalable implementation based on the decomposition of the model optimization among multiple threads (see Section 2.4). This decomposition depends on the allocated resources, the number of available cores per system node and the computational cost of each model optimization. Thus, the longest running model computations, such as the gamma models (+G and +I+G), are split among multiple threads whereas the lightest ones are executed by one or two threads. The final objective is to achieve the workload balance among all the involved processes. Thus, this new implementation is able to take advantage of hybrid shared/distributed memory architectures, such as clusters of multicore nodes, without compromising significantly its efficiency.

This new implementation of jModelTest2 has been evaluated on the Westmere-EP cluster, where each node has 12 physical cores and can run up to 24 simultaneous threads thanks to hyperthreading. The measured execution times and the associated speedups are shown in Figure 12 and Table 5, respectively. The experimental results have been obtained using 12 threads per node for executions from 192 up to 480 threads, thus 16,
24, 33 and 40 nodes have been used for executions on 192, 288, 396 and 480 threads, respectively. The performance results using 40 threads (4 nodes) and 88 threads (8 nodes) are also shown for comparative purposes against the results of the shared and distributed memory versions, evaluated using up to 40 and 88 cores, respectively. Here the resources allocated for each type of model (i.e., uniform, +I, +G or +I+G) are selected depending on the number of total resources. For example, an execution with 4 nodes and 12 threads per node would use 4 threads for each gamma model (+G and +I+G) and a single thread for the rest.

Figure 12: Scalability of the hybrid shared/distributed version (Westmere-EP testbed)

This benchmarking has also taken into account the evaluation of the impact of hyperthreading in the performance of this new implementation. Thus, the executions with 576, 696 and 792 threads have used 32 nodes \times 18 \text{ threads per node}, 29 nodes \times 24 \text{ threads per node} and 44 nodes \times 18 \text{ threads per node}, respectively. The main conclusion
Table 5: Execution times (HH:mm:ss) of the hybrid shared/distributed memory implementation (Westmere-EP testbed)

<table>
<thead>
<tr>
<th>Threads</th>
<th>ALIGN1</th>
<th>ALIGN2</th>
<th>ALIGN3</th>
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</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>100:11:08</td>
<td>12:53:56</td>
<td>12:36:04</td>
<td>04:01:50</td>
</tr>
<tr>
<td>40</td>
<td>02:58:04</td>
<td>03:13:29</td>
<td>03:09:01</td>
<td>00:06:52</td>
</tr>
<tr>
<td>88</td>
<td>01:25:55</td>
<td>00:12:50</td>
<td>00:11:39</td>
<td>00:03:26</td>
</tr>
<tr>
<td>192</td>
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<td>00:02:01</td>
</tr>
<tr>
<td>288</td>
<td>00:37:07</td>
<td>00:04:59</td>
<td>00:04:20</td>
<td>00:01:27</td>
</tr>
<tr>
<td>396</td>
<td>00:33:14</td>
<td>00:04:37</td>
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<tr>
<td>480</td>
<td>00:28:36</td>
<td>00:03:42</td>
<td>00:04:15</td>
<td>00:01:37</td>
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<td>576</td>
<td>00:25:59</td>
<td>00:03:41</td>
<td>00:03:33</td>
<td>00:01:13</td>
</tr>
<tr>
<td>696</td>
<td>00:26:13</td>
<td>00:03:19</td>
<td>00:03:11</td>
<td>00:01:08</td>
</tr>
<tr>
<td>792</td>
<td>00:23:22</td>
<td>00:03:24</td>
<td>00:03:44</td>
<td>00:01:05</td>
</tr>
</tbody>
</table>

derived from the analysis of these performance results is that jModelTest2 takes advantage of hyperthreading, both running the maximum number of simultaneous threads per node (24) or sharing half of the physical cores (18 threads running on 12 physical cores).

The analysis of the results shows that this implementation achieves significantly higher scalability, speedups around 230, in the range 203-257, which is the result of multiplying the scalability of the distributed memory processing (speedups around 30) by the scalability obtained by the parallel execution of the optimization of the models (speedups up to 8). In fact, this multilevel parallel implementation increases 8 times jModelTest2 performance, that is to say, its performance benefits are equivalent to the scalability obtained from the parallelization of the model optimization.

4 Conclusions

A popular tool for the statistical selection of models of DNA substitution is jModelTest, a sequential Java application that requires vast computational resources, especially CPU hours, which has motivated the development of its parallel implementation (jModelTest2,
distributed under a GPL license at http://code.google.com/jmodeltest2). This pa-
paper presents its three parallel execution strategies: (1) a shared memory multithread
GUI-based desktop version; (2) a distributed memory cluster-based version with work-
load distribution through message-passing; and (3) a multilevel hybrid shared/distributed
memory version that distributes the computation of the likelihood estimation task across
cluster nodes while taking advantage, through a thread-based parallelization, of the mul-
tiple cores available within each node.

The performance evaluation of these three strategies has shown that the hybrid
shared/distributed memory implementation of jModelTest2 presents significantly higher
performance and scalability than the shared and distributed memory versions, overcom-
ing their limitations that force their execution using only up to 32-40 cores. Thus, the
new implementation can take advantage efficiently of the use of up to several hundreds
of cores. The observed parallel efficiencies are around 38-49%, with speedups in the
range 203-257 on 528 physical cores. This performance has been obtained thanks to the
workload balance provided by the thread-based decomposition of the most costly model
optimization tasks.

The shared memory implementation of jModelTest2 provides scalable performance
although generally up to 40 threads. Nowadays this limitation is becoming more and
more important as the number of available cores per system continues increasing, espe-
cially with the advent of manycore processors which definitely demand further workload
decomposition in jModelTest2.

The distributed memory implementation shows similar performance results as the
shared memory version despite supporting distributed memory architectures with hun-
dreds of cores. In fact, using more than 32 cores results in highly inefficient scalability
gains due to the significant workload imbalance present in jModelTest2. However, this
solution avoid memory limitations with large input data.
TODO: Write something about HYBRID??

Acknowledgments

Special thanks to Stephane Guindon for his help.

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References


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