Photon-Counting Gamma Camera Based on an Electron-Multiplying CCD

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Abstract—We have developed a compact ultra-high-resolution gamma camera (UGC), based on an electron-multiplying chargecoupled device (EMCCD) coupled to a columnar CsI(Tl) scintillator. The EMCCD is suitable for photon-counting gamma-camera imaging, since it has an extremely low readout noise, even at high frame rates (<1 electron/pixel at 50 images/s or 11 Mpixel/s). The high frame rate prevents overlapping of scintillation events and reduces the accumulation of dark current noise. In this paper, we describe the drive- and readout electronics of the EMCCD, the image processing hardware and software, and the first ultra-high-resolution gamma-ray images obtained with the UGC. A digital signal processor (DSP) facilitates real-time frame analysis, comprising photon counting and energy discrimination, and reduces the data stream from 162 kB per frame to 8 B per detected scintillation event (i.e., for a typical application in multipinhole single photon emission computed tomography (SPECT) a data reduction from 28 GB/h by a factor of 20 000 to 1.4 MB/h). Such a reduced data stream is needed for applications that require the use of a large number of gamma cameras simultaneously. The image processing hardware that we describe allows the images from the EMCCD to be processed in real-time, at a rate of 50 images per second. First gamma-camera images with a spatial resolution as good as 60 μ m full-width of half-maximum (FWHM) are shown. The prototype UGC allows for photon counting without the need for an image intensifier and has energy discriminating capabilities.

Index Terms—Charge-coupled device (CCD), digital signal processor (DSP), electron-multiplying charge-coupled device (EMCCD), gamma camera, molecular imaging, photon-counting, readout electronics, single photon emission computed tomography (SPECT).

I. INTRODUCTION

C OMPACT high-resolution gamma cameras are very useful in applications like small animal pinhole single photon emission computed tomography (SPECT) imaging. With small detectors one can place many pinhole cameras around the object of investigation, resulting in a high counting sensitivity [1]–[9]. Other applications of compact gamma cameras include planar gamma camera imaging [10]–[12], radiation monitoring (e.g., [13]), tumor surgery [14], and autoradiography [15].

In recent years different high-resolution detectors have been proposed. For example semiconductor detectors based on

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CdZnTe have been developed by several groups for nuclear medicine and X-ray astronomy applications (see for example [16] and [17]). Examples of two-dimensional (2-D) and three-dimensional readout of the CdZnTe are the MediPix2 Chip [18] and methods like those proposed by He *et al.* [19], respectively.

Another option to obtain high spatial resolution is the use of a detector based on micro-columnar scintillation crystals. The columnar structure prevents resolution loss due to light spreading in the scintillator. As an example, thallium-doped CsI has high-Z components, a high density, and an excellent scintillation light yield, which makes it an excellent detector material for gamma-ray detection. These particular crystals can be grown in tiny columnar structures, yielding a high spatial resolution when read out with a charge-coupled device (CCD) (e.g., [20]). The potential of CCDs to read out crystals for photon-counting applications is growing, since the quantum efficiency and noise properties of CCDs are steadily improving. Methods are foreseen for eliminating depth-of-interaction problems in scintillation pinhole gamma cameras based on columnar crystals [8].

One can distinguish two classes of scintillation gamma (or X-ray) cameras. One is the integrating camera in which each pixel gives a signal that is more or less proportional to the total amount of light received from scintillations observed during a fairly long period. As a result one loses information about the amount of scintillation light that each individual gamma quantum contributes to a (CCD) pixel. Also the center of gravity of the individual scintillation events can no longer be determined, which reduces possibilities to optimize spatial resolution. A second class consists of photon-counting scintillation gamma cameras. Photon-counting scintillation gamma cameras often have the potential to analyze properties of each light flash and to discriminate whether a light flash should be rejected on the basis of the amount and spatial distribution of the scintillation light observed. Photon counting as defined in the present paper involves that positions and intensities of individual flashes are estimated in CCD-frames. The estimated locations of flashes with a certain range of intensities are then used to form a gamma-camera image. The intensity estimate can be used for energy discrimination, which in turn can be used for the rejection of scintillations that are caused by scattered γ -rays or to discriminate noise. In Fig. 1 we present the principle of photon-counting gamma-camera imaging using our CCD-based gamma camera: the ultra gamma camera (UGC). The figure illustrates the case when a line pattern is imaged.

In gamma-camera imaging, photon counting is not only desirable for energy discrimination, but also 1) for suppressing noise

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Fig. 1. Principle of photon counting in the CCD-based gamma camera in the case a line pattern on the scintillator is imaged: if the frame rate is high enough, each frame contains very few light clouds, which decreases the probability of overlap. In addition, the short time in which each frame is recorded reduces dark current noise accumulation, so the individual scintillation flashes can be distinguished better from the background.



Fig. 2. EMCCD chip with (a) store area and active (photosensitive) area and (b) a possible set-up of a compact EMCCD-based gamma camera. The fiber optic taper can be used to increase the effective detector area per CCD and enables close packing of detector surfaces. A Peltier element can be used to cool the CCD chip in order to reduce the dark noise.

in the estimated number of detected gamma-rays and 2) for improving spatial resolution.

- 1) The estimated number of detected gamma quanta varies due to the varying number of light photons detected from each scintillation; the number of light photons per scintillation that reach a pixel varies because the light yield differs for each event and because the light spreads differently at different positions in a scintillation crystal. In addition, the dark current noise and readout noise introduce an extra variation in the detected signal per scintillation event. As a result, the estimate of the number of gamma interactions based on the amount of detected light can become quite inaccurate when integrating imaging systems instead of photon-counting systems are used. Furthermore, if the number of gamma photons in integrating systems is low, the dark noise build-up over the integration period will approach the light signal from a scintillation flash. When the CCD can be read out at a high frame rate in a photon-counting system such as proposed in Fig. 1, the light flashes can be detected before too much dark noise has accumulated.
- 2) Spatial resolution improvements may be obtained from photon counting by estimating the center of gravity of each light flash. This may help to improve the resolution limit of the gamma camera over the resolution as

is set by the light spread in the scintillator and optical coupling.

When CCDs are used for reading out scintillation events they should have both a sufficiently high quantum efficiency and low dark current noise. Several CCDs with these properties are available. However, when these CCDs are operated at sufficiently high frame rates for photon-counting gamma-camera imaging, the readout noise becomes too high. A solution to this problem is to use an electron-multiplying CCD (EMCCD) such as that introduced in 2001 by E2V Technologies, Essex, U.K., and Texas Instruments. EMCCDs have extremely low readout noise, even at high readout speed (readout noise below 1 electron/pixel/readout at 11 MHz whereas the best conventional CCDs have a readout noise in the range of 10 to 100 electron/pixel/readout). Therefore, even at high frame rates, the total noise of an EMCCD can be very low with only moderate cooling of the CCD and without the need for an image intensifier [21]-[25].

In this paper we present a compact high-resolution photoncounting gamma camera based on an EMCCD, the UGC. A set-up of the UGC with scintillator and EMCCD is shown in Fig. 2. The main goal of the present paper is to describe the developed drive- and readout electronics and associated software for photon detection used in the UGC and to show some initial results of ultra-high-resolution gamma-ray imaging. On-board digital signal processing is presented as well.



Fig. 3. Block diagram of the electronics of the UGC.



Fig. 4. Schematic diagram of an EMCCD showing the extra gain register and the multiplication by impact ionization in the gain register. The $R\phi^2$ electrode of the conventional readout register is replaced by a dc electrode and an $R\phi^2HV$ electrode in the gain register.

II. MATERIALS AND METHODS

The electronics of the UGC includes the following:

- 1) an EMCCD;
- 2) clock drivers for driving the EMCCD;
- 3) analog readout electronics;
- 4) an analog–digital (A/D) converter;
- 5) a first-in first-out (FIFO) buffer;
- 6) a digital signal processor (DSP)-board for image processing and photon counting and for sending the detected locations and intensities to a personal computer (PC).

Two field programmable gate array (FPGA) based timing generators generate the digital timing clocks for the CCD and the digital logic components. A block diagram of the system is shown in Fig. 3. The first three parts together form a compact CCD camera, the last three parts form a digital image processing unit. The CCD camera is connected to the A/D converter in the digital image processing unit via a coaxial cable. This allows the digital image processing to be separated from the camera electronics and saves space in the camera. Furthermore it is possible to process the output of multiple CCD cameras with a single digital image processing unit.

A. Electron-Multiplying CCD

An EMCCD has a conventional CCD architecture except for an electron-multiplication structure (the gain register—see Fig. 4) that is inserted between the end of the readout register and the output amplifier. The gain register is similar to the readout register, except that one of the three phases is replaced by two electrodes, the first held at a fixed potential (dc) and the second clocked as normal but with a higher voltage amplitude



Fig. 5. Block diagram of the multiplication clock driver.

(between 40 V and 50 V; see Fig. 4). This combination of a fixed voltage electrode and a clocked electrode, with the relatively large difference between them, results in a high electric field. Due to this high electric field, electrons that are transferred through the gain register can experience avalanche multiplication (or impact ionization), which increases the number of electrons per charge packet and provides gain (see Fig. 4). The multiplication per transfer is quite small; only a factor of around 1.01 to 1.015, but when executed over a large number of transfers, a substantial gain is achieved. For example, with a multiplication factor of 1.015 per transfer over 591 transfers (in the case of the CCD65), a gain of 6630 is achieved.

When conventional CCDs are operated at high frame rates and cooled thermoelectrically to reduce the dark noise, the readout noise from the output amplifier is usually the main source of noise. Since the readout noise is independent of the signal level, the insertion of the electron amplification before the output amplifier (see Fig. 4) may increase the signal in the EMCCD above the readout noise, effectively reducing the readout noise. Therefore both the readout noise and the dark noise in a cooled EMCCD operating at high gain and high frame rate are very low. This causes a third noise source to become significant: the clock induced charge [(CIC), sometimes called spurious charge or transfer induced charge]. The CIC can be minimized by 1) making the parallel transfer frequency as large as possible (up to the maximum specified in the datasheet), 2) making the clock rise and fall times not too short, and 3) using reduced clocking amplitudes. The next section describes how clock drivers can be implemented to minimize CIC.

EMCCDs are available from E2V Technologies as L3Vision CCDs and from Texas Instruments as Impactron CCDs. The EMCCD used in the present work is a nonantibloomed (100% fill-factor) CCD65 from E2V Technologies. The CCD65 is a front-illuminated device with 288 lines and 576 pixels per line. The active area is 11.52 mm × 8.64 mm (20 μ m × 30 μ m pixel size) and the device operates in inverted-mode [(IMO), multiphase pinned (MPP)] in order to keep dark current low. For our application, the CCD has been equipped with a 3 mm fiber optic window, which is coupled in very close proximity of the CCD with optical cement. On top of this window, scintillators can be easily and efficiently coupled.

B. CCD Clock Drivers

Most of the drive electronics of the EMCCD are similar to the drive electronics of a conventional CCD. Digital electronics are used to generate the necessary clock sequences and level shifting buffers convert the digital clock sequences to the levels required by the CCD. However a significant difference between driving a conventional CCD and driving an EMCCD is that a higher voltage multiplication clock is required for the gain.

The high voltage multiplication clock of the L3Vision CCD requires voltages of up to 45 V in order to obtain a gain of around $1000 \times$ in the multiplication register. Conventional pulse buffers, with fast edges, would lead to significant power dissipation, both on and off the CCD, and might also cause dangerous clock overshoots that could damage the CCD. The total clock power can be calculated using $P = CV^2 f$, at around 2 W. For fast clock edges of a few nanoseconds, the on-CCD power can approach 1 W, which is very undesirable. Better efficiency is achieved by using a resonant circuit with sine wave shape, resulting in an on-CCD power below 100 mW. Fig. 5 shows a block diagram of the multiplication clock driver as used inhere. A field effect transistor (FET) drives the primary winding of a transformer, the timing being determined by a logic level input. The power supply is derived from a summing integrator, one input setting the output level, and the other providing feedback from an amplitude detector connected to the HV output. Capacitors were adjusted to obtain resonance dependent upon printed circuit board (PCB) layout, with the CCD in circuit. The timing of the input pulse was then adjusted to give the correct relationship between the clocks, as defined in the CCD65 datasheet.

For buffering and level shifting the image and store clocks, we used the EL7457 chip from Elantec. In order to keep the CIC noise as low as possible, a series resistance was added at each output of the EL7457 chip to optimize the rise and fall times of the image and store clocks. Rise and fall times of approximately 200 ns were used, as recommended by E2V Technologies [26]. Pulse amplitudes were kept to the minimum required to transfer charge without smear, and were initially set at the typical values of -5 and +7 V. For the readout and gain register clock buffers (R ϕ 1, R ϕ 2, and R ϕ 3) and the charge detection node reset clock (see Figs. 3, and 4), we similarly used another driver chip from Elantec—the EL7156.

C. Analog Readout Electronics

The format of the output signal of an EMCCD is the same as the format of a conventional CCD. Therefore a standard CCDprocessor (Sony CXA1310AQ) was used to perform correlated double sampling (CDS) and to add line-, field- and frame-synchronization patterns to the video signal. The standard analog video signal (CCIR) obtained this way can be directly viewed on a TV for debugging purposes or digitized and processed in the image processing unit.

D. DSP for Image Processing

For real-time photon counting, the analog video signal is digitized with a high-speed A/D converter and analyzed using a DSP. We used the fastest DSP Starter Kit (DSK) from Texas Instruments that is available—the DSK6416. It is based on the TMS320C6416 fixed point DSP. An input board containing the A/D converter, a FIFO-buffer and an FPGA-based timing generator is connected to the DSK via the DSK's expansion port connectors. The timing generator provides the sample clocks for the A/D converter, the timing for the FIFO buffer, and an interrupt signal for the DSP to start the direct memory access (DMA)-transfer of a block of pixel values from the FIFO-buffers into the SDRAM memory on the DSP board. The timing generator therefore is synchronized to the timing generator in the camera (frame sync; see block diagram in Fig. 3).

The photon-counting algorithm (see Section II-F) runs on the DSP and reads the images from the external SDRAM memory, transferring them to the internal DSP memory in blocks of 576 pixels (one line of a CCD-image). This is done with DMA-transfers, one block being processed while the next block is transferred to the internal memory. The results of the photon-counting algorithm (position and intensity of the detected scin-tillation light flashes) are sent to a PC via a USB connection on the DSK.

E. Implementation

For multipinhole SPECT imaging it is desirable that the gamma detectors are compact, such that one can put many detectors around an object to increase the sensitivity of the system [3], [8]. Therefore we developed a compact CCD headboard ($4 \text{ cm} \times 4 \text{ cm}$). The headboard only contains the CCD and some decoupling capacitors. Via a 50 pin high-density connector, it is connected to a second board. This second board contains the drive electronics and the readout electronics for converting the CCD output into an analog video signal. A coaxial cable is used to connect the analog video signal to an A/D converter on the digital image processing board. A picture of the headboard with the CCD and the board with drive- and readout electronics is shown in Fig. 6.

In order to achieve high sensitivity and low noise, the EMCCD is thermoelectrically cooled down to -30 °C by using a water-cooled Peltier element. This almost totally eliminates the dark noise; this is of particular importance for the imaging of low energy isotopes (e.g., I-125, 27–35 keV) or in the presence of light loss due to strong fiber optic tapering.



Fig. 6. Prototype of the newly developed PCBs for the compact EMCCDbased gamma camera.

F. Photon-Counting Algorithm

An efficient photon-counting algorithm is implemented in the DSP software in order to detect and localize scintillation events in real-time (50 images per second). One can set upper and lower limits for the intensity to distinguish scintillation events from background noise and disturbances like the direct interaction of gamma photons in the CCD.

The algorithm works as follows: each measurement starts with a background measurement in which the average (A) of the background signal is calculated for each pixel from 3000 images (1 minute measurement with no radioactivity). After the background measurement is finished, the gamma camera imaging can be started. Each CCD image (I_{cccd}) is first background corrected by subtracting the average background image A

$$I_{\text{bg-corr.}} = I_{\text{ccd}} - A. \tag{1}$$

The background-corrected image $(I_{bg\cdot corr.})$ is then smoothed with a 2-D Gaussian kernel (G; FWHM of 0.15 mm) to suppress high frequency noise in the image

$$I_{\text{smoothed}} = I_{\text{bg-corr.}} * *G \tag{2}$$

where ** denotes a 2-D convolution. To increase speed, the convolution is implemented as two orthogonal one-dimensional Gaussian convolutions, which is mathematically identical to a single 2-D convolution because of the separability of the Gaussian convolution operation. In this filtered image (I_{smoothed}) , scintillation events will appear as bright regions. If the value of a local maximum in I_{smoothed} lies between the lower limit ll and the upper limit ul, it is assumed to be a relevant scintillation event and its position and intensity are sent to the PC. In Section III some example images illustrating the algorithm are shown and discussed.

III. EXPERIMENTAL RESULTS

Fig. 7 shows some images of the photon-counting algorithm at work. An area on the CCD with a size of 2 mm \times 2 mm is shown. Fig. 7(a) shows a raw CCD image I_{ccd} , and Fig. 7(b) shows the average background image A. Fig. 7(c) shows the CCD image minus the average background image $I_{bg.corr.}$.



Fig. 7. Images illustrating the algorithm at work, showing 2 mm \times 2 mm CCD-area. (a) Raw CCD image: I_{ccd} . (b) Average background image: A. (c) Frame (a) minus Frame (b): $I_{bg,corr.}$. (d) Frame (c) after smoothing: $I_{smoothed}$. (e) Local maxima in $I_{smoothed}$ of Frame (d). (f) Position of detected scintillation event.

Fig. 7(d) shows the same image after smoothing I_{smoothed} , which provides blobs with different intensities that can be used for localizing the events. In Fig. 7(e) the local maxima provided by the blobs in I_{smoothed} as well as their differing intensities represented by different grey values are shown. Fig. 7(f) shows the remaining event locations after intensity discrimination: only one local maximum has a value between the lower limit and the upper limit, and therefore only one scintillation event is detected in this image.

To evaluate the spectral capabilities of the UGC, the camera was flood irradiated with a gamma-source and a histogram of the detected flash intensities was plotted from the listmode output (i.e., the detected energy spectrum). This experiment was carried out both with Co-57 (122 keV) and with I-129 (30 keV). The results are shown in Fig. 8. A well-defined single peak is seen both for Co-57 and for I-129. Energy resolutions of 40 keV and 42 keV FWHM are estimated from the Co-57 spectrum and the I-129 spectrum, respectively.

To determine spatial resolution, line pattern images with the camera were acquired using the set-up as shown in Fig. 9(a). Two lead blocks separated by a narrow slit (50 μ m wide) were used to project a line pattern from a Co-57 radioactive source (122 keV; 4 MBq; 3 mm diameter) onto a 1 mm thick microcolumnar scintillator (CsI(Tl) FOS from Hamamatsu). The use of a slit formed by two lead blocks is a standard method to measure the intrinsic resolution of clinical gamma cameras. Since the CCD has a nonsquare pixel size (20 μ m \times 30 μ m), the resolution has been measured for two orthogonal directions: the slit approximately parallel to the CCD-columns and the slit approximately parallel to the CCD-lines. The energy-discrimination settings (lower-limit ll and upper-limit ul) for the photoncounting algorithm in the DSP were set very wide, with only a lower-limitll to exclude most of the noise. Further energy discrimination was done in the PC after the measurements were finished, by selecting only those events from the listmode data that were in the valid energy range.



Fig. 8. Detected energy spectrum for Co-57 (solid line) and for I-129 (dotted line). A measurement without activity is also shown (dashed line) to illustrate the difference between the scintillation signal and the background and CCD noise.



Fig. 9. Resolution measurement: (a) measurement setup, (b) resulting image, and (c) image intensity profile.

In Fig. 9(b) and (c), the resulting gamma-camera image and a profile over 1 mm perpendicular to the line pattern are shown for the case that the slit is parallel to the CCD-columns. An-FWHM line width of 77 μ m was measured from the profile. Corrected for the beam width of 50 μ m, this gives a spatial resolution in the direction parallel to the CCD-lines of approximately 60 μ m FWHM. The resolution measurement in the perpendicular direction (slit parallel to the CCD-lines) results in an FWHM line-width of 108 μ m. After correcting for the beam width, this gives a FWHM resolution of 96 μ m in the columndirection. The difference in x and y resolution may be caused by the different pixel lengths in the x and y direction.

IV. CONCLUSION AND DISCUSSION

We developed an EMCCD-based compact gamma camera with ultra-high spatial resolution and the ability to perform energy discrimination. The camera operates at 50 images per second and with the presented DSP hardware and software it is able to do real-time photon counting. Using dedicated electronics and algorithms, we were able to reach an intrinsic spatial resolution as good as 60 μ m FWHM. The resolution is degraded in the direction where the pixel length is longer, which indicates that a better resolution may be achieved when smaller CCD pixels are used. The frame rate of the camera is high enough to limit dark current noise build-up and it enables a maximum count rate that is more than sufficient for small animal SPECT. For example, in the U-SPECT-I system [9] during a typical small animal data acquisition, less than one gamma photon per second per cm^2 is incident on the detector during normal operation, which means that in that case most of the CCD-images will not be illuminated by scintillation events and therefore the probability of overlapping scintillation events is negligible.

For the present system a fast algorithm for scintillation detection was implemented in the DSP. This algorithm does not take into account all statistical properties of the dark current and the scintillation signal. In the future the performance of the camera may be improved by the use of statistical detection approaches. This will require an efficient implementation to preserve real-time processing.

First experiments demonstrate that the described electronics and event processing are able to do efficient real-time photon counting including energy discrimination. In future work we intend to investigate how the developed gamma camera can be optimized for the imaging of other isotopes and gamma energies, and to investigate effects of different scintillators and fiber optic tapering or other means of optical coupling.

The relatively thin columnar crystal layer (1 mm) used in the present work is adequate to secure the required detection yield for lower photon energies and prevents light spreading. Higher energy photons though, such as those emitted by radionuclides like Tc-99 m (140 keV), have a low probability of interaction with such thin crystals. Longer columnar crystals are under development at several companies. However, in the case of pinhole collimation, a "parallax" error would remain, since for offset source positions the radiation penetrates into the columns at an angle, and therefore the column in which the interaction will take place cannot be determined a priori. Consequently, different columns will scintillate when gammas are emitted from a single point. This will result in an undesirable line pattern instead of a well-defined point-like emission of light at the interface between the crystal and the CCD. We have proposed to avoid this problem by directing the fibers toward the pinhole ("cone beam crystals"). Using such crystals, the scintillation light is transmitted along the same direction as the direction traveled by the gamma quantum. As a result, the depth-of-interaction in the scintillator will not influence the place where the light hits the CCD [8].

The resulting detector may help to bring about significant improvements in radio-molecular imaging.

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