A Formal Approach to Verification of Linear Analog Circuits with Parameter Tolerances

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Abstract

This contribution presents an approach to formal verification of linear analog circuits with parameter tolerances. The method proves that an actual circuit fulfills a specification in a given frequency interval for all parameter variations. It is based on a curve-driven bound computation for value sets using interval arithmetic. Some examples demonstrate the feasibility of our approach.

I. Introduction

Formal verification, in particular equivalence checking, is an attractive alternative to simulation and very popular in the digital world today. Its main concept - to prove that two circuits have the same in/output behavior - can also be transferred to analog circuits. In this paper we will present an approach to formal verification of linear analog circuits. Basically, two circuit descriptions on different levels of abstraction with parameter tolerances are compared. The approach verifies the following hypothesis:

For all frequencies in a given frequency interval and for all parameter variations within the given parameter tolerances the first or actual (act.) circuit fulfills the specification described by the second or specifying (spec.) circuit having also parameters given in tolerances.

Many other approaches have been proposed to compute performance characteristics from an actual circuit and to compare it with given specifications, like yield estimation [1], worst-case analysis [2, 3, 4] or design centering [5]. Most of these approaches are based on probabilistic methods giving good and reliable results for the mentioned tasks. However, they are not able to prove, that a circuit with tolerance parameter fulfills a certain specification for all parameter combinations, because the tolerance parameters are modeled with a probabilistic density function, or the computed regions are non pessimistic approximations like convex polyhedrons. In contrast to the above, the proposed approach deals with intervals, enabling an exact proof of the correctness of the design.

The main idea of our procedure is to compare the value sets of the complex transfer functions $H_{\text{spec}}$ and $H_{\text{act}}$. Related work in computing value sets can be found in [7, 8, 9]. All these methods restrict the transfer functions to special classes, for example linear or multilinear dependent transfer functions [8]. While our approach deals with a much larger class of transfer functions. Additionally, most of them deal only with outer bound computations of the value sets.

II. Circuit Description

Linear analog circuits can be described by transfer functions. Symbolic analysis methods [6] are able to calculate transfer functions from netlists resulting in a parameterized description of circuit behavior

$$H(s, p) = [p_1, p_2, ..., p_n] ,$$

where $p$ is the parameter vector of $n$ parameters. Here, each parameter tolerance is given as an interval defining the parameter interval vector

$$p = [p_i] = [p_{i\min}, p_{i\max}] , i = 1..n .$$

Interval variables are printed in italics and interval vectors are printed in bold italics. The parameters are assumed to be independent and belong to a finite real interval. $p$ is an $n$-dimensional hypercube in the parameter space.

We compute a safe approximation of the value sets

$$A = \{ H(s) = j \omega_0 . p ) | p \in p \}$$

for each given transfer
function. A safe approximation for the actual transfer function at a fixed frequency is an outer bound of the value set

\[ \tilde{A}_{\text{act, safe}} \supseteq A_{\text{act}} = \left\{ H_{\text{act}}(s = j\omega_0 \cdot p_{\text{act}}) | p_{\text{act}} \in P_{\text{act}} \right\}. \tag{3} \]

A safe approximation at a fixed frequency \( s = j\omega_0 \) for the specifying transfer function is an inner bound of the value set

\[ \tilde{A}_{\text{spec, safe}} \subseteq A_{\text{spec}} = \left\{ H_{\text{spec}}(s = j\omega_0 \cdot p_{\text{spec}}) | p_{\text{spec}} \in P_{\text{spec}} \right\}. \tag{4} \]

In Figure 1 an example for the outer and inner bound \( \tilde{A}_{\text{act, safe}} \) and \( \tilde{A}_{\text{spec, safe}} \) is shown.

![Figure 1: Value sets \( A_{\text{spec}}, \tilde{A}_{\text{act}} \) and bounds \( \tilde{A}_{\text{act, safe}}, \tilde{A}_{\text{spec, safe}} \) for \( H_{\text{spec}}, H_{\text{act}} \) at a fixed frequency.](image)

If \( \tilde{A}_{\text{act, safe}} \subset \tilde{A}_{\text{spec, safe}} \) holds, \( H_{\text{act}} \) fulfills \( H_{\text{spec}} \) at a specific frequency \( \omega_0 \). The extension to a proof for a frequency interval is described in Section V.

III. Outer Bound

An outer bound of a transfer function value set can be calculated by an interval extension using complex interval arithmetic [10, 11]:

\[ \tilde{A}(p) \supseteq A(p) = \left\{ H(s = j\omega_0 \cdot p) | p \in P \right\}. \tag{5} \]

Dividing the interval vector \( p \) into a union of subinterval vectors \( p_k \) according to \( p = \bigcup_{k=1}^{m} p_k \) the unified outer bound \( \tilde{A}(p) = \bigcup_{k=1}^{m} \tilde{A}_k(p_k) \) for \( m \to \infty \) converges to the tight or exact bound of the value set \( A(p) \) (see [12]). Based on these results we compute an as tight as possible outer bound for the actual transfer function \( H_{\text{act}} \) by subdividing the intervals recursively. For each subdivision the algorithm chooses a parameter that leads to the smallest overestimation. This technique automatically divides only those parameter intervals that contribute to the overestimation. If a parameter does not have any impact on the overestimation, it will not be divided leading to much less subdivided intervals and very small overestimation.

After subdivision, the resulting complex intervals are geometrically combined into a single circumscribing polygon (see Figure 2 for an example). This polygon describes an outer boundary \( \tilde{A}(p) \) for the bound \( A(p) \) of the exact value set \( A \). In most cases the proposed algorithm deals with boundary description by polygons. However, for the ease of understanding, the regions themselves are used instead of their boundaries in the remainder of the paper.

![Figure 2: Computation of an outer bound of the function: \( H = \frac{a}{(1+b^2) \cdot 0.1+j \cdot 0.1 \cdot a + 1} \), \( a = [4..5.6], b = [-0.4..0.6] \).](image)

For transfer functions with many parameters the problem of overestimation is still an substantial property of the algorithm. However, if e.g. the transfer function is given in linear dependent form (with the parameters as independent coefficients of the nominator and denominator polynoms, [7, 13]), a more efficient algorithm for calculating the outer bounds can be selected, for example the algorithm explained in Section IV.

IV. Inner Bound

From the viewpoint of interval analysis the calculation of an inner bound is a much bigger problem, because the inclusion property of the interval arithmetic [11] only facilitates the safe computation of outer bounds. However, if we restrict the class of functions to those having a simply connected region for the complex value set, an inscribing polygon can be constructed which is proven to belong to the value set.

A. Safe Path Between Two Points

The goal of this method is to find a region containing the actual parameterized curve between two endpoints.
Figure 3: a) Maximum curvature and intermediate point Z on a curve ρ, b) safe path region approximated by a polygon

In Figure 3 a) an (unknown) curve ρ (plotted in bold) with its endpoints A and B is shown. The curve ρ is given by a complex function F(q) of one parameter $q = [q, \bar{q}]$, while the endpoints can be calculated according to $A = \{\text{Re}(F(q)), \text{Im}(F(q))\}, \quad B = \{\text{Re}(F(\bar{q})), \text{Im}(F(\bar{q}))\}$. The radius of curvature of the parameterized curve ρ is defined by

$$r(q) = \frac{R' + i R''}{R' - i R''}, \quad R' = \frac{\partial \text{Re}(F(q))}{\partial q}, \quad R'' = \frac{\partial^2 \text{Re}(F(q))}{\partial q^2},$$

$$I' = \frac{\partial \text{Im}(F(q))}{\partial q}, \quad I'' = \frac{\partial^2 \text{Im}(F(q))}{\partial q^2}.$$

Using interval arithmetic a lower bound $r$ for the radius of curvature can be computed. If it is smaller than half the distance between the two endpoints A and B ($\frac{1}{2}d_{AB} > r_1$ in Figure 3 a)) the interval $q$ is divided into two intervals by inserting an additional point (Z) at half the interval of q ($Z = \left\{ \text{Re}(F(\frac{q + \bar{q}}{2})), \text{Im}(F(\frac{q + \bar{q}}{2})) \right\}$). The generated intervals $[q, \frac{q + \bar{q}}{2}], [\frac{q + \bar{q}}{2}, \bar{q}]$ are then processed in the same way as the original interval. If the radius of curvature is larger than half the distance between the endpoints (i.e. $r > \frac{1}{2}d_f$), the region of the actual parameterized curve ρ can be bounded by two segments of a circle which is pessimistically approximated by a polygon consisting of 3 lines (see Figure 3 b).

B. Inner Bound of a Value Set Using Curvature Examination

Assuming that the value set A of a complex function H is a simply connected region, every closed path, which lies safely within the value set, encloses a part of the value set. By means of the algorithm in Section IV A, such a path and the resulting inner bound of the value set can be computed as follows:

**Inner Bound (F(p), ω)**

Divide parameter hypercube into a set of 2-dimensional faces
Map the faces in the H(jω)-space
Compute the enveloping polygon E of the faces in the H(jω)-space:

$$E = \left[ e_1, e_2, \ldots, e_k \right]$$

for each segment $(e_i, e_{i+1})$ of the polygon E do
Compute curvature driven path $(e_i, e_{i+1}, F(p), p = [p_0, \ldots, p_i])$

return inscribing polygon of $\hat{A}$

Figure 4: Algorithm for computing an inner bound

Figure 5: a) Faces of the 4-dimensional hypercube in the parameter space, b) faces and enveloping polygon mapped to the H(jω)-space, c) resulting inscribing polygon

In Figure 5 a simple example is presented in order to explain the main steps of the algorithm.

Because the enveloping polygon E is determined in a sequence with counterclockwise steps, the safe path can be computed using only an inner polygon approximation of the circle segment in order to get a pessimistic bound. The result is a polygon which describes the safe inner bound $\hat{A}$ by its boundary.

One reason for computing the enveloping polygon from the image of the parameter cube faces and not from the corners using a simple convex hull approach is, that along the edges of the faces only one parameter is varying and all others are constant. This results in a much lower overestimation for the interval computation of the radius of curvature and, therefore, reduces computation time and complexity.

V. Extension to Proof within Frequency Interval

With the previously defined algorithms for computing inner and outer bounds, a proof that the actual circuit fulfills a specifying circuit is possible at a single frequency $\omega_0$. In order to extend the proof to a given frequency
interval, this frequency interval is introduced as an additional special interval parameter.

An approach handling the whole frequency interval in one step is not feasible, because a 3-dimensional calculation and a proof that the actual tube $V_{act}$ lays inside the specifying tube $V_{spec}$ would necessarily be a computationally tough problem. Hence, the frequency interval is divided into several small intervals and for each interval a projection of the frequency interval onto the $H(\omega)$ plane at one frequency is carried out resulting in a 2-dimensional pessimistic but simple comparison for each small frequency interval.

**Figure 6**: a) 3-dimensional frequency interval, b) 2-dimensional projection

In Figure 6 a) the 3-dimensional value set for a frequency interval: $\omega = [\omega_1, \omega_2]$ is shown. Figure 6 b) displays the corresponding 2-dimensional projection. The dark shaded volume $V_{act}$ is mapped to the dark shaded region $A_{act}$. The face $A_{spec}(\omega)$ is hatched in Figure 6 a) and b). The other areas in Figure 6 are the faces of $H_{spec}$ which extend into the frequency interval. They are called the frequency faces $F_{spec,p_i}(\omega, p_1, p)$ and have only one parameter $p_i$ and $\omega$ in an interval. In addition to the proof that $A_{act}(\omega)$ is inside $A_{spec}(\omega)$, all frequency faces must not touch $A_{act}(\omega)$.

In Figure 7 the formal verification algorithm is presented. Note, that it tries to calculate the outer bound for the faces using curvature examination (algorithm of Section IV B) because this method computes a tighter outer bound much faster than the direct interval method (algorithm of Section III). In order to use this algorithm for determining outer bounds, it has to be proved that the value set is bounded by its edges. This is checked for the faces $F_{spec,p_i}(\omega, p_1, p)$ having two interval parameters $\omega$, $p_i$ by investigating the perpendicular area vector being strictly positive or negative using interval arithmetic:

\[
\eta = \frac{\partial \text{Re}(E_{spec,p_i})}{\partial \omega} \frac{\partial \text{Im}(E_{spec,p_i})}{\partial p_1} - \frac{\partial \text{Re}(E_{spec,p_i})}{\partial p_1} \frac{\partial \text{Im}(E_{spec,p_i})}{\partial \omega}.
\]

**Formal Verification** ($H_{spec}, H_{act}, \omega = [\omega_1, \omega_2], P_{spec}, P_{act}$)

Divide $\omega$ in $k$ parts $[\omega_1, \omega_2, ..., \omega_k]$

**for each** $\omega_i$

Compute $A_{act, safe}^{\omega_i} = \text{Outer Bound} (H_{act}, \omega_i, P_{act})$

Compute $A_{spec, safe, \omega_i} = \text{Inner Bound} (H_{spec}, \omega_i, P_{spec})$

Compute $A_{spec, safe, \omega_i} = \text{Inner Bound} (H_{spec}, \omega_i, P_{spec})$

**for all edges** $p_j$ of $A_{spec, safe, \omega_i}$

if $n \subset \mathbb{R}^+$ or $n \subset \mathbb{R}^-$ then

Compute $F_{spec, safe, p_j} = \text{Outer Bound} (H_{spec}, \omega_i, P_{spec})$ (Section IV)

else

Compute $F_{spec, safe, p_j} = \text{Outer Bound} (H_{spec}, \omega_i, P_{spec})$ (Section III)

if not ($A_{act, safe} \subseteq A_{spec, safe, \omega_i} \wedge \forall p_j: A_{act, safe} \cap F_{spec, safe, p_j} = \emptyset$) then

if frequency interval is too small then

return false

else

Reduce frequency interval $\omega_i$

return true

**Figure 7**: Formal verification algorithm

**VI Experimental Results**

The algorithm is implemented using the symbolic math package Maple V™ [14]. The circuits are first linearized at their operating point and afterwards analyzed with a symbolic analyzer in order to get the transfer function including the parameters in symbolic form. Due to the prototype implementation and the use of Maple V™ the CPU times of the algorithm are still high. A more efficient implementation will reduce them drastically.

Two examples are presented in order to show the feasibility of the approach. The first example deals with a CMOS operational amplifier (actual circuit), which is verified against a behavioral specification at a high level of abstraction. The OP consists of 8 MOS transistors (see Figure 8). The MOS transistors are modelled by a simple nonlinear model including technology parameters. In this example, the W/L ratios of all single or paired transistors on the signal path constitute the tolerance (see Table 1). The behavioral model of the specification (spec. circuit) is given as a transfer function (see Figure 8). The parameters and their tolerances are presented in Table 1.
over 100 frequency intervals. Two intervals at the frequencies 1 .. 2.15 Hz and 100 .. 120 Hz are given in Figure 9. Some areas and bounds are identified as follows. \( A_{\text{act}} \) is the outer bound of the actual circuit, \( A_{\text{spec, \omega}} \) is the inner bound of the specifying circuit at the lower frequency of the frequency interval, \( \tilde{P}_{\text{spec}} \) are the outer frequency faces of certain parameters. In this case the algorithm terminates successfully. A change in circuit parameters will lead to a negative verification result. In this case the designer gets hints from the relative positions of the areas.

A second example deals with a bandpass. A netlist is verified versus a behavioral description of a bandpass. The netlist and the transfer function of the behavioral description are shown in Figure 10.

![Figure 8: a: Schematic of the OP versus b: behavioral description](image1)

**Table 1:** Parameters and their tolerances of the actual circuit and specifying behavioral model

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Nominal Interval</th>
<th>Parameter</th>
<th>Symbol</th>
<th>Specific Interval</th>
</tr>
</thead>
<tbody>
<tr>
<td>W/L of M1, M2</td>
<td>8.8</td>
<td>Gain</td>
<td>( A_0 )</td>
<td>&gt; 93 dB</td>
</tr>
<tr>
<td>W/L of M3, M4</td>
<td>29.3</td>
<td>Gain-bandwidth</td>
<td>gwB</td>
<td>&gt; 1 MHz</td>
</tr>
<tr>
<td>W/L of M5</td>
<td>13.33</td>
<td>Phase margin</td>
<td>phm</td>
<td>&lt; 60°</td>
</tr>
<tr>
<td></td>
<td></td>
<td>Phase tolerance</td>
<td>( \Delta \phi )</td>
<td>&lt; 5°</td>
</tr>
</tbody>
</table>

![Figure 9: Computed bounds for the MOS-OP at two frequency intervals: 1 .. 2.15 Hz and 100 .. 120 Hz](image2)

The verification process starts with a desired frequency interval from 1 Hz to 6 MHz resulting finally in

\[
H(s) = \frac{\frac{A_0}{\omega_g} e^{i \Delta \phi}}{1 + \frac{\sin(\text{phm})}{\text{gwB} \cdot 2\pi} s^2 + \frac{\cos(\text{phm}) - 1}{\text{gwB}^2 \cdot 4\pi^2} s^4 + s^2}
\]

![Figure 10: a: Netlist and b: behavioral description of the bandpass example](image3)

The netlist has 5 resistors which are assumed to vary correlated with one relative parameter \( r_{\text{tol}} \) and to have an additional independent variance for each independent resistor \( (R_{2, \text{tol}}, R_{3, \text{tol}}, R_{4, \text{tol}}) \). The two capacitors are modelled in the same way. The tolerance parameters for the two circuits are:

**Table 2:** Parameters and their tolerances of the bandpass netlist and the specifying behavioral bandpass model

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Interval</th>
<th>Parameter</th>
<th>Symbol</th>
<th>Specification</th>
<th>Interval</th>
</tr>
</thead>
<tbody>
<tr>
<td>( r_{\text{tol}} )</td>
<td>1 ± 1%</td>
<td>Gain</td>
<td>( A_0 )</td>
<td>1 ± 5%</td>
<td>0.95 .. 1.05</td>
</tr>
<tr>
<td>( R_{2, \text{tol}} )</td>
<td>1 ± 0.1%</td>
<td>Center frequency</td>
<td>( \omega_g )</td>
<td>1 kHz ± 5%</td>
<td>950 Hz .. 1.05 kHz</td>
</tr>
<tr>
<td>( R_{3, \text{tol}} )</td>
<td>1 ± 0.1%</td>
<td>Phase tolerance</td>
<td>( \Delta \phi )</td>
<td>&lt; 10°</td>
<td>-10° .. +10°</td>
</tr>
<tr>
<td>( R_{4, \text{tol}} )</td>
<td>1 ± 0.1%</td>
<td>( q_{\text{tol}} )</td>
<td>1 ± 1%</td>
<td></td>
<td></td>
</tr>
<tr>
<td>( C_{1, \text{tol}} )</td>
<td>1 ± 0.1%</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>
The algorithm starts with a frequency interval from 100 Hz to 10000 Hz. In Figure 11 the areas and bounds for 7 selected frequency intervals are shown. The entire frequency interval is split by the algorithm into 210 intervals. Note, that in this example and in the previous one the frequency faces $F_{\text{spec}}$ are computed mostly using the curvature algorithm resulting in a much better approximation of the areas.

![Figure 11: Results from the bandpass example at 7 different frequency intervals](image)

**VII Conclusion**

In this contribution we have presented an approach to formal verification of linear analog circuits with parameters given in tolerances. The main goal is to give an automatic method for proving that an actual circuit fulfills a specification. Additionally, we focus on making as less restrictions to the class of handled circuits as possible. With respect to these goals the disadvantage of having exponential time complexity in terms of the parameters for the actual circuit has to be accepted. For the smaller class of linear dependent circuits (parameters as independent coefficients of the nominator and denominator polynomials) the curvature driven bound algorithm with linear time complexity can be chosen.

The proposed approach enables the user to verify his implementation against the specification without doing multiple simulations. In the failure case the value set in the complex $H(j\omega)$ plane graphically supports the user in finding actual problems in the circuits. Finally, the algorithm can easily be extended to prove the stability of the circuits using the zero inclusion condition [15].

**References**