Message from the Technical Program Chairs

Welcome to the 13th IEEE Conference on High-Performance Switching and Routing (HPSR 2012). This conference brings together researchers from around the world to present the latest advances in the fields of switching and routing. This year, we are proud to present a rich technical program comprising of 39 excellent technical paper presentations, one keynote presentation, two tutorials by world-renowned experts in the field, and an industrial panel discussion involving six experts from leading companies.

We received a total of 87 valid paper submissions, which were carefully peer-reviewed by our Technical Program Committee (TPC). Each paper received at least three reviews, while many papers received no less than four, thus hopefully providing ample and valuable feedback to the authors and ensuring high confidence in the outcome of the review process. In total, the TPC completed 321 reviews, for an average of 3.7 reviews per paper. Each paper was rated on a scale of 1 to 4 with respect to four criteria: originality, technical content, presentation, and impact. The overall rating was determined as the average across these criteria. The cut-off point for acceptance was 2.5, which resulted in 39 papers being accepted for presentation at the conference. We would like to thank the TPC members for their timely, thorough, and constructive input.

The technical program (Monday through Wednesday) comprises of eight regular technical sessions entitled: Switch Architecture, Lookup, Routing, Topologies, Quality of Service, Flow Switching, and Energy Savings, and one special technical session dedicated to the Best Paper Award candidates. In this session, the four papers with highest review ratings are presented. The final decision to determine the best paper is made by the audience by a democratic voting process.

Preceding the regular technical program, two excellent invited tutorials will be presented on Sunday. The first tutorial, entitled “Evolution of Ethernet from Spanning Tree to TRILL, and other Interesting Fabric Issues,” will be presented by Radia Perlman from Intel Research. She has made numerous very influential contributions to the field of networking, including the Spanning Tree Protocol, the IS-IS routing protocol, and most recently TRILL. The second tutorial, entitled “Providing Security in Routing Infrastructures,” will be presented by Charlie Kaufman. He is currently a Security Architect for Windows Azure, Microsoft's Cloud Computing offering and was previously a member of the Windows Core Architecture group. He was a major contributor to IETF standards efforts designing DNSSEC and IPsec.

On Monday morning, Radia Perlman (Intel, USA) will deliver a keynote speech on "Folklore of Protocol Design," sharing with the audience some of her extensive experience in the design of networking protocols and lessons learned during the span of her career.

An exciting opportunity to interact with experts from industry arises during the Wednesday morning Panel Discussion on “The Future of Datacenter Networking,” organized and moderated by Mitchell Gusat (IBM Research - Zurich, Switzerland). The panelists and their respective affiliations are: Achim Autenrieth (ADVA, Germany), Vijoy Pandey (IBM, USA), Radia Perlman (Intel, USA), Ana Radovanović (Google, USA), Srdan Tomašević (Absolut OK, Serbia), and Ed Turner (Gnodal, Great Britain). This promises to be a lively and possibly controversial exchange on the shape of networking in data centers.

We would like to extend our gratitude to General Chair Aleksandra Smiljanić for her leadership and guidance and for her enormous contributions in organizing this conference. Furthermore, we would like to thank General Co-chair Mounir Hamdi, Tutorial Chair Dejan Kostić, Panel Chair Mitchell Gusat, and Publication Chair Ljiljana Trajković for their excellent contributions in bringing together a strong and diversified program.
Finally, and most importantly, we would like to thank the authors and presenters of technical papers, the keynote and tutorial presenters, as well as the panelists for their excellent contributions that make this event a resounding success.

We hope you will enjoy the conference!

HPSR 2012 Technical Program Chairs:
H. Jonathan Chao, Polytechnic Institute of NYU, USA
Eiji Oki, University of Electro-Communications - Tokyo, Japan
Cyriel Minkenberg, IBM Research - Zurich, Switzerland